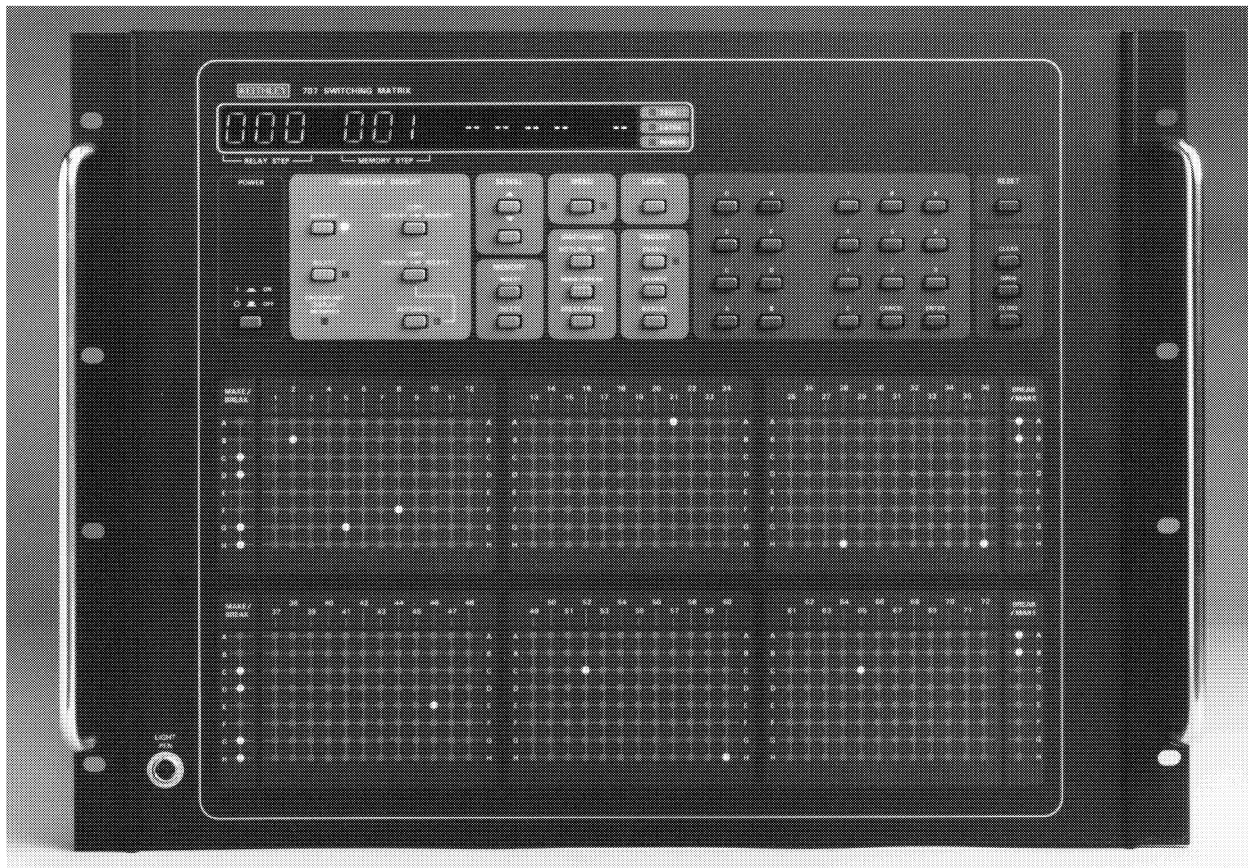


Model 707 Switching Matrix

Operator's Manual



Contains Operating and Servicing Information

KEITHLEY

WARRANTY

Keithley Instruments, Inc. warrants this product to be free from defects in material and workmanship for a period of 1 year from date of shipment.

Keithley Instruments, Inc. warrants the following items for 90 days from the date of shipment: probes, cables, rechargeable batteries, diskettes, and documentation.

During the warranty period, we will, at our option, either repair or replace any product that proves to be defective.

To exercise this warranty, write or call your local Keithley representative, or contact Keithley headquarters in Cleveland, Ohio. You will be given prompt assistance and return instructions. Send the product, transportation prepaid, to the indicated service facility. Repairs will be made and the product returned, transportation prepaid. Repaired or replaced products are warranted for the balance of the original warranty period, or at least 90 days.

LIMITATION OF WARRANTY

This warranty does not apply to defects resulting from product modification without Keithley's express written consent, or misuse of any product or part. This warranty also does not apply to fuses, software, non-rechargeable batteries, damage from battery leakage, or problems arising from normal wear or failure to follow instructions.

THIS WARRANTY IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING ANY IMPLIED WARRANTY OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR USE. THE REMEDIES PROVIDED HEREIN ARE BUYER'S SOLE AND EXCLUSIVE REMEDIES.

NEITHER KEITHLEY INSTRUMENTS, INC. NOR ANY OF ITS EMPLOYEES SHALL BE LIABLE FOR ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL OR CONSEQUENTIAL DAMAGES ARISING OUT OF THE USE OF ITS INSTRUMENTS AND SOFTWARE EVEN IF KEITHLEY INSTRUMENTS, INC., HAS BEEN ADVISED IN ADVANCE OF THE POSSIBILITY OF SUCH DAMAGES. SUCH EXCLUDED DAMAGES SHALL INCLUDE, BUT ARE NOT LIMITED TO: COSTS OF REMOVAL AND INSTALLATION, LOSSES SUSTAINED AS THE RESULT OF INJURY TO ANY PERSON, OR DAMAGE TO PROPERTY.

KEITHLEY

Keithley Instruments, Inc. • 28775 Aurora Road • Cleveland, OH 44139 • 216-248-0400 • Fax: 216-248-6168 • <http://www.keithley.com>

CHINA:	Keithley Instruments China • Yuan Chen Xin Building, Room 705 • 12 Yumin Road, Dewai, Madian • Beijing 100029 • 8610-62022886 • Fax: 8610-62022892
FRANCE:	Keithley Instruments SARL • BP 60 • 3 Allée des Garays • 91122 Palaiseau Cédex • 33-1-60-11-51-55 • Fax: 33-1-60-11-77-26
GERMANY:	Keithley Instruments GmbH • Landsberger Strasse 65 • D-82110 Germering, Munich • 49-89-8493070 • Fax: 49-89-84930759
GREAT BRITAIN:	Keithley Instruments, Ltd. • The Minster • 58 Portman Road • Reading, Berkshire RG30 1EA • 44-118-9575666 • Fax: 44-118-9596469
ITALY:	Keithley Instruments SRL • Viale S. Gimignano 38 • 20146 Milano • 39-2-48303008 • Fax: 39-2-48302274
NETHERLANDS:	Keithley Instruments BV • Avelingen West 49 • 4202 MS Gorinchem • 31-(0)183-635333 • Fax: 31-(0)183-630821
SWITZERLAND:	Keithley Instruments SA • Kriesbachstrasse 4 • 8600 Dübendorf • 41-1-8219444 • Fax: 41-1-8203081
TAIWAN:	Keithley Instruments Taiwan • IFL., 1, Min Yu First Street • Hsinchu, Taiwan, R.O.C. • 886-35-778462 • Fax: 886-35-778455

Model 707 Switching Matrix Instruction Manual

© 1988, Keithley Instruments, Inc.
Test Instrumentation Group
All rights reserved.
Cleveland, Ohio, U.S.A.
August 1992, Fifth Printing
Document Number: 707-901-01 Rev. E

Asyst is a registered trademark of Asyst Software Technologies.

Freon and Teflon are registered trademarks of E.I. du Pont de Nemours & Company, Incorporated.

Hewlett-Packard is a registered trademark of Hewlett-Packard Company.

IBM is a registered trademark of International Business Machines Corporation.

Microsoft is a registered trademark of Microsoft Corporation.

PC<>488 is a trademark of Capital Equipment Corporation.

PC-XT and PC-AT are trademarks of International Business Machines Corporation.

TEKTRONIX and TEK are registered trademarks of Tektronix, Incorporated.

Safety Precautions

The following safety precautions should be observed before using this product and any associated instrumentation. Although some instruments and accessories would normally be used with non-hazardous voltages, there are situations where hazardous conditions may be present.

This product is intended for use by qualified personnel who recognize shock hazards and are familiar with the safety precautions required to avoid possible injury. Read the operating information carefully before using the product.

The types of product users are:

Responsible body is the individual or group responsible for the use and maintenance of equipment, and for ensuring that operators are adequately trained.

Operators use the product for its intended function. They must be trained in electrical safety procedures and proper use of the instrument. They must be protected from electric shock and contact with hazardous live circuits.

Maintenance personnel perform routine procedures on the product to keep it operating, for example, setting the line voltage or replacing consumable materials. Maintenance procedures are described in the manual. The procedures explicitly state if the operator may perform them. Otherwise, they should be performed only by service personnel.

Service personnel are trained to work on live circuits, and perform safe installations and repairs of products. Only properly trained service personnel may perform installation and service procedures.

Exercise extreme caution when a shock hazard is present. Lethal voltage may be present on cable connector jacks or test fixtures. The American National Standards Institute (ANSI) states that a shock hazard exists when voltage levels greater than 30V RMS, 42.4V peak, or 60VDC are present. **A good safety practice is to expect that hazardous voltage is present in any unknown circuit before measuring.**

Users of this product must be protected from electric shock at all times. The responsible body must ensure that users are prevented access and/or insulated from every connection point. In some cases, connections must be exposed to potential human contact. Product users in these circumstances must be trained to protect themselves from the risk of electric shock. If the circuit is capable of operating at or above 1000 volts, **no conductive part of the circuit may be exposed.**

As described in the International Electrotechnical Commission (IEC) Standard IEC 664, digital multimeter measuring circuits (e.g., Keithley Models 175A, 199, 2000, 2001, 2002, and 2010) measuring circuits are Installation Category II. All other instruments' signal terminals are Installation Category I and must not be connected to mains.

Do not connect switching cards directly to unlimited power circuits. They are intended to be used with impedance limited sources. NEVER connect switching cards directly to AC mains. When connecting sources to switching cards, install protective devices to limit fault current and voltage to the card.

Before operating an instrument, make sure the line cord is connected to a properly grounded power receptacle. Inspect the connecting cables, test leads, and jumpers for possible wear, cracks, or breaks before each use.

For maximum safety, do not touch the product, test cables, or any other instruments while power is applied to the circuit under test. ALWAYS remove power from the entire test system and discharge any capacitors before: connecting or disconnecting cables or jumpers, installing or removing switching cards, or making internal changes, such as installing or removing jumpers.


Do not touch any object that could provide a current path to the common side of the circuit under test or power line (earth) ground. Always make measurements with dry hands while standing on a dry, insulated surface capable of withstanding the voltage being measured.


Do not exceed the maximum signal levels of the instruments and accessories, as defined in the specifications and operating information, and as shown on the instrument or test fixture panels, or switching card.


When fuses are used in a product, replace with same type and rating for continued protection against fire hazard.

Chassis connections must only be used as shield connections for measuring circuits, NOT as safety earth ground connections.

If you are using a test fixture, keep the lid closed while power is applied to the device under test. Safe operation requires the use of a lid interlock.

If a  screw is present, connect it to safety earth ground using the wire recommended in the user documentation.

The  symbol on an instrument indicates that the user should refer to the operating instructions located in the manual.

The  symbol on an instrument shows that it can source or measure 1000 volts or more, including the combined effect of normal and common mode voltages. Use standard safety precautions to avoid personal contact with these voltages.

The **WARNING** heading in a manual explains dangers that might result in personal injury or death. Always read the associated information very carefully before performing the indicated procedure.

The **CAUTION** heading in a manual explains hazards that could damage the instrument. Such damage may invalidate the warranty.

Instrumentation and accessories shall not be connected to humans.

Before performing any maintenance, disconnect the line cord and all test cables.

To maintain protection from electric shock and fire, replacement components in mains circuits, including the power transformer, test leads, and input jacks, must be purchased from Keithley Instruments. Standard fuses, with applicable national safety approvals, may be used if the rating and type are the same. Other components that are not safety related may be purchased from other suppliers as long as they are equivalent to the original component. (Note that selected parts should be purchased only through Keithley Instruments to maintain accuracy and functionality of the product.) If you are unsure about the applicability of a replacement component, call a Keithley Instruments office for information.

To clean the instrument, use a damp cloth or mild, water based cleaner. Clean the exterior of the instrument only. Do not apply cleaner directly to the instrument or allow liquids to enter or spill on the instrument.

SPECIFICATIONS

OVERVIEW

CAPACITY: Six plug-in cards per mainframe.

EXPANSION CAPACITY: Daisy-chain expansion of up to four Slave units with one Master unit.

ANALOG BACKPLANES: Backplanes provide automatic row expansion between similar relay cards within one mainframe.

DISPLAY: 14-segment alphanumeric LED display, plus individual status LEDs.

MEMORY: Storage for 100 matrix setups, lithium battery backup.

PROGRAMMED SETTling TIME: 0 to 65 seconds in 1ms increments.

FRONT PANEL MENU: Digital I/O; External Trigger edge; Matrix Ready level; Master/Slave operation; IEEE-488 address; Relay Settling Time; Self Test; Card Identify; factory defaults.

TRIGGER SOURCES: External Trigger (TTL compatible, programmable edge, 600ns minimum pulse width); IEEE-488 bus (TALK, GET, "X"); manual.

STATUS OUTPUT: Matrix Ready (TTL compatible, programmable high- or low-true): goes false when relays are switched, true at end of Programmed Settling Time.

MAKE BEFORE BREAK, BREAK BEFORE MAKE:

Programmable by row.

LIGHT PEN OPTION: Controls crosspoints, memories, make before break and break before make. One light pen controls Master and all Slaves.

EXECUTION SPEED

MAXIMUM TRIGGER RATE: 200 setups per second (stepping through previously stored setups with make-before-break and break-before make disabled).

TRIGGER RESPONSE TIME:

External Trigger: < 1ms.

IEEE-488 GET: < 1ms.

RESPONSE TO IEEE-488 COMMAND (to close a single relay, excluding relay settling time):

Stand Alone: < 15ms.

Master and Four Slaves: < 55ms.

DOWNLOAD TIME (one setup to 707):

Stand Alone: 60ms typical.

IEEE-488 BUS IMPLEMENTATION

MULTILINE COMMANDS: DCL, LLO, SDC, GET, GTL, UNT, UNL, SPE, SPD.

UNILINE COMMANDS: IFC, REN, EOI, SRQ, ATN.

INTERFACE FUNCTIONS: SH1, AH1, T6, TE0, L4, LE0, SR1, RL1, PP0, DC1, DT1, C0, E1.

PROGRAMMABLE PARAMETERS: All parameters programmable except for IEEE-488 bus address and Master/Slave operating mode.

GENERAL

DIGITAL I/O (TTL compatible):

Data: 8 inputs, 8 outputs.

Control: Input Latch, Output Strobe.

REAR PANEL CONNECTORS:

Two BNC: External Trigger, Matrix Ready.

One DB-25: Digital I/O.

Two 8-pin DIN: Master/Slave In, Master/Slave Out.

One 6-pin screw terminal plug: Relay Test.

ENVIRONMENT: Operating: 0 to 50°C.

Storage: -25 to 65°C.

POWER: 90-125V ac or 180-250V ac (internally/externally selected), 50-60Hz, 140VA maximum.

RELAY DRIVE: 2.9A minimum.

DIMENSIONS, WEIGHT: 356mm high × 432mm wide × 574mm deep (14 in. × 17 in. × 22.6 in.). Net weight without cards 18.2kg (40 lbs.).

ACCESSORIES SUPPLIED: Instruction manual, power line cord, relay test connector, fixed rack mounting hardware.

ACCESSORIES AVAILABLE:

Model 7070: Universal Adapter Card

Model 7071: General Purpose Matrix Card

Model 7072: Semiconductor Matrix Card

Model 7073: Coaxial Matrix Card

Model 7078-DIN: 8-Pin DIN Cable (Master/Slave), 1.8m (6 ft.)

Model 7078-PEN: Programming Light Pen (includes holder)

Model 7079: Slide Rack Mounting Kit

Model 7007-1: Shielded IEEE-488 Cable, 1m (3.2 ft.)

Model 7007-2: Shielded IEEE-488 Cable, 2m (6.6 ft.)

Model 7051-2: BNC to BNC Cable, 0.6m (2 ft.)

Model 7051-5: BNC to BNC Cable, 1.5m (5 ft.)

MODEL 707 SWITCHING MATRIX

Specifications subject to change without notice.

Contains information on Model 707 features, specifications, and accessories.

SECTION 1

General Information

Includes an overview of front and rear panel controls, installing matrix cards, card connections, system expansion, and basic switching operation.

SECTION 2

System Start-up

Contains detailed information on operating the Model 707. Use this section as a reference to all front panel operations.

SECTION 3

Operation

Describes information on connecting the Model 707 to the IEEE-488 bus and programming the instrument from a computer.

SECTION 4

IEEE-488 Programming

Includes detailed operating principles and block diagrams for the various circuits within the instrument.

SECTION 5

Principles of Operation

Details maintenance procedures for the Model 707, including fuse replacement, line voltage selection, Relay Test software, and troubleshooting.

SECTION 6

Maintenance

Lists replacement parts, and also includes component layout and schematic drawings for the Model 707.

SECTION 7

Replaceable Parts

Table of Contents

SECTION 1—General Information

1.1	INTRODUCTION	1-1
1.2	FEATURES	1-1
1.3	WARRANTY INFORMATION	1-1
1.4	MANUAL ADDENDA	1-1
1.5	SAFETY SYMBOLS AND TERMS	1-1
1.6	SPECIFICATIONS	1-2
1.7	UNPACKING and INSPECTION	1-2
1.7.1	Inspection for Damage	1-2
1.7.2	Shipping Contents	1-2
1.7.3	Instruction Manual	1-2
1.7.4	Relay Test Software	1-2
1.8	REPACKING FOR SHIPMENT	1-2
1.9	OPTIONAL ACCESSORIES	1-2

SECTION 2—System Start-up

2.1	INTRODUCTION	2-1
2.2	FRONT PANEL FAMILIARIZATION	2-1
2.3	REAR PANEL FAMILIARIZATION	2-6
2.4	INSTALLING AND REMOVING CARDS	2-8
2.5	CARD CONNECTIONS	2-10
2.6	EXPANDING MATRIX SIZE	2-12
2.6.1	Single Unit Expansion	2-12
2.6.2	Multiple Unit Expansion	2-18
2.6.3	System Expansion Issues	2-22
2.6.4	Documenting System Configuration	2-22
2.7	BASIC SWITCHING OPERATION	2-24
2.7.1	Powerup	2-24
2.7.2	Selecting Make/Break and Break/Make Rows	2-24
2.7.3	Modifying a Relay Setup	2-24
2.7.4	Storing Setup and Sending to Relays	2-25

SECTION 3—Operation

3.1	INTRODUCTION	3-1
3.2	SETUP DATA PATHS	3-1
3.3	POWER-UP PROCEDURE	3-3
3.3.1	Line Voltage Selection	3-3
3.3.2	Line Power Connections	3-3
3.3.3	Power Switch	3-3
3.3.4	Power-up Self-test and Messages	3-3
3.3.5	Power-up Configuration	3-4
3.3.6	Master/Slave Power-up	3-5
3.4	DISPLAYS AND MESSAGES	3-5
3.4.1	Alphanumeric Display	3-5
3.4.2	Display Messages	3-6

3.4.3	IEEE-488 Status Indicators	3-7
3.4.4	Crosspoint Displays LEDs	3-7
3.4.5	Make/Break and Break/Make LEDs	3-9
3.4.6	Light Pen	3-9
3.5	SELECTING CROSSPOINT DISPLAY	3-11
3.6	MODIFYING CROSSPOINT DISPLAY	3-11
3.7	COPYING CROSSPOINT DISPLAY	3-12
3.8	INSERTING AND DELETING STORED SETUPS	3-13
3.9	MENU OPERATIONS	3-13
3.9.1	Digital I/O	3-15
3.9.2	External Trigger	3-15
3.9.3	Matrix Ready	3-16
3.9.4	Stand-alone and Master/Slave	3-17
3.9.5	IEEE-488 Bus Address	3-18
3.9.6	Relay (Hardware) Settling Times	3-19
3.9.7	Card Labels	3-19
3.9.8	Self-test	3-19
3.9.9	Factory Defaults	3-19
3.10	SELECTING SWITCHING PARAMETERS	3-20
3.10.1	Programmed Settling Time	3-20
3.10.2	Make/Break and Break/Make Rows	3-20
3.11	TRIGGERING	3-21
3.11.1	Trigger Sources	3-21
3.11.2	Front Panel Triggering	3-21
3.11.3	Trigger Overrun Conditions	3-22
3.11.4	External Trigger Input	3-26
3.11.5	Matrix Ready Output	3-26
3.11.6	IEEE-488 Bus Triggering	3-26
3.12	RESETTING	3-26
3.13	BACKPLANE JUMPERS	3-26

SECTION 4—IEEE-488 Programming

4.1	INTRODUCTION	4-1
4.2	A SHORT-CUT TO IEEE-488 OPERATION	4-1
4.3	BUS CONNECTIONS	4-3
4.3.1	Cable Connections	4-3
4.4	INTERFACE FUNCTION CODES	4-5
4.5	PRIMARY ADDRESS PROGRAMMING	4-5
4.6	CONTROLLER PROGRAMMING	4-6
4.7	FRONT PANEL ASPECTS OF IEEE-488 OPERATION	4-6
4.7.1	Front Panel Error Messages	4-6
4.7.2	IEEE-488 Status Indicators	4-8
4.7.3	LOCAL key	4-9
4.7.4	Simultaneous Front Panel and Bus Operation	4-9
4.8	GENERAL BUS COMMAND PROGRAMMING	4-9
4.8.1	REN (Remote Enable)	4-10
4.8.2	IFC (Interface Clear)	4-10
4.8.3	LLO (Local Lockout)	4-10
4.8.4	GTL (Go To Local) and Local	4-10
4.8.5	DCL (Device Clear)	4-11
4.8.6	SDC (Selective Device Clear)	4-11
4.8.7	GET (Group Execute Trigger)	4-12
4.8.8	SPE, SPD (Serial Polling)	4-12

4.9	DEVICE-DEPENDENT COMMAND PROGRAMMING	4-13
4.9.1	A - External Trigger	4-17
4.9.2	B - Matrix Ready	4-19
4.9.3	C - Close Crosspoint	4-20
4.9.4	D - Display	4-21
4.9.5	E - Edit Pointer	4-22
4.9.6	F - Enable/disable Triggers	4-23
4.9.7	G - Data Format	4-24
4.9.8	H - Hit Key	4-29
4.9.9	I - Insert Blank Setup	4-30
4.9.10	J - Self-test	4-31
4.9.11	K - EOI and Hold-off	4-32
4.9.12	L - Download Setups	4-33
4.9.13	M - SRQ and Serial Poll Byte	4-34
4.9.14	N - Open Crosspoint	4-37
4.9.15	O - Digital Output	4-38
4.9.16	P - Clear Crosspoints	4-39
4.9.17	Q - Delete Setup	4-40
4.9.18	R - Restore Defaults	4-41
4.9.19	S - Programmed Settling Time	4-42
4.9.20	T - Trigger	4-43
4.9.21	U - Status	4-44
4.9.22	V - Make/Break	4-50
4.9.23	W - Break/Make	4-51
4.9.24	X - Execute	4-52
4.9.25	Y - Terminator	4-53
4.9.26	Z - Copy Setup	4-54
4.10	RELAY COMMAND COMBINATIONS	4-55
4.11	TIMING CONSIDERATIONS	4-55
4.12	PROGRAMMING EXAMPLES	4-58
4.12.1	Interrupt Example	4-58
4.12.2	Triggering Example	4-61

SECTION 5—Principles of Operation

5.1	INTRODUCTION	5-1
5.2	OVERALL FUNCTIONAL DESCRIPTION	5-1
5.3	MICROCOMPUTER	5-1
5.3.1	Reset Circuit	5-1
5.3.2	Address Decoding	5-1
5.3.3	Memory	5-4
5.4	RELAY CONTROL CIRCUITRY	5-4
5.4.1	Matrix Card Interface	5-6
5.4.2	Matrix Card Logic	5-6
5.5	DISPLAY CIRCUITRY	5-10
5.5.1	Display Data	5-13
5.5.2	Front Panel Keys	5-13
5.5.3	Display Interface	5-13
5.5.4	Refresh Display/Read Keyboard	5-13
5.6	LIGHT PEN INTERFACE	5-14
5.7	MASTER/SLAVE CIRCUITRY	5-15
5.7.1	Serial Communication	5-15
5.7.2	Control Signals	5-17
5.8	DIGITAL I/O	5-17
5.9	IEEE-488 BUS INTERFACE	5-17
5.10	POWER SUPPLIES	5-19

SECTION 6—Maintenance

6.1	INTRODUCTION	6-1
6.2	LINE VOLTAGE SELECTION	6-1
6.3	FUSE REPLACEMENT	6-2
6.4	FIXED RACK INSTALLATION	6-2
6.5	DISASSEMBLY	6-6
6.6	BATTERY REPLACEMENT	6-11
6.8	MAINFRAME TROUBLESHOOTING	6-11
6.8.1	Recommended Test Equipment	6-12
6.8.2	Power-up Self-test	6-12
6.8.3	Power Supply Checks	6-12
6.8.4	Digital Board Checks	6-12
6.8.5	Display Board Checks	6-16
6.9	RELAY TEST SOFTWARE	6-16
6.9.1	Running the Program	6-19
6.9.2	Analyzing Errors	6-20
6.9.3	Using an Extender Card	6-20
6.10	CLEANING	6-20
6.10.1	Backplane	6-20
6.10.2	Fan Filter	6-20

SECTION 7—Replaceable Parts

7.1	INTRODUCTION	7-1
7.2	PARTS LIST	7-1
7.3	ORDERING INFORMATION	7-1
7.4	FACTORY SERVICE	7-1
7.5	COMPONENT LOCATION DRAWINGS AND SCHEMATIC DIAGRAMS	7-1

APPENDICES

A	Card Configuration Worksheet	A-1
B	Controller Programs	B-1
C	IEEE-488 Bus Overview	C-1
D	Equivalent IEEE-488 Commands for Front Panel Keys	D-1
E	ASCII Character Codes and IEEE-488 Multiline Interface Command Messages	E-1
F	Glossary of Terms	F-1

List of Tables

SECTION 2—System Start-up

2-1	Row-Column and Column-Column Paths	2-11
2-2	Analog Bus Usage	2-15
2-3	Model 707 External Expansion Cables	2-17
2-4	Response Time Comparison	2-22
2-5	Model 707 Card Configuration	2-23

SECTION 3—Operation

3-1	Setup Data Paths	3-2
3-2	Power-up, Reset, and Factory Defaults	3-4
3-3	Error Messages	3-6
3-4	Information Messages	3-7
3-5	Maximum Number of Closed Crosspoints	3-12
3-6	Menu Operations	3-14
3-7	Status of Slave Unit Controls	3-18
3-8	Make/Break and Break/Make Front Panel Operation	3-21
3-9	Front Panel Messages for Trigger Sources	3-21

SECTION 4—IEEE-488 Programming

4-1	IEEE-488 Contact Designation	4-4
4-2	Model 707 Interface Function Codes	4-5
4-3	HP BASIC 4.0 IEEE-488 Statements	4-6
4-4	Front Panel IEEE-488 Messages	4-6
4-5	General Bus Commands and Associated BASIC Statements	4-9
4-6	Factory Default, Power-up, and DCL/SDC Conditions	4-11
4-7	Order of Command Execution	4-14
4-8	Device-Dependent Command Summary	4-15
4-9	Example Setup of Master/Slave Configuration	4-25
4-10	Byte Counts for Data Format	4-26
4-11	Typical Transmission and Hold-off Times for a Stand-alone Unit	4-56
4-12	Typical Transmission and Hold-off for a Master and One Slave	4-57

SECTION 5—Principles of Operation

5-1	Display Segment Assignments	5-12
-----	-----------------------------------	------

SECTION 6—Maintenance

6-1	Line Fuse Values	6-2
6-2	Fixed Rack Parts	6-2
6-3	Recommended Troubleshooting Equipment	6-12
6-4	Power Supply Checks	6-12
6-5	Microcomputer Checks	6-13
6-6	Relay Control Checks	6-13
6-7	Display Interface Checks	6-14
6-8	Digital I/O Checks	6-14
6-9	Light Pen Checks	6-15
6-10	Master/Slave Checks	6-15
6-11	Display Board Checks	6-17

APPENDIX B

B-1	BASIC Statements Necessary to Send Bus Commands	B-4
-----	---	-----

APPENDIX C

C-1	IEEE-488 Bus Command Summary	C-4
C-2	Hexadecimal and Decimal Command Codes	C-5
C-3	Typical Addressed Command Sequence	C-6
C-4	Typical Device-Dependent Command Sequence	C-6
C-5	IEEE Command Group	C-6

List of Illustrations

SECTION 2—System Start-up

2-1	Model 707 Front Panel	2-2
2-2	Setup Data Transfers	2-3
2-3	Model 707 Rear Panel	2-7
2-4	Installing a Matrix Card	2-9
2-5	Connecting Instruments to Rows	2-10
2-6	Connecting Instruments to Columns	2-11
2-7	Backplane Buses	2-12
2-8	Backplane Expansion of Analog Bus #1	2-13
2-9	Backplane of Analog Bus #2	2-14
2-10	Backplane Expansion of Analog Bus #3	2-15
2-11	Row Connections with Differential Card Models	2-16
2-12	Example of Partial Matrix Expansion	2-17
2-13	Row Connections of Stand-alone Units	2-18
2-14	Examples of Master/Slave Interconnect Cables	2-19
2-15	Master/Slave Column Locations	2-20
2-16	Example of Master/Slave Row Expansion	2-21

SECTION 3—Operation

3-1	Paths for Relay Setup Data	3-1
3-2	Line Voltage Switch	3-3
3-3	Alphanumeric Display	3-5
3-4	Crosspoint Display	3-8
3-5	Light Pen	3-10
3-6	Crosspoint Display Keys	3-11
3-7	Data Entry Keys	3-12
3-8	Memory Keys	3-13
3-9	Digital I/O Port	3-15
3-10	Rear Panel BNC Jacks	3-15
3-11	Sample External Trigger Pulses	3-16
3-12	Sample Matrix Ready Pulses	3-16
3-13	Master/Slave Connectors	3-17
3-14	IEEE-488 Bus Connector	3-18
3-15	Switching Keys	3-20
3-16	Trigger Keys	3-21
3-17	Timing without Make/Break or Break/Make Rows	3-23
3-18	Timing with Either Make/Break or Break/Make Rows	3-24
3-19	Timing with Both Make/Break and Break/Make Rows	3-25
3-20	Handle and Front Panel Screws	3-27
3-21	Backplane Jumpers	3-28
3-22	Backplane Removal	3-29

SECTION 4—IEEE-488 Programming

4-1	Flowchart of Example Program	4-1
4-2	IEEE-488 Connector	4-3
4-3	IEEE-488 Connections	4-3
4-4	IEEE-488 Connector Location	4-4
4-5	Contact Assignments	4-4
4-6	External Trigger Pulse	4-17
4-7	Matrix Ready Pulse	4-19
4-8	G0 and G1 Full Output Formats	4-26
4-9	G2 and G3 Inspect Output Formats	4-27
4-10	G4 and G5 Condensed Output Formats	4-27
4-11	G6 and G7 Binary Output Formats	4-28
4-12	SRQ Mask and Serial Poll Byte Format	4-34
4-13	READY and MATRIX READY Signal Timing	4-42
4-14	U0 Machine Status Word	4-44
4-15	U1 Error Status Word	4-45
4-16	U3 Relay Step Pointer	4-46
4-17	U4 Number of Slaves	4-46
4-18	U5 Card Identification	4-47
4-19	Relay Settling Time	4-47
4-20	U7 Digital Input	4-48
4-21	U8 Relay Test Input	4-48
4-22	Interrupt Example Flowchart (Main Program)	4-58
4-23	Interrupt Flowchart (Interrupt Service Routine)	4-59
4-24	Interrupt Example Program Listing	4-24
4-25	Trigger Connections for Triggering Example	4-62
4-26	Analog Connections for Triggering Example	4-63
4-27	Triggering Example Flowchart	4-64
4-28	Triggering Example Program Listing	4-65

SECTION 5—Principles of Operation

5-1	Model 707 Block Diagram	5-2
5-2	Digital Board Block Diagram	5-3
5-3	RAM and Battery Backup	5-5
5-4	Matrix Card Interface Simplified Schematic	5-7
5-5	Matrix Card Interface Timing Diagram	5-8
5-6	Typical Matrix Card Logic Block Diagram	5-8
5-7	IDDATA Timing Diagram	5-9
5-8	Display Board Block Diagram	5-11
5-9	Display Interface Simplified Schematic	5-13
5-10	Light Pen Interface Simplified Schematic	5-14
5-11	Master/Slave Interface Simplified Schematic	5-16
5-12	Digital I/O Interface Simplified Schematic	5-18

SECTION 6—Maintenance

6-1	Line Voltage Switch	6-1
6-2	Captive Nut Installation	6-3
6-3	Nut Bar on Flange	6-3
6-4	Chassis Support Sizing	6-4
6-5	Chassis Support Assembly	6-5
6-6	Right Side View of Disassembly	6-6

6-7	Front View of Disassembly	6-7
6-8	Left-side View of Disassembly.....	6-9
6-9	Troubleshooting Programs	6-16
6-10	Relay Control Waveforms	6-17
6-11	Display Interface Waveforms	6-18
6-12	Operation of Relay Test Program.....	6-19

APPENDIX C

C-1	IEEE Bus Configuration	C-2
C-2	IEEE-488 Handshake Sequence	C-3
C-3	Command Codes	C-7

SECTION 1

General Information

1.1 INTRODUCTION

This section contains general information about the Model 707 Switching Matrix. The Model 707 is designed as a programmable switch for connecting signal paths in a matrix topology. It is for applications requiring a large-scale matrix (up to 576 crosspoints per mainframe and 2880 crosspoints per master/slave configuration). Plug-in cards are available for general and special purpose switching applications.

Section 1 is arranged as follows:

- 1.2 Features
- 1.3 Warranty Information
- 1.4 Manual Addenda
- 1.5 Safety Symbols and Terms
- 1.6 Specifications
- 1.7 Unpacking and Inspection
- 1.8 Repacking for Shipment
- 1.9 Optional Accessories

1.2 FEATURES

Key features of the Model 707 Switching Matrix include:

- The six-slot mainframe accepts any mix of 8-row by 12-column matrix cards.
- Rows are extended within the mainframe to minimize system wiring and interconnect requirements.
- Storage of 100 sets of relay setups, which can be uploaded or downloaded through the IEEE-488 interface.
- An active front panel LED display shows the current relay status, a stored setup, or an editing scratchpad.
- High-speed triggering of stored setups.
- Make/break and break/make switching are programmable by rows. Operation is transparent to the user and independent of the relay setup.
- With five units connected in a master/slave configuration, the maximum matrix size is eight rows by 360 columns (2880 crosspoints on one IEEE-488 address).
- An optional light pen is available for interactive controlling of relay states, editing stored relay setups, and selecting make/break and break/make rows.

1.3 WARRANTY INFORMATION


Warranty information is located on the inside front cover of this instruction manual. Should your Model 707 require warranty service, contact the Keithley representative or authorized repair facility in your area for further information. When returning the mainframe for repair, be sure to fill out and include the service form at the back of this manual to provide the repair facility with the necessary information.


1.4 MANUAL ADDENDA

Any improvements or changes concerning the mainframe or manual will be explained in an addendum included with the unit. Be sure to note these changes and incorporate them into the manual before using or servicing the unit.

1.5 SAFETY SYMBOLS AND TERMS

The following symbols and terms may be found on an instrument or used in this manual:

The  symbol on an instrument indicates that the user should refer to the operating instructions located in the instruction manual.

The  symbol on an instrument shows that high voltage may be present on the terminal(s). Use standard safety precautions to avoid personal contact with these voltages.

The **WARNING** heading used in this manual explains dangers that could result in personal injury or death. Always read the associated information very carefully before performing the indicated procedure.

The **CAUTION** heading used in this manual explains hazards that could damage the instrument. Such damage might invalidate the warranty.

1.6 SPECIFICATIONS

Model 707 specifications can be found at the front of this manual. These specifications are exclusive of the matrix card specifications, which are located in their appropriate instruction manual.

1.7 UNPACKING AND INSPECTION

1.7.1 Inspection for Damage

Upon receiving the Model 707, carefully unpack it from its shipping carton and inspect the unit for any obvious signs of physical damage. Report any damage to the shipping agent immediately. Save the original packing carton for possible future reshipment.

1.7.2 Shipment Contents

The following items are included with every Model 707 order:

- Model 707 Switching Matrix
- Model 707 Instruction Manual
- Power line cord
- Relay test connector
- Relay Test Software diskettes
- Fixed rack mounting hardware
- Additional accessories as ordered

1.7.3 Instruction Manual

The Model 707 Instruction Manual is drilled to fit in the supplied three-ring binder. Add any matrix card instruction manuals after the mainframe manual.

If an additional Model 707 Instruction Manual is needed, order the manual package (Keithley Part Number 707-901-00). The manual package includes an instruction manual and any applicable addenda.

1.7.4 Relay Test Software

The Relay Test Software verifies the operation of matrix relay cards designed for use in the Model 707 (e.g., Models 7071, 7072, and 7073). The software is supplied in two media formats:

- 5¼" diskette (Keithley P/N 707-851)—For an IBM PC or compatible with a CEC IEEE-488 interface. (PC-488-CEC is available from Keithley Instruments.)
- 3½" diskette (Keithley P/N 707-852)—For Hewlett Packard Series 200/300 controllers running HP BASIC 4.0.

1.8 REPACKING FOR SHIPMENT

Should it become necessary to return the Model 707 for repair, carefully pack the unit in its original packing carton or the equivalent, and include the following information:

- Advise as to the warranty status of the mainframe.
- Write ATTENTION REPAIR DEPARTMENT on the shipping label.
- Fill out and include the service form located at the back of this manual.

1.9 OPTIONAL ACCESSORIES

The following accessories are available for the Model 707:

Model 7070 Universal Adapter Card—The Model 7070 card installs in the Model 707 and is jumper-selectable for use either as a backplane extender or a breadboard. It has quick disconnect screw terminals and 10-ft. ribbon cables.

Model 7071 General Purpose Matrix Card—The Model 7071 card has 8 rows by 12 columns of 3-pole Form A switching for general purpose applications. It installs in the Model 707 and has mass terminated connectors in addition to quick-disconnect screw terminals.

Model 7072 Semiconductor Matrix Card—The Model 7072 card has 2 rows by 12 columns of 2-pole Form A for low current switching, 4 rows by 12 columns of 2-pole Form A for general purpose switching, and 2 rows by 12 columns of 1-pole Form A for C-V switching. It installs in the Model 707 and has 3-lug triaxial connectors.

Model 7073 Coaxial Matrix Card—The Model 7073 card has 8 rows by 12 columns of 1-pole Form A switching (up to 30MHz) for applications with single-ended instruments. It installs in the Model 707 and has BNC connectors.

Model 7078-DIN 8-pin DIN Cable—The Model 7078-DIN cable has two 8-pin circular (DIN) connectors and is 1.8m (6 ft.) long. Multiple cables are used for connecting Model 707 units in a master/slave configuration through the rear panel master/slave connectors.

Model 7078-PEN Programming Light Pen—The Model 7078-PEN connects to the Model 707 front panel. It is used to toggle the states of crosspoint LEDs, make/break LEDs, and break/make LEDs. A pen holder is included.

Model 7079 Slide Rack Mounting Kit—The Model 7079 kit consists of two sets of support brackets, equipment slides, and hardware for mounting the Model 707 in a standard 19-inch equipment rack or cabinet.

Model 7007 Shielded IEEE-488 Cables—The Model 7007 connects the Model 707 to the IEEE-488 bus using shielded cables to reduce electromagnetic interference (EMI). The Model 7007-1 is one meter (3.3 ft.) long and has an EMI shielded IEEE-488 connector at each end. The Model 7007-2 cable is identical to the Model 7007-1, but is 2m (6.6 ft.) long.

Model 7051 BNC to BNC Cables—The Model 7051 cables are for making connections to External Trigger and Matrix Ready on the Model 707 rear panel. The Model 7051-2 is a 50 Ω BNC to BNC cable (RG-58C), which is 0.6m (2 ft.) long. The Model 7051-5 cable is identical to the Model 7051-2, but is 1.5m (5 ft.) long.

Model 8000-14 Enclosures—The Model 8000-14 is a 19"-wide by 14"-high open-backed steel enclosure. It is supplied with hardware to mount a bench-top Model 707. The top cover of the enclosure can be removed to access jumpers between cards installed in a Model 707.

SECTION 2

System Start-up

2.1 INTRODUCTION

This section contains introductory information on operating your instrument and is intended to help you get your Model 707 up and running as quickly as possible. It includes a brief description of operating controls and connections. Once you are familiar with the material presented here, refer to Section 3 for more detailed information.

Section 2 is organized as follows:

- 2.2 Front Panel Familiarization:** Briefly describes each front panel control and outlines display operations.
- 2.3 Rear Panel Familiarization:** Outlines each aspect of the Model 707 rear panel, including connectors.
- 2.4 Installing and Removing Cards:** Itemizes a general procedure for installation and removal of matrix cards in the Model 707 mainframe.
- 2.5 Card Connectors:** Describes where to connect instruments and DUTs to the matrix rows and columns.

2.6 Expanding Matrix Size: Discusses methods for expanding the matrix, both internal to the mainframe and with multiple units.

2.7 Basic Switching Operation: Provides a general procedure for powering up the Model 707, choosing make/break or break/make operation, modifying the crosspoint display, storing the setup, and sending the setup to the relays.

2.2 FRONT PANEL FAMILIARIZATION

An overview of the Model 707 operation is given in the following paragraphs. The front panel of the instrument is shown in Figure 2-1. Figure 2-2 illustrates setup data transfers within the Model 707. This pictorial will be helpful in understanding the operations of individual front panel keys.

All front panel keys except POWER are momentary contact switches. Some keys have an LED to indicate the selected function. The keys are color-coded into functional groups for ease of operation.

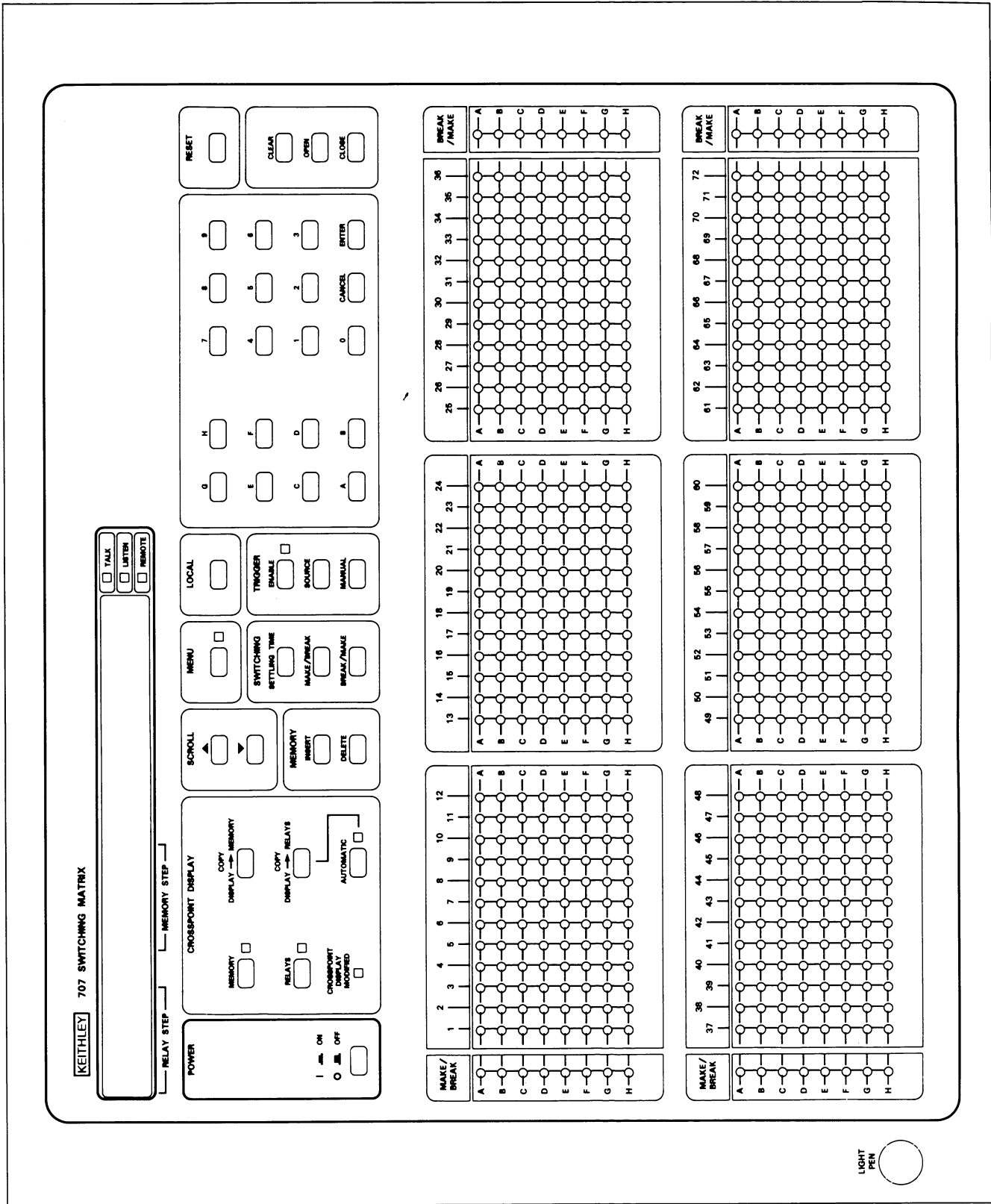


Figure 2-1. Model 707 Front Panel

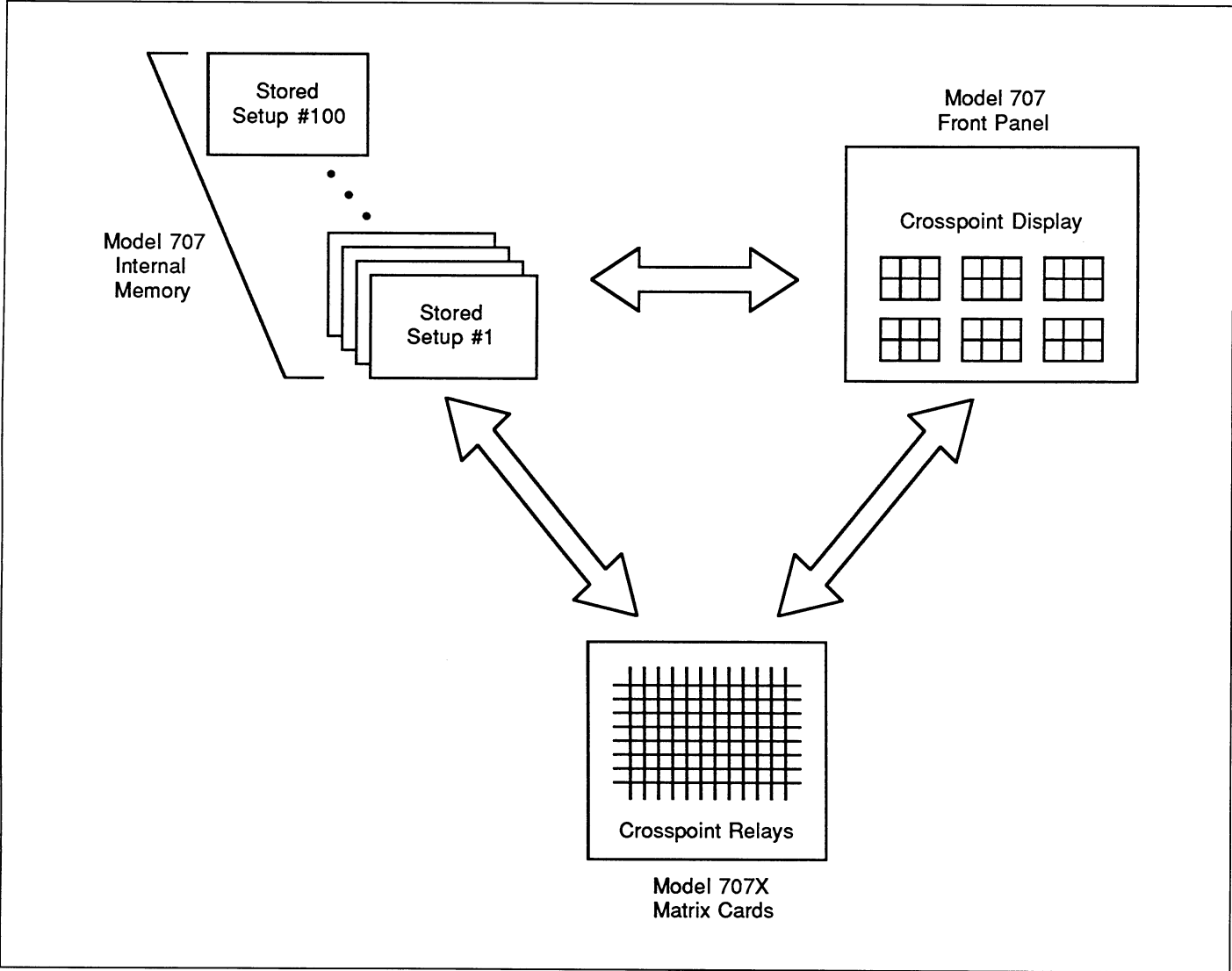


Figure 2-2. Setup Data Transfers

POWER—AC power switch turns the unit on or off.

CROSSPOINT DISPLAY GROUP

MEMORY—Displays a stored relay setup (from location shown in MEMORY STEP field) on the crosspoint display and lights the MEMORY indicator.

RELAYS—Displays the current relay setup on the crosspoint display and lights the RELAYS indicator.

CROSSPOINT DISPLAY MODIFIED—Lights when changes are made to the crosspoint display (by front panel keys or light pen), making it different from the original configuration.

COPY DISPLAY → MEMORY—Copies the displayed crosspoint configuration to the location shown in the MEMORY STEP field.

COPY DISPLAY → RELAYS—Copies the displayed crosspoint configuration to the relays.

AUTOMATIC (COPY DISPLAY → RELAYS)—When this LED is lit, any change to the crosspoint display is sent to the relays at the same time. The pushbutton toggles the LED on an off.

SCROLL GROUP

SCROLL ▲—If MEMORY indicator is lit, increments MEMORY STEP field and displays setup on crosspoint LEDs. If RELAYS indicator is lit, increments RELAY STEP field, displays setup on crosspoint LEDs, and sends setup to relays. Also used for scrolling up through a list of multiple choice parameters.

SCROLL ▼—Same actions as the SCROLL ▲ key except that it decrements and scrolls down.

MEMORY GROUP

INSERT—Inserts a blank setup at the location shown in the MEMORY STEP field. Moves higher stored setups up one memory location.

DELETE—Deletes the setup at the location shown in the MEMORY STEP field. Moves higher stored setups down one memory location.

MENU—Steps through the available menu items:

- View digital input, program digital output.
- Select whether rising or falling edge of External Trigger pulse triggers Model 707.
- Select active high or active low Matrix Ready output signal.
- Select master/slave or stand-alone operation.
- Program IEEE-488 address.
- View longest relay settling time of present card configuration.
- View unit configuration by slot number and card model number.
- Execute self-test.
- Restore factory defaults (and clear stored setups).

Programmable parameters can be changed with the SCROLL or data entry keys and then pressing ENTER.

SWITCHING GROUP

SETTLING TIME—Displays the current value of programmed settling time. (This delay begins after the relay settling time.) To change the value, enter between 0-65000msec and press ENTER.

MAKE/BREAK—Selects rows to operate as make/break (make-before-break) for all setups. First enter row designation (A-H), then press MAKE/BREAK to toggle the state for that row and immediately reprogram the Model 707 for the new operation.

BREAK/MAKE—Same action as MAKE/BREAK except that it selects break/make (break-before-make) rows. (Selecting a row for break/make de-selects it for make/break and vice versa.)

LOCAL—When in remote (REMOTE on), returns the Model 707 to local mode (REMOTE off). It restores operation of other front panel controls unless LLO (Local Lockout) is in effect.

TRIGGER GROUP

ENABLE—Toggles between triggers enabled and triggers disabled. When triggers are enabled, the LED is lit.

SOURCE—Displays current trigger source. Use **SCROLL** keys to display sources, then press **ENTER** to select the desired source:

TRIG ON TALK - IEEE talk command
TRIG ON GET - IEEE GET command
TRIG ON X - IEEE X command
TRIG ON EXT - External trigger pulse (rear panel input)
TRIG ON KEY - Front panel **MANUAL** key only

MANUAL—Generates a trigger from front panel if triggers are enabled (no matter which trigger source is selected). If the trigger source is **TRIG ON KEY**, only the **MANUAL** key generates a trigger.

DATA ENTRY (A-H, 0-9)—These keys are for entering row/column addresses and setup locations, selecting make/break and break/make rows, and entering various numeric values.

CANCEL—If the value in the alphanumeric display has been modified, this key restores the current parameter value. If the value in the alphanumeric display has not been modified, this key returns the Model 707 to the previous display. **CANCEL** also exits from menu mode if no changes have been made.

ENTER—If the value in the alphanumeric display has been modified, pressing this key stores the parameter value. Also invokes immediate action items from the menu and exits menu mode (except when digital I/O is displayed).

RESET—Performs the same functions as cycling power (all relays are opened, triggers are disabled, **RELAY STEP** to 000, **MEMORY STEP** to 001, etc.), except powerup self-checking and master/slave loop initialization.

CLEAR—Turns off all crosspoint display LEDs. If the **AUTOMATIC (COPY DISPLAY → RELAYS)** indicator is lit, all relays are opened immediately.

OPEN—Turns off crosspoint LED of row/column shown on alphanumeric display. If the **AUTOMATIC (COPY DISPLAY → RELAYS)** indicator is lit, the corresponding relay opens immediately.

CLOSE—Same action as **OPEN** key except that it turns on

the crosspoint LED and relay.

ALPHANUMERIC DISPLAY—A 14-character display that can show:

- Error messages.
- Menu item selections.
- Last setup sent from memory to the relays (**RELAY STEP** field).
- Last setup recalled from memory to the crosspoint display (**MEMORY STEP** field).
- Trigger source.
- Programmed settling time.
- Alphanumeric key presses (row/column addresses, setup locations).

IEEE-488 STATUS INDICATORS

TALK, LISTEN, REMOTE—These three LED indicators apply to instrument operation over the IEEE-488 bus. The **TALK** and **LISTEN** indicators show when the unit has been addressed to talk or listen. **REMOTE** turns on to show when the unit is in the IEEE-488 remote state. See Section 4 for detailed information on operation over the bus.

CROSSPOINT DISPLAY LEDs—Show open and closed crosspoints of the current relay setup, a stored relay setup, or an edited relay setup. Each LED block of 8 rows by 12 columns shows on/off states of one card. States can be changed by front panel keys, triggers, or optional light pen. Crosspoint configurations can be stored in memory or sent to relays.

MAKE/BREAK ROW LEDs—Shows which rows are selected for make/break operation. The LEDs can be turned on or off by the **MAKE/BREAK**, **BREAK/MAKE** keys or an optional light pen.

BREAK/MAKE ROW LEDs—Same function as **MAKE/BREAK** row LEDs except for showing which rows are selected for break/make operation. Note that selecting a row for break/make de-selects it for make/break and vice versa.

LIGHT PEN—An optional input device for toggling the on/off state of the Crosspoint Display LEDs, **MAKE/BREAK** row LEDs, and **BREAK/MAKE** row LEDs. One light pen is used to control the LEDs of up to five Model 707 mainframes.

2.3 REAR PANEL FAMILIARIZATION

An overview of the rear panel of the Model 707 is in the paragraphs that follow. The rear panel is shown in Figure 2-3. In addition to the various connectors, a column locator diagram for a master or stand-alone unit is provided on the rear panel.

CARD SLOTS—The Model 707 accepts up to six plug-in matrix cards (8 rows by 12 columns) per mainframe.

MASTER/SLAVE OUT—An 8-pin DIN connector for connecting a cable to the next mainframe in a master/slave daisy chain configuration.

MASTER/SLAVE IN —An 8-pin DIN connector for connecting a cable from the previous mainframe in a master/slave daisy chain configuration.

EXTERNAL TRIGGER INPUT—A BNC jack for applying a trigger pulse to change to the next relay setup, if triggers are enabled and TRIG ON EXT is selected as the source. Pulses must be TTL-compatible, negative- or positive-going (selected by a menu item), with a duration greater than 600nsec.

MATRIX READY OUTPUT—A BNC jack providing a TTL-compatible, high- or low-true level (selected by a menu item). It goes false when relays are switched and goes true after the sum of the relay settling time and the programmed settling time.

RELAY TEST—A 6-pin quick-disconnect terminal block with logic ground and four logic inputs for testing cross-point relay closures. Wiring between this terminal block and rows A and B of any card in the group of cards to be tested is necessary for the test.

DIGITAL I/O—A DB-25 connector for the TTL-compatible digital I/O with data lines for eight inputs and eight outputs. It also contains control lines for handshaking (Input Latch and Output Strobe). Input lines are viewed and output lines are programmed through a menu item.

IEEE-488 INTERFACE—This connector interfaces the Model 707 to the IEEE-488 bus. IEEE interface function codes are marked adjacent to the connector.

LINE VOLTAGE SELECTED—The position of this switch determines the operating voltage range of the instrument: 90-110V, 105-125V, or 180-220V, 210-250V.

AC RECEPTACLE—Power is applied through the supplied power cord to the 3-terminal AC receptacle.

LINE FUSE—The line fuse provides protection for the AC power line input. The fuse rating must match the line voltage setting.

FAN FILTER—The fan filter keeps dirt from being drawn into the instrument by the internal cooling fan. The filter should be kept clean to ensure proper instrument cooling.

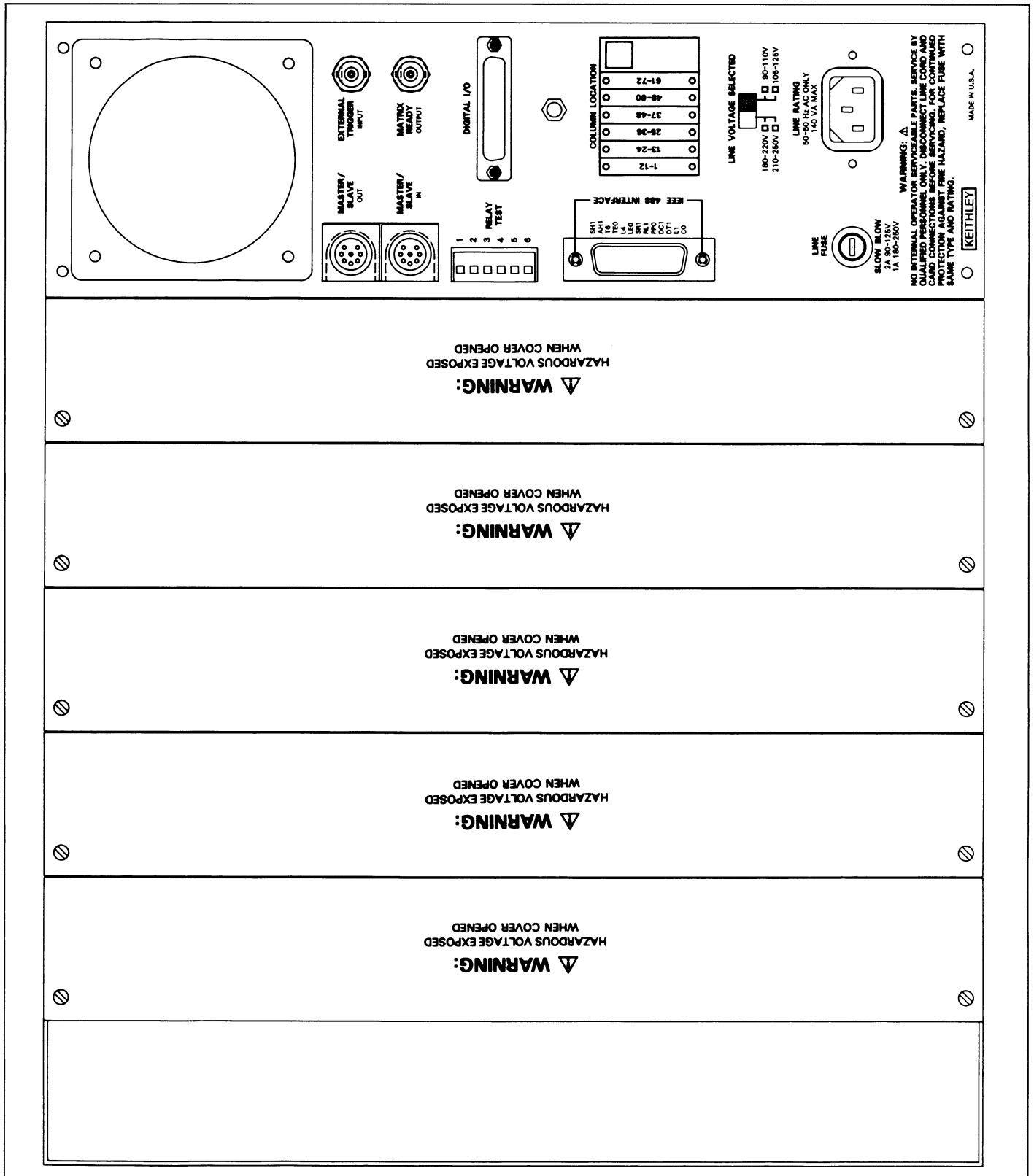


Figure 2-3. Model 707 Rear Panel

2.4 INSTALLING AND REMOVING CARDS

Before operating the Model 707 in a test environment, matrix cards (up to six per mainframe) must be installed into the mainframe. Note that matrix cards are not necessary to program setups. Setups for master/slave configurations can be programmed as long as the MASTER/SLAVE OUT to MASTER/SLAVE IN loop connections are present. (See paragraph 2.6.2.)

WARNING

Before installing/removing cards or making card connections, turn off mainframe power and disconnect the line cord. Also, ensure that no power is applied from the user's circuit.

Install a card in the Model 707 as follows, using Figure 2-4 as a guide. Instructions specific to each card can be found in the appropriate card manual.

CAUTION

Do not touch the card surfaces, connectors, or components to avoid contamination that could degrade card performance.

1. Ensure that the access door on top of the mainframe is fully closed and locked down. (The bottom side of the access door has card guides.)
2. Remove the slot cover from the desired slot.
3. With one hand grasping the card's handle, and the other supporting its weight, line up the card with the card guides in the slot. Ensure that the component side is facing the fan of the mainframe.

4. Slide the card into the mainframe until it is fully seated in the backplane connectors. Finger-tighten the spring-loaded mounting screws at the back of the card to lock it in place.

WARNING

The mounting screws must be secured to ensure a proper chassis ground connection between the card and the mainframe. Failure to properly secure this ground connection may result in personal injury or death due to electric shock.

NOTE

Some cards have connectors that are inaccessible once the card is fully inserted into the mainframe (e.g., the quick disconnect terminal blocks on Model 7071 cards). In these cases, connect wires to the row and column terminal blocks before seating it in the backplane connectors.

The SMB coax jumpers used between Model 7072 cards do not have to be installed before the cards are inserted. Use the access door on top of the mainframe for this purpose. (Because of the access door, the Model 7079 slide rack kit is recommended for rack-mounted units.)

5. To remove a matrix card, first turn off the mainframe and disconnect the line cord. Ensure no voltage is applied from the user's circuit. Remove any internal cabling between cards through the unit's access door. Loosen the spring-loaded mounting screws and pull out the card by its handle.

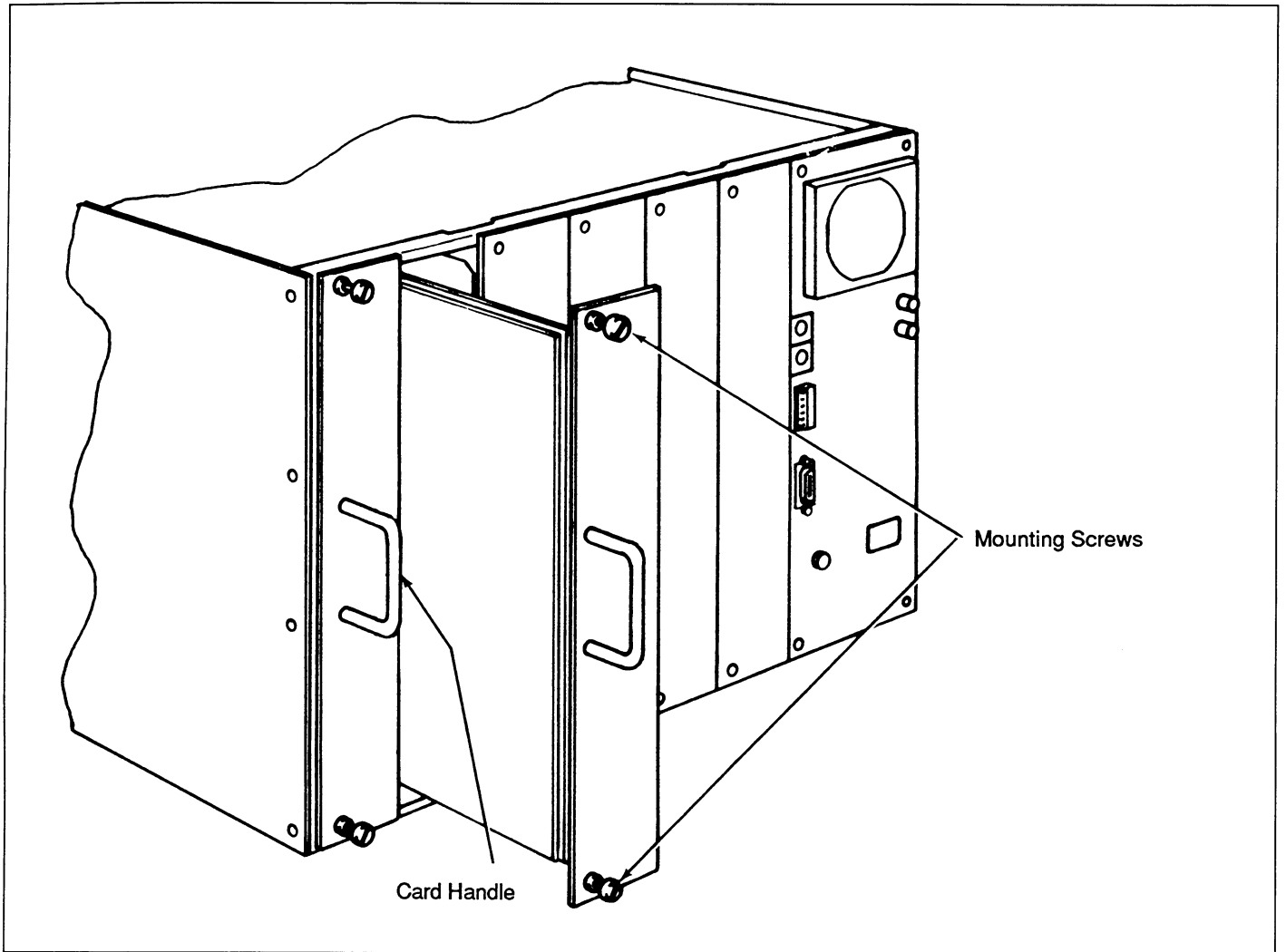


Figure 2-4. Installing a Matrix Card

2.5 CARD CONNECTIONS

Each card designed for the Model 707 is configured as an 8-row by 12-column matrix. The rows are lengthened by adding columns from other cards (of the same model number). Connections for row expansion are usually internal to the mainframe, either through the analog backplane buses or with user-installed jumpers, depending on the card model. Rows can also be expanded across mainframe boundaries, either in a master/slave or stand-alone/stand-alone configuration. In a master/slave configuration of up to five mainframes, the rows are extended to 360 columns maximum. (Paragraph 2.6 describes master/slave expansion.)

Expansion of rows leads to a long, narrow matrix. If your application requires few instruments and many DUTs, connect the instruments to rows (up to 8) and the DUTs to columns (up to 72 with 6 cards). This connection scheme is optimum because the row-column path has only one crosspoint, as shown in Figure 2-5.

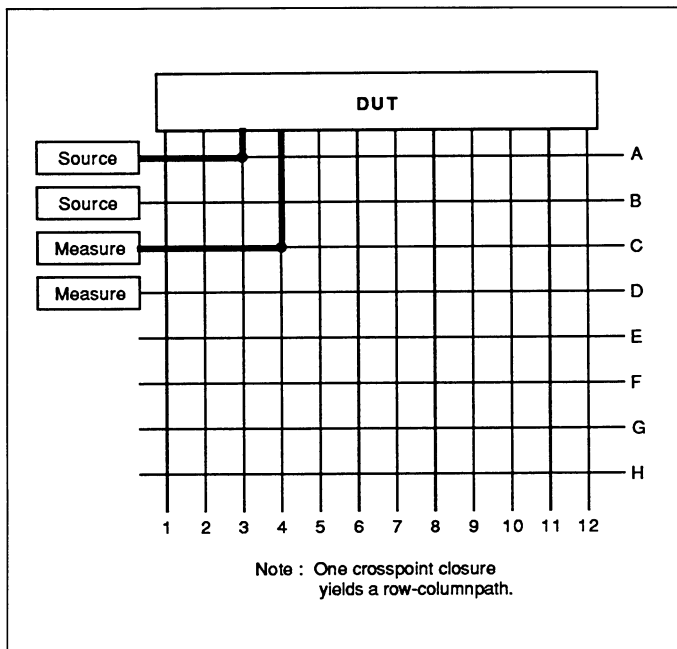


Figure 2-5. Connecting Instruments to Rows

Selecting the row connections for instruments is important with cards designed for multiple applications. Using the Model 7072 as an example, the recommended connections are as follows:

- Rows A and B (low current) - Picoammeters, electrometers.
- Rows C through F (general purpose) - DMMs, sources.
- Rows G and H (C-V characteristics) - C-V analyzers.

An alternate connection scheme of the long, narrow matrix has all connections on the columns, both instruments and DUTs. This is done when the series of tests requires a large number of instruments and DUTs, with only a few signals for each test. As seen in Figure 2-6, with two cards, two crosspoint relays must be closed to complete a path from column-column (a safety benefit when sourcing). Paths with multiple crosspoints have additional path resistance and contact potential than single crosspoint paths.

Crosspoint programming becomes more complex with column-column paths because of the number of possible paths for large matrices and the choice of rows to complete the path. (See Table 2-1.)

The row completion choice for column-column paths on multiple application cards follows the recommendations given previously for row-column paths. That is, with a Model 7072 card, close a crosspoint relay in row A or B for low current applications, row C, D, E, or F for general purpose switching, and row F or G for C-V switching.

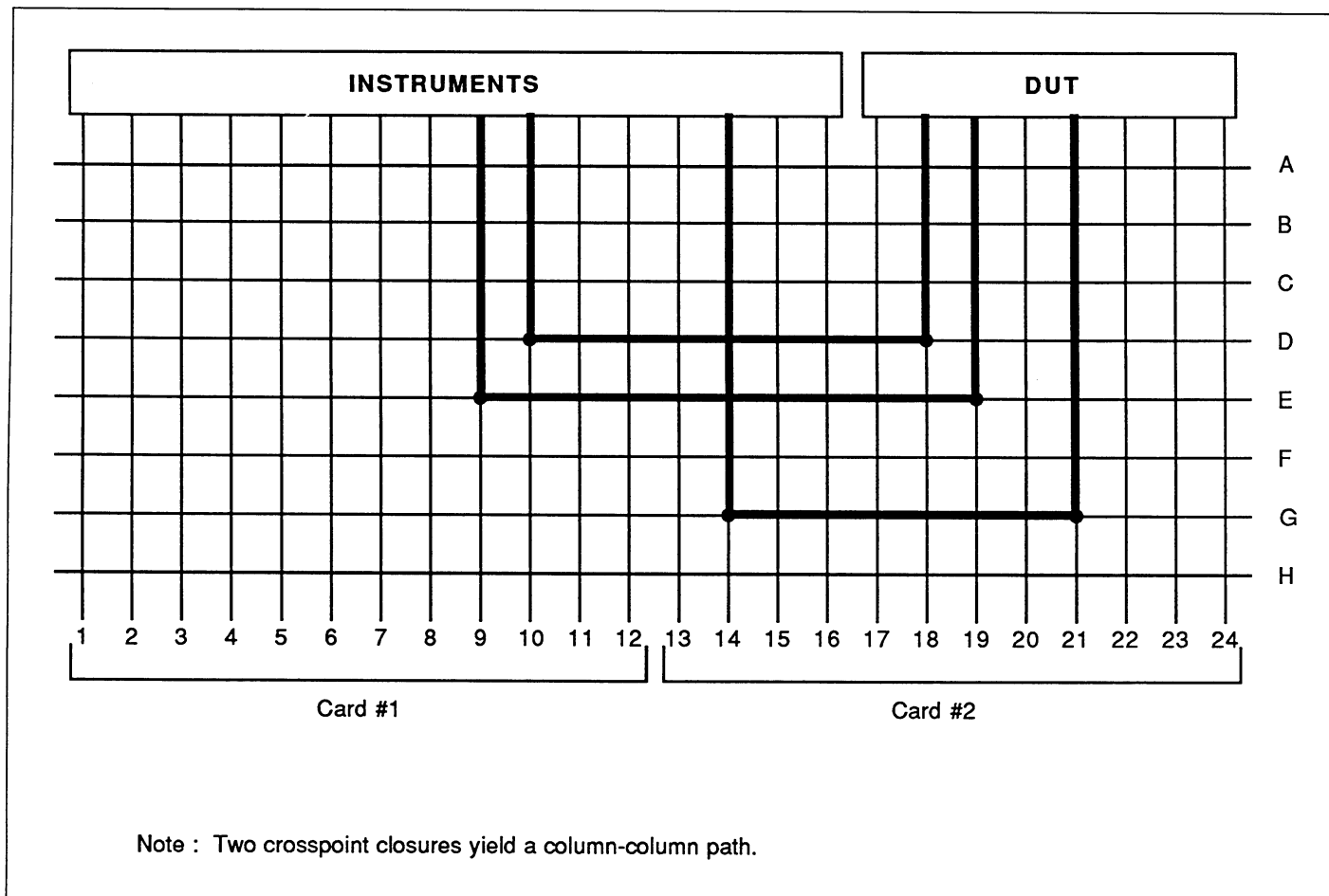


Figure 2-6. Connecting Instruments to Columns

Table 2-1. Row-Column and Column-Column Paths

Connection Scheme	Crosspoints per Path	Possible Paths for 8 Rows by 12 Columns	Possible Paths 8 Rows by 72 Columns
Row-Column	1	96	576
Column-Column	2	66	2556

Notes

1. The crosspoints per path do not take into account any isolator relays that may be present on a card.
2. Each column-column path can be through one of eight rows (e.g., connect column 1 to column 2 by closing A1 and A2, or close B1 and B2, etc.).

2.6 EXPANDING MATRIX SIZE

The 8-row by 12-column matrix cards of the Model 707 mainframe are building blocks for larger matrices. Matrix expansion is accomplished by two methods:

- Internal to the mainframe - The Model 707 backplane automatically extends rows from other like cards. Special purpose rows (not extended by the backplane) are extended by user-installed jumpers between adjacent cards.
- External to the mainframe - A master/slave connection of up to five mainframes is an extension of the rows (up to 8 rows by 360 columns). Also, individual rows and columns can be connected between cards or between mainframes.

The paragraphs that follow explain matrix expansion in detail.

2.6.1 Single Unit Expansion

Expansions to a single unit are either connections internal to the mainframe or external connections of the cards in the same mainframe.

Internal Expansion and Isolation

Internal expansion is done automatically through the Model 707 backplane. Each of the six mainframe slots has three card edge connectors, as shown in Figure 2-7 and described below:

- Upper connectors (J101-J106) - Consists of two analog buses (pins 1-22 and pins 23-86) to expand card rows.
- Middle connectors (J107-J112, 34-pin connectors) - Consists of a third analog bus to expand card rows.
- Lower connectors (J113-J118, 30-pin connectors) - This is a digital bus for mainframe control of the matrix cards.

The **first analog bus** expands signals HI and LO of four rows. The LO of an individual row surrounds the HI path and is between the adjacent rows, as shown in Figure 2-8. These rows are used for 2-pole, general purpose switching.

The **second analog bus** expands signals HI, LO, and GUARD of eight rows. The GUARD of each row surrounds the HI and LO paths and separates adjacent rows. See Figure 2-9. These rows are used for 3-pole, general purpose switching and when the guard signal needs to be switched.

The **third analog bus** expands eight rows of a signal HI path and a common ground (chassis). The common

ground surrounds the HI path and separates adjacent rows, as shown in Figure 2-10. These rows are for 1-pole switching in common ground (high frequency) systems or floating signals (with an additional row for switching low).

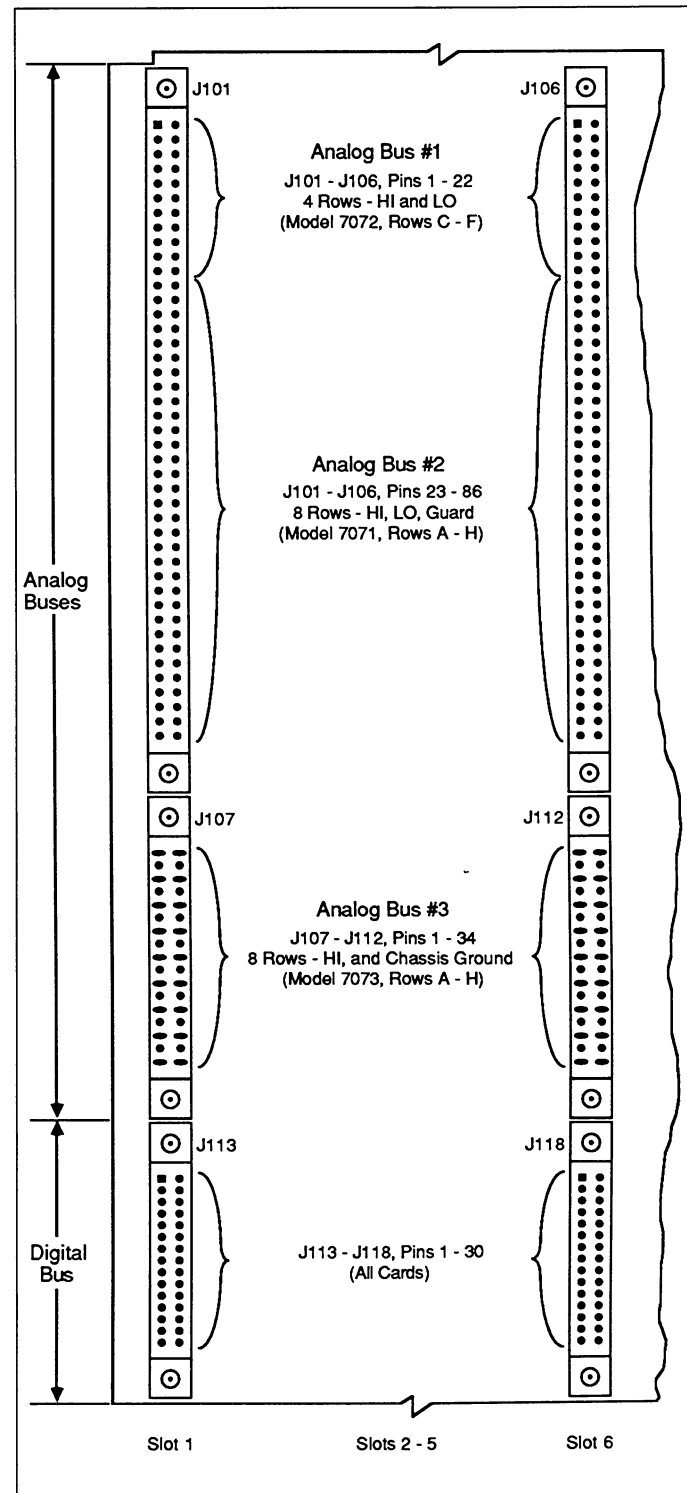


Figure 2-7. Backplane Buses

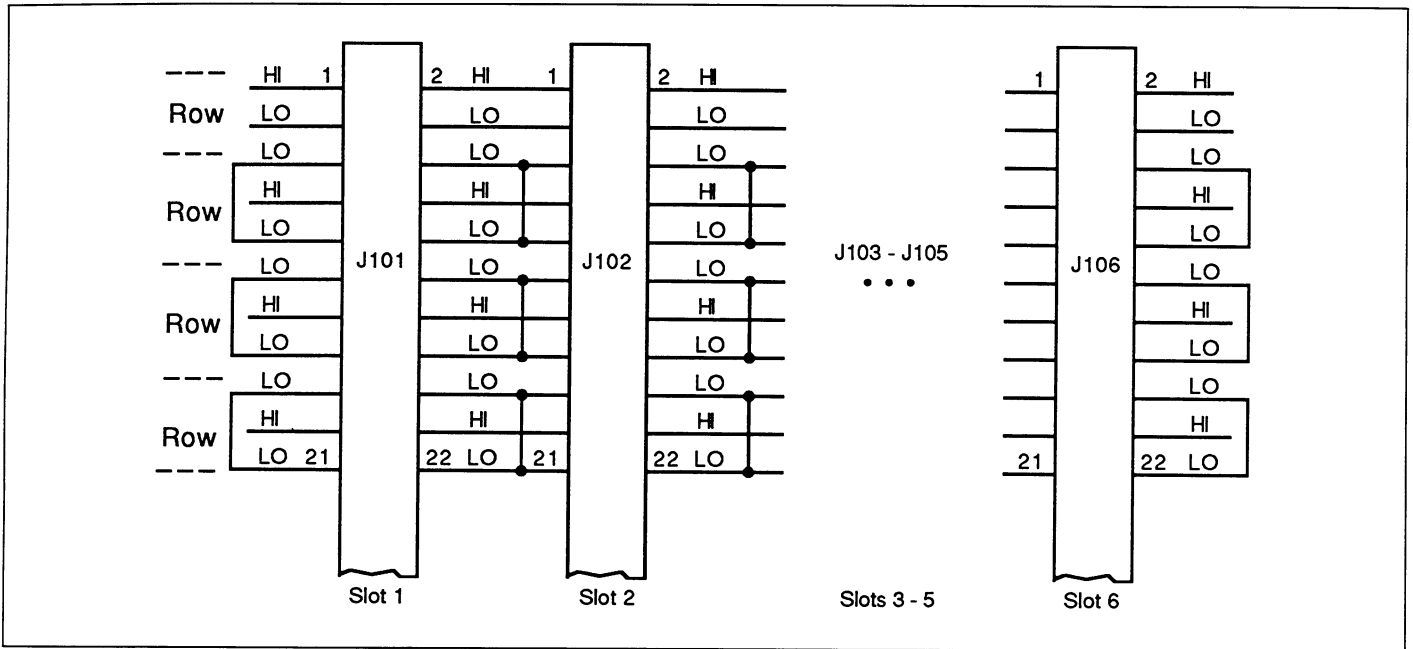


Figure 2-8. Backplane Expansion of Analog Bus #1

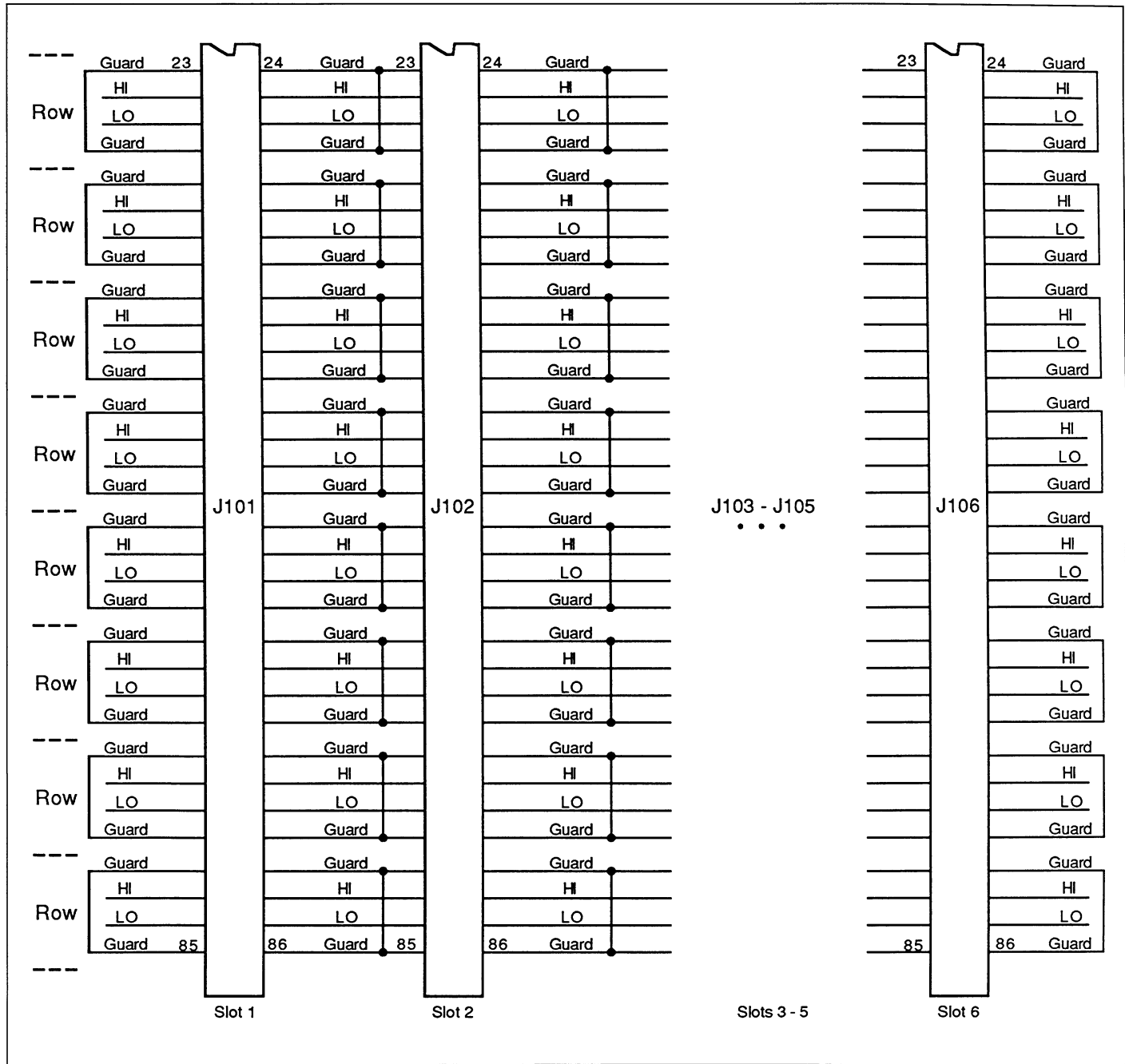


Figure 2-9. Backplane Expansion of Analog Bus #2

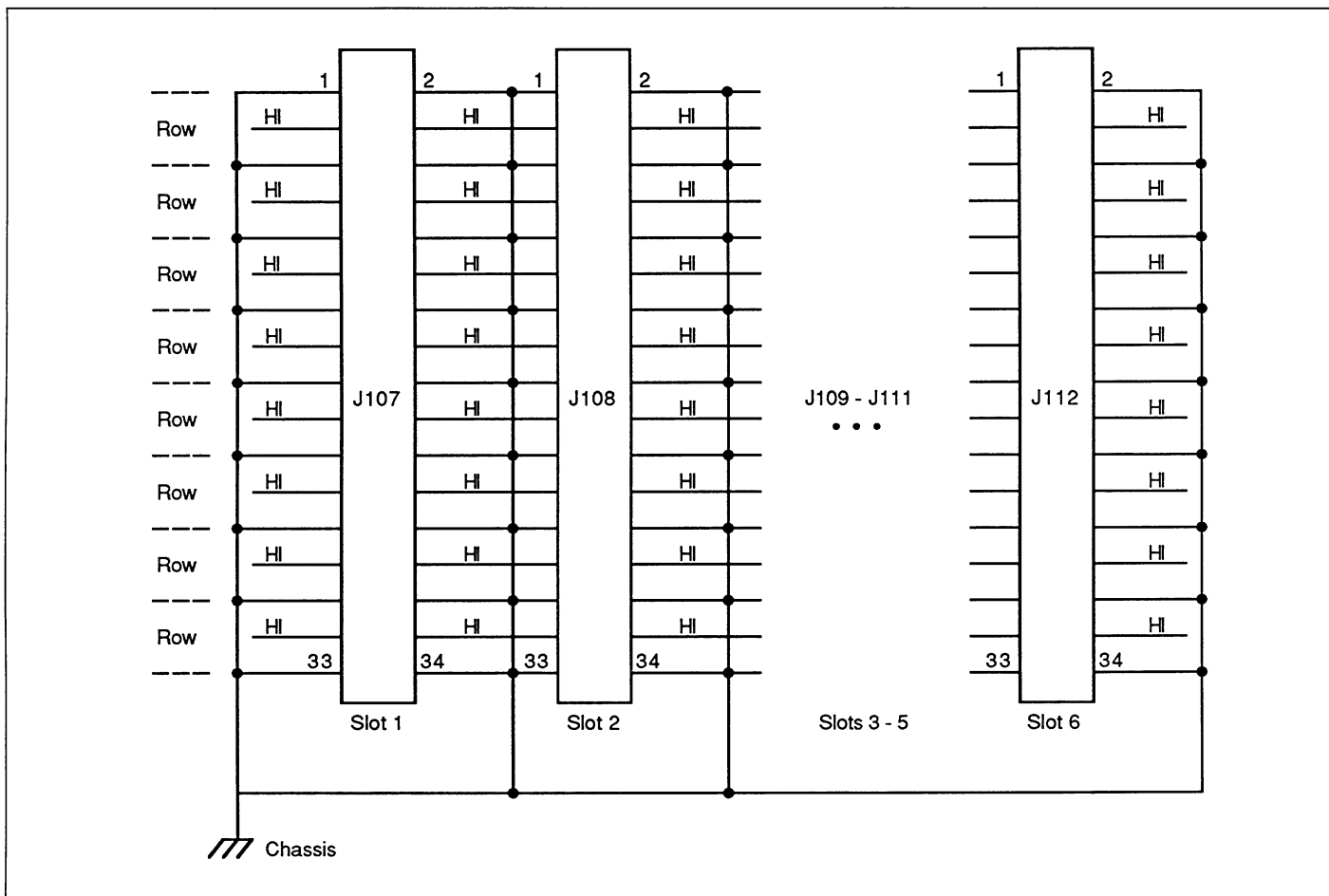


Figure 2-10. Backplane Expansion of Analog Bus #3

Matrix cards for use in the Model 707 have different edge connectors, depending on the signal path configuration of each card model. Analog bus usage by the cards is summarized in Table 2-2. All cards have a connector for the digital bus.

Note that rows A, B, G, and H of Model 7072 cards are expanded with SMB coax jumpers between adjacent cards to lessen signal losses of the low current and C-V rows. The jumpers are internal to the mainframe.

When a mainframe contains different card models, instruments must be connected to each card type because of the differing analog bus usage. An example of this is shown in Figure 2-11.

Table 2-2. Analog Bus Usage

Card	Rows	Analog Backplane Bus
7071	A-H	J101-J106, pins 23-86
7072	C-F	J101-J106, pins 1-22
7073	A-H	J107-J112, pins 1-30

In addition to expanding rows in a mainframe, it is also possible to isolate card rows to some extent. There are factory-installed jumpers on the backplane that can be removed to separate the general purpose rows. These jumpers, which are behind the Model 707 front panel, are between slots 3 and 4 of analog bus #1 (Model 7072 rows C-F) and analog bus #2 (Model 7071 rows A-H). Removing these jumpers effectively separates the mainframe into two 3-slot units. (Jumper removal is described in paragraph 3.13.)

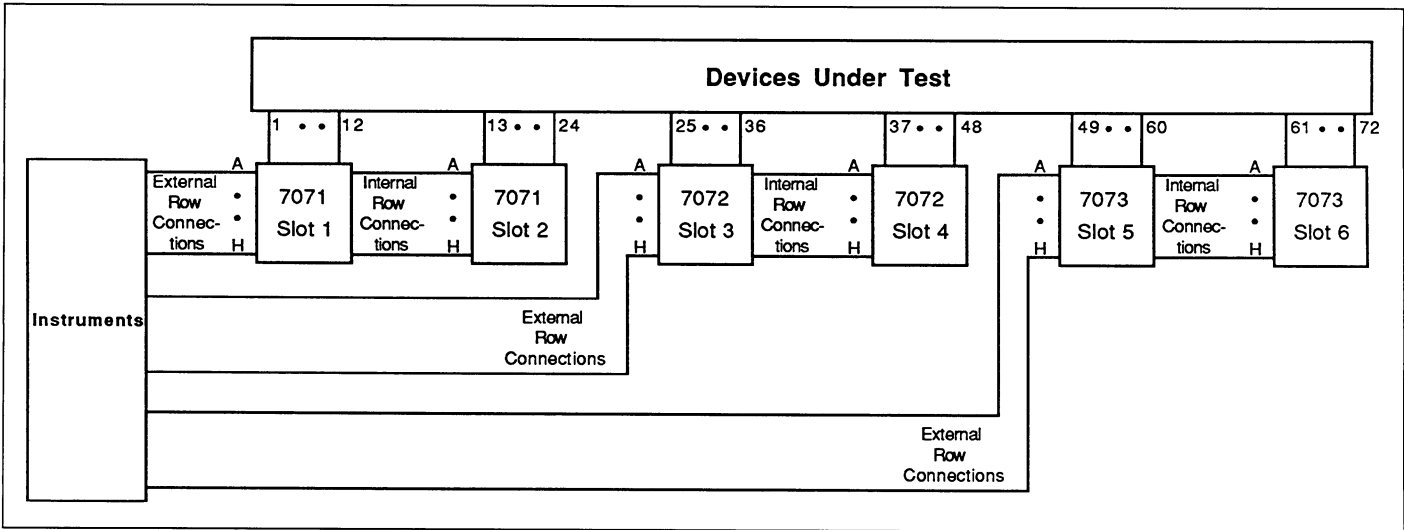


Figure 2-11. Row Connections with Different Card Models

Another isolation method is simply to disconnect the SMB coax jumpers between adjacent Model 7072 cards. These jumpers (for rows A, B, G, and H) are accessed through a door on top of the mainframe.

Jumpers on the Model 7073 card let you selectively expand rows on the backplane to slots on either or both sides, or completely isolate rows from the backplane.

External Expansion

External expansion within a single mainframe is possible with user-installed wiring between rows and columns of like cards. The available accessory cables for external connections are listed in Table 2-3.

CAUTION

Connecting dissimilar cards together often degrades performance of both cards. For example, connecting a Model 7072 to a Model 7073 would degrade low current switching on the 7072 and high frequency switching done with the 7073.

An example of external expansion uses the mainframe as one 6-slot unit and, for some applications, as two 3-slot units. After removing the backplane jumpers, just use external row jumpers between the cards in slots 3 and 4 to select the desired configuration.

External expansion of the cards can also be used to implement a partial matrix. As shown in Figure 2-12 for Model 7071 cards, a column connection is made between the two isolated general purpose backplanes. With the example connections shown, three crosspoints must be closed to source (increasing the safety factor), but only one crosspoint closure is needed to measure (recommended for sensitive instruments).

Table 2-3. Model 707 External Expansion Cables

Model No.	Description	Expansion
7078-KIT 7078-MTC	Mass Terminated Cable Kit Mass Terminated Cable (20 ft.)	7071 rows/columns
7078-TRX-3 7078-TRX-10	3-lug Triax-Triax Cable (3 ft.) 3-lug Triax-Triax Cable (10 ft.)	7072 rows/columns
7051-2 7051-5	BNC-BNC Cable (2 ft.) BNC-BNC Cable (5 ft.)	7073 rows/columns

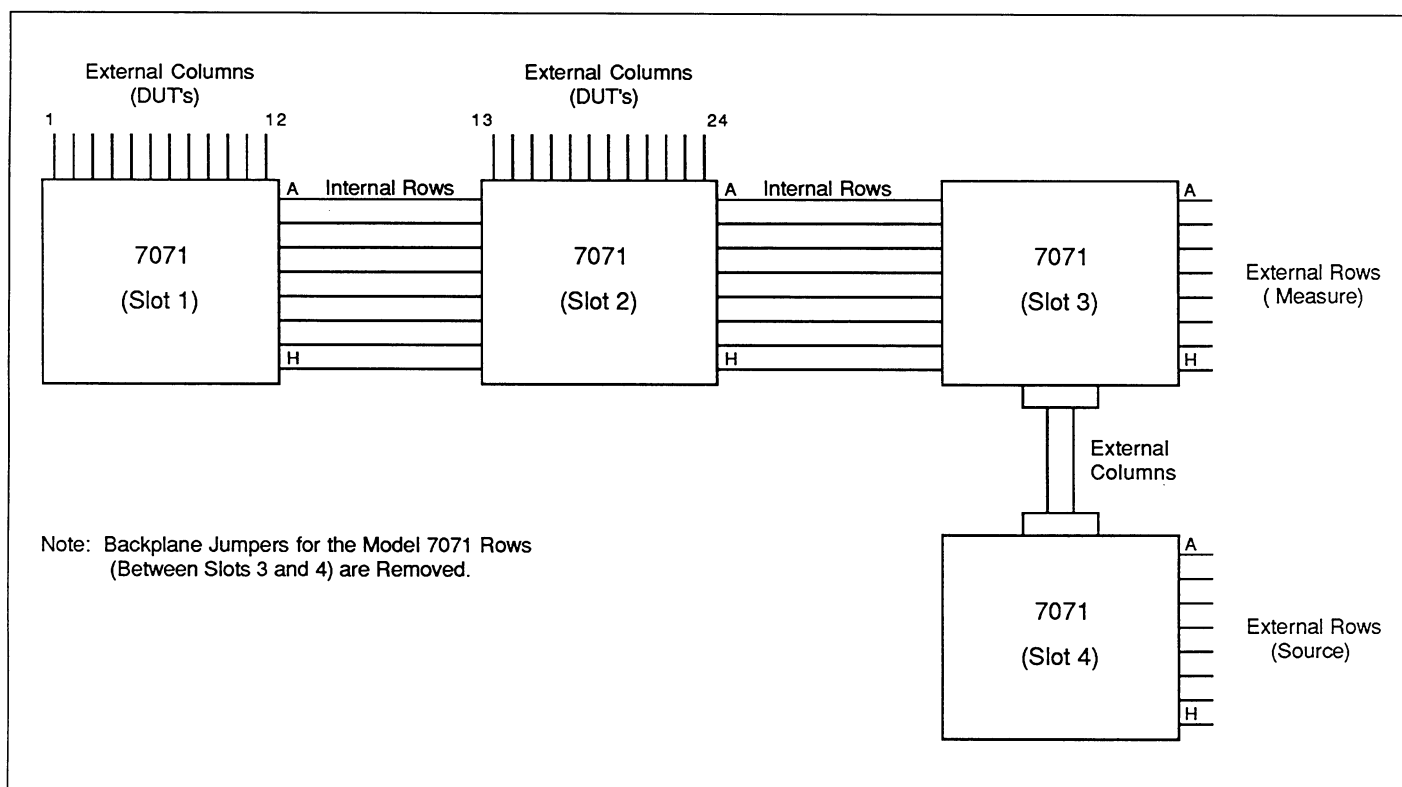


Figure 2-12. Example of Partial Matrix Expansion

2.6.2 Multiple Unit Expansion

Analog Expansion

One method to expand a matrix across mainframe boundaries is to connect cards of separate stand-alone units, either by rows or columns. Each unit has a different IEEE-488 bus address and is programmed independently. The additional digital I/O ports are available for programming.

The analog backplane buses can be expanded between separate mainframes. As seen in Figure 2-13, a mass terminated cable can be used to extend Model 7071 card rows (J101-J106, pins 23-86). This configuration of two stand-alone units is an 8-row by 144-column general purpose matrix, with each mainframe programmed independently.

As an example of expansion by columns, consider a 16-row by 72-column matrix of Model 7072 cards. This can be done by connecting all columns of card #1 in one unit to all columns of card #1 in another unit, and so on for all cards. (Triax T-adapters are used in this example to connect instruments or devices to the columns.)

Analog Expansion and Control Expansion

Another method of expanding a matrix with multiple mainframes is to connect up to five units in a master/slave configuration. This is done by connecting the rows of like

cards in separate units, as shown previously in Figure 2-13, but also by connecting the units in a closed loop of DIN cables for communication and control. A master/slave system configuration appears as one unit with expanded card capacity. That is, only the master unit is addressed by the IEEE-488 bus controller.

A master/slave configuration extends matrix rows yielding a long, narrow matrix. Figure 2-14 shows the connections between two units having Model 7071 cards. With five units, the maximum matrix size is 8 rows by 360 columns. Figure 2-15 shows the column assignments for the maximum configuration.

If the mainframes of a master/slave configuration contain different card models, group like cards as much as possible. This will reduce the need to extend the analog buses with external cables.

In some cases, external row expansion is not necessary at all (e.g., one unit only with Model 7071 cards, the second unit only with Model 7072 cards).

The example of Figure 2-16 shows the row expansion, but not the closed loop of DIN cables for master/slave communication and control. The figure shown is actually three matrices (one 8-row by 72-column and two 8-row by 144-column) that are programmed as one 8-row by 360-column matrix.

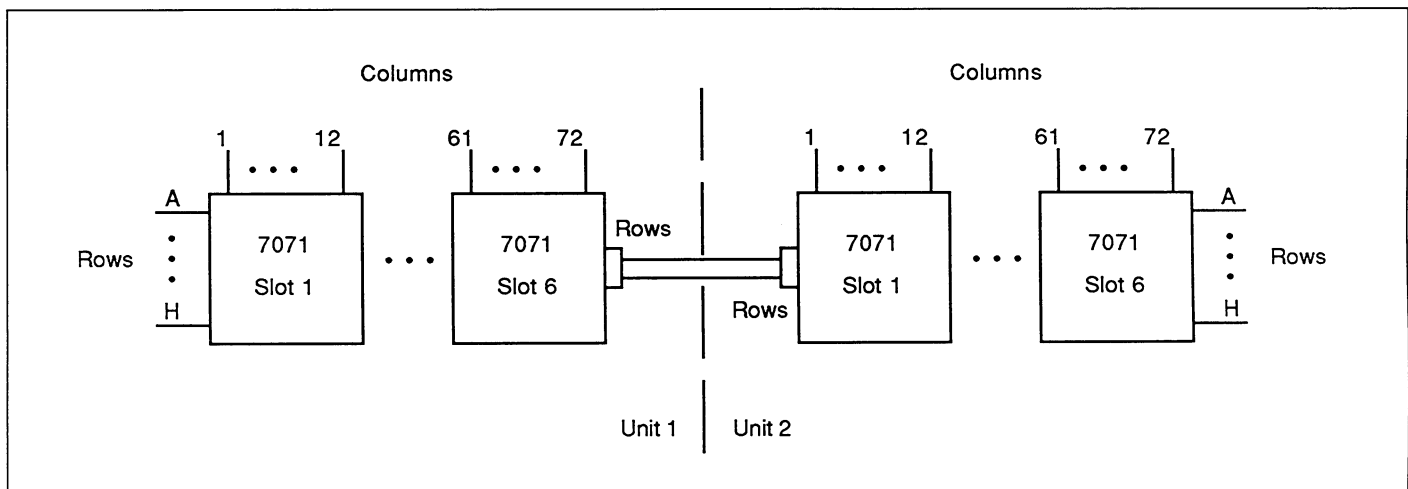


Figure 2-13. Row Connections of Stand-alone Units

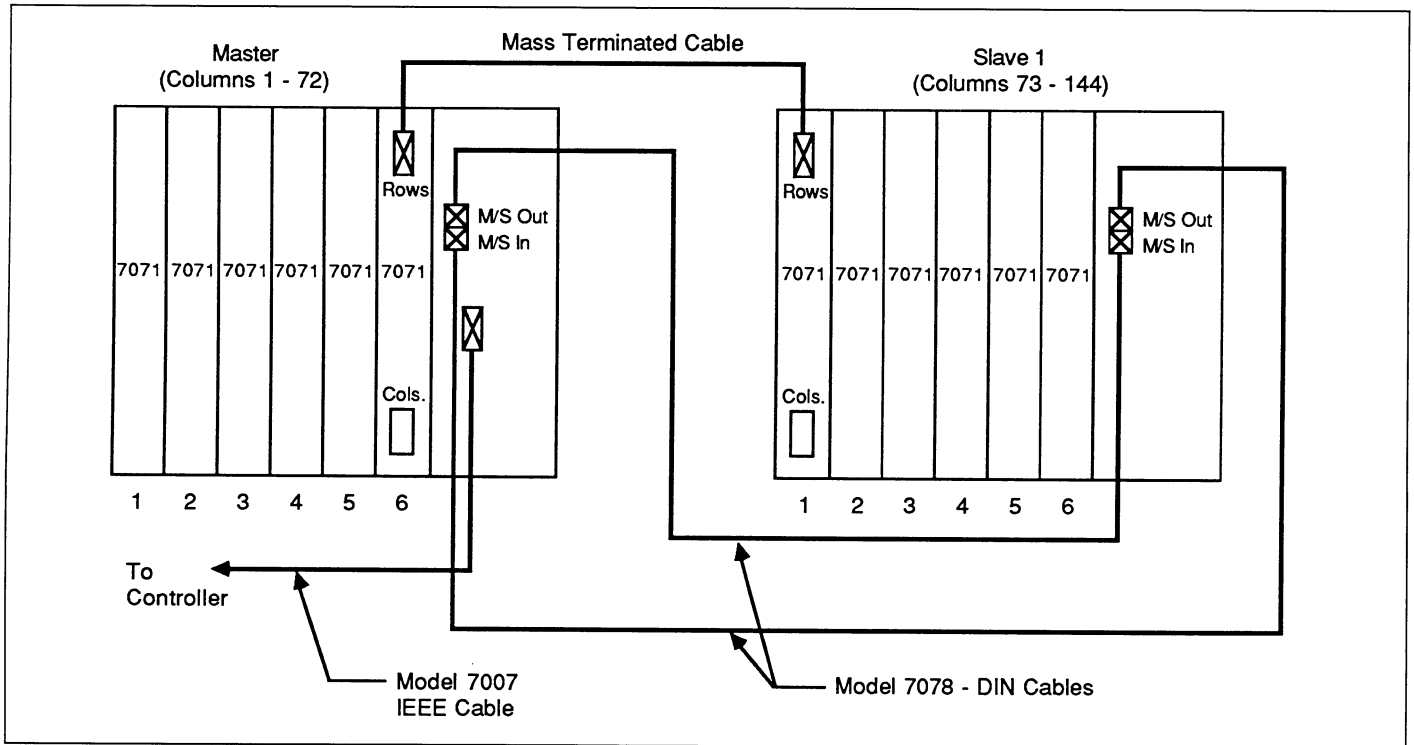


Figure 2-14. Example of Master/Slave Interconnect Cables

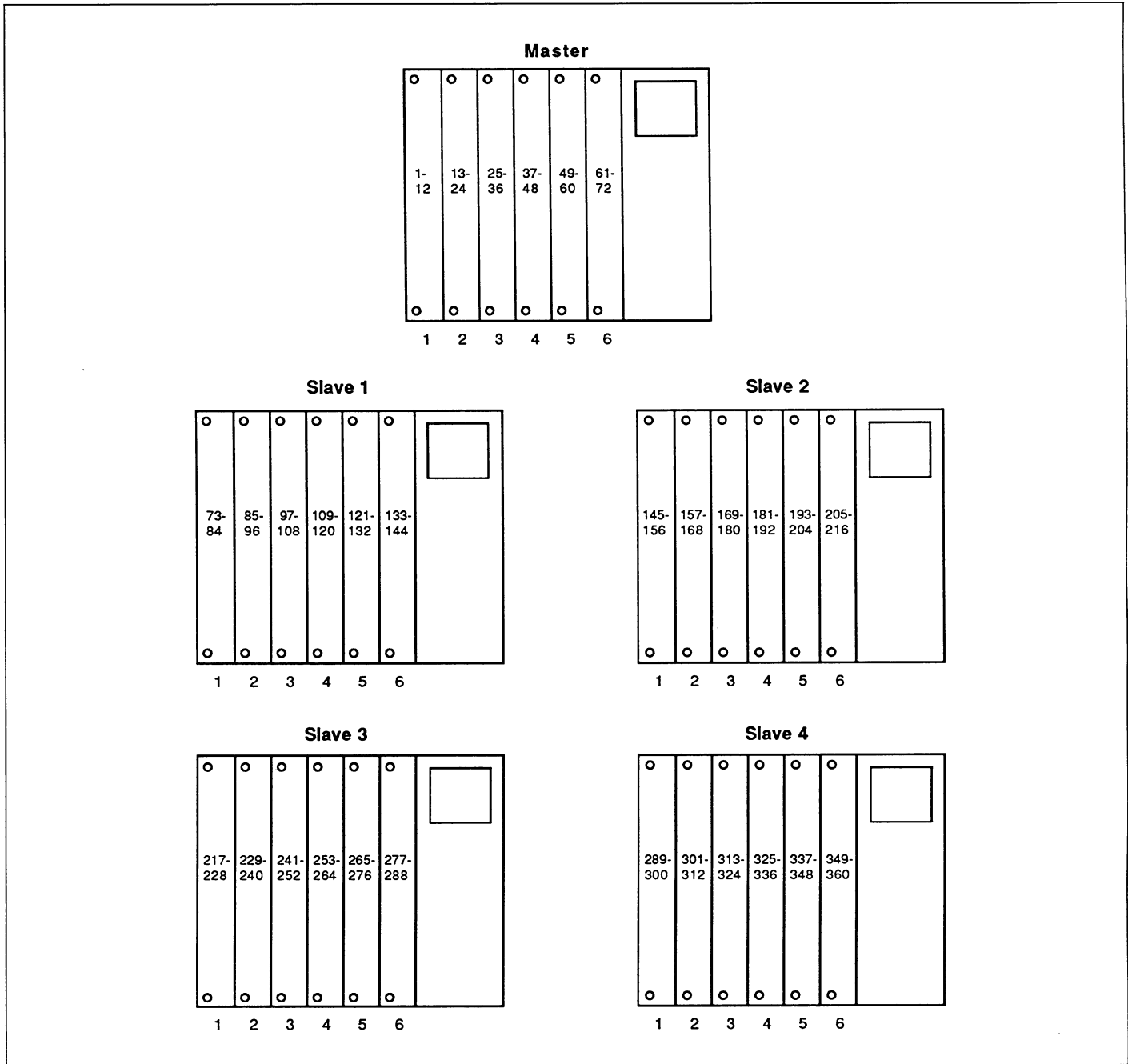


Figure 2-15. Master/Slave Column Locations

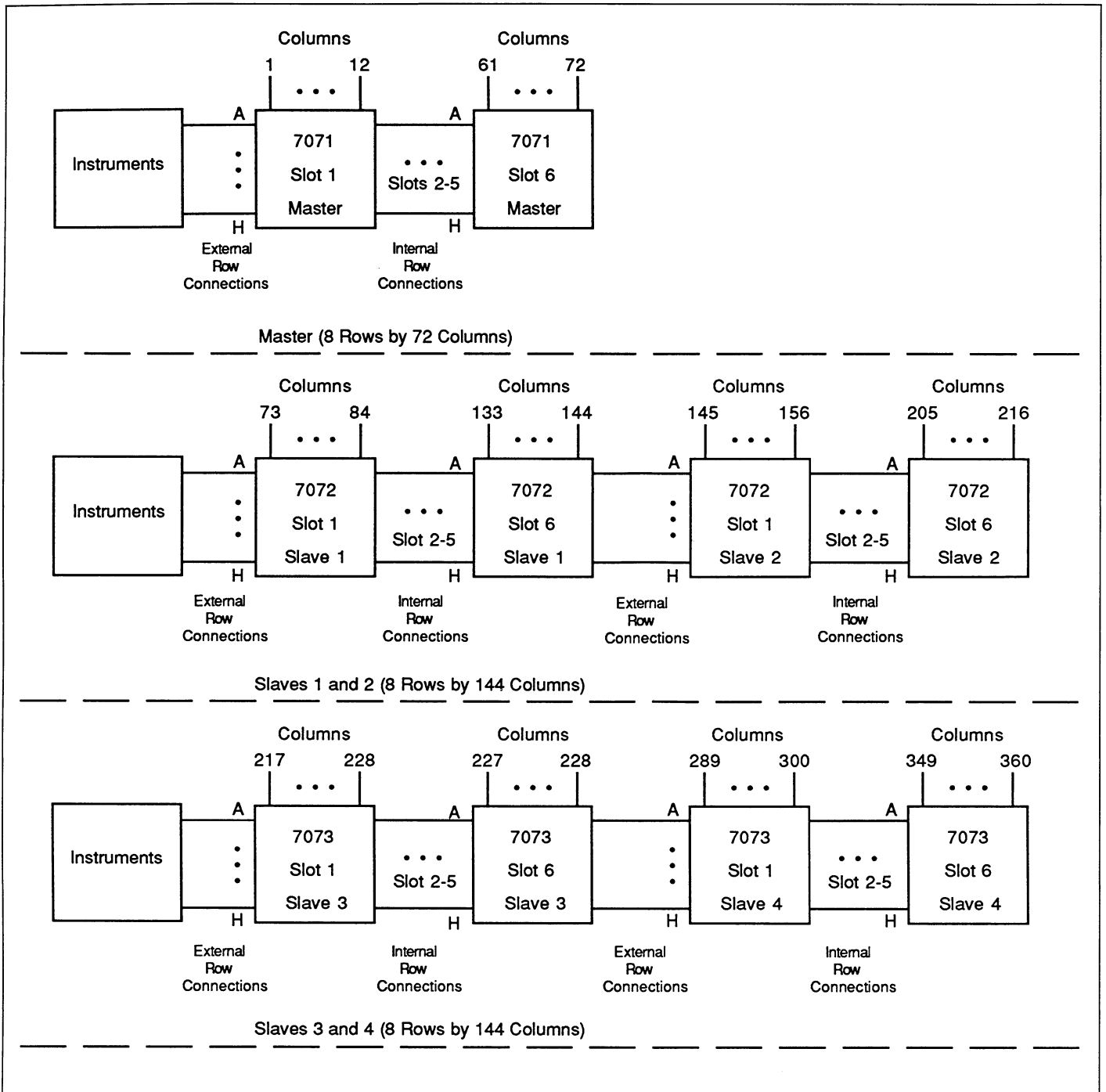


Figure 2-16. Example of Master/Slave Row Expansion

2.6.3 System Expansion Issues

Matrix expansion by Model 707 mainframes affects system specifications and speed. The extent depends on the size and configuration of the switching system.

Within a mainframe, internal row expansion decreases isolation among like cards and increases offset current. Isolation relays (on the Model 7072), backplane jumpers (for general purpose rows), and SMB coax jumpers (on the Model 7072) help lessen these effects.

Expansion of units along rows or columns also degrades the isolation and offset current specifications because of the number of parallel paths and relays on each signal line.

There are several issues that affect system speed, among them are:

- Relay settling time - Each matrix card has a predefined relay settling time (3msec for the 7071, 15msec for the 7072 and 7073). When card types are mixed in a system, the longest settling time is in effect.
- Bus communication - A master/slave setup responds slower to bus commands because all communication is through the master unit and the data transmission among the units is verified with handshaking. Table 2-4 compares some typical times.

Table 2-4. Response Time Comparisons

Action	Stand-alone	Master with 4 Slaves
Respond to bus command to close single relay	<15ms	<55ms
Download one setup to 707	60ms typical	—

2.6.4 Documenting System Configuration

With the connection flexibility of the matrix topology and the expansion/isolation options of the Model 707, it is important to document the system configuration.

An example table for tracking card connections and expansion is shown in Table 2-5. Use the top portion of the table to note system operation and size, the FROM/TO portion to list card row and column connections, and the lower portion for notes concerning expansion and operation (e.g., make/break and break/make rows).

Table 2-5. Model 707 Card Configuration

Slot _____		Model _____	
Mainframe:	Stand-alone _____	Master _____	
	Slave1 _____	Slave2 _____	Slave3 _____ Slave4 _____
System Size:	____ rows _____	columns _____	IEEE Address _____
FROM (Instrument connection or DUT pin)	External Card Connection	TO (Instrument connection or DUT pin)	
	Row A		
	B		
	C		
	D		
	E		
	F		
	G		
	H		
	Column 1		
	2		
	3		
	4		
	5		
	6		
	7		
	8		
	9		
	10		
	11		
	12		
Expansion:			
—Backplane bus (rows)		—SMB coax jumpers (rows)	
—Point to point wiring (rows/cols.)		—Mass terminated cable (rows/cols.)	
—BNC coax cable (rows/columns)		—Triax cable (rows/cols.)	
Notes:			

2.7 BASIC SWITCHING OPERATION

The following paragraphs will take you through a simple, general, step-by-step procedure to edit a matrix setup, store it in memory, and send the setup to the relays. Although the steps are described with front panel operations, the procedure can be performed over the IEEE-488 bus. (An example program showing this is given in paragraph 4.2.) Even with no instruments or DUTs connected to the matrix cards, the procedure will still have instructional benefits.

See Section 3 for more operation details, including master/slave configurations.

2.7.1 Powerup

Check that the instrument is set to correspond to the available line voltage. The line voltage switch is located on the rear panel. If the switch is set to the correct position, connect the instrument to a grounded AC outlet using the supplied power cable and turn on the unit.

CAUTION

If the switch setting does not correspond to the available line power, do not change the switch setting and power up the unit as the line fuse will probably blow. Instead, proceed to paragraph 6.2 for the line voltage selection procedure.

The Model 707 will perform a powerup self-test to check ROM, RAM, card configuration, stored setups, master/slave loop, indicators, and displays. It will then display the software revision level and IEEE-488 bus address.

When the self-test has completed, the Model 707 is configured with:

- All relays opened.
- RELAYS indicator lit (crosspoint display shows current relay setup).
- RELAY STEP to 000 (a pseudo setup memory that is cleared at powerup and sent to the relays).
- MEMORY STEP to 001.

Other powerup defaults are detailed in paragraph 3.3.

2.7.2 Selecting Make/Break and Break/Make Rows

Select make-before-break, break-before-make, or don't care operation for the rows. The selections will be in effect for all relay switching, even if a stored setup is not used. (As a general rule, use make/break operation for current sources and break/make for voltage sources.)

Use the data entry keys to select a row, then press MAKE/BREAK or BREAK/MAKE to toggle the state. (Selecting one state for a row de-selects it for the other.) This operation can also be performed with the light pen by using it to turn on/off the MAKE/BREAK and BREAK/MAKE LEDs.

2.7.3 Modifying a Relay Setup

Perform the following steps to edit a matrix setup.

Step 1: Select a Stored Setup

If you want to modify setup #1, just press the MEMORY key. The MEMORY indicator will light. To select another setup (up to location 100), use the numeric data entry keys (leading zeros are not necessary), then press the MEMORY key.

Step 2: Modify the Displayed Setup

Use the data entry keys to select a crosspoint address (A1 through H72), then press the OPEN or CLOSE key. Keystrokes will be shown on the alphanumeric display and the CROSSPOINT DISPLAY MODIFIED indicator will light.

If you have the optional light pen, toggle the state of a crosspoint LED by holding the light pen perpendicular to and touching the front panel overlay and pressing the light pen button.

Continue editing with the front panel keys or light pen until the crosspoint display shows the desired configuration.

2.7.4 Storing Setup and Sending to Relays

The following steps detail front panel operations necessary to store and use the modified setup data.

Step 1: Storing Setup in Memory

To store the modified setup at the location shown in the MEMORY STEP field, just press the COPY DISPLAY→MEMORY key. This action overwrites the old setup data at that location with the newly modified setup.

To select a different memory location, key in a valid location number, then press the COPY DISPLAY→MEMORY key. The MEMORY STEP field is set to the new location.

Step 2A: Sending Setup to Relays

To make the newly modified setup the current relay setup, just press the COPY DISPLAY→RELAYS key. The relay states will be changed to reflect the modified setup data. If the MEMORY LED is lit, the RELAY STEP field will be set equal to the MEMORY STEP field. In effect, this copies a setup from memory to the relays.

Step 2B: Triggering Setup to Relays

If you modified setup #1 and restored it to memory at the same location, a single trigger will copy the setup to the relays. Do this by pressing the trigger SOURCE key, scrolling to the "TRIG ON KEY" display and pressing ENTER. Then press the trigger ENABLE key. Pressing the trigger MANUAL key will copy setup #1 to the relays and set the RELAY STEP field to 001.

SECTION 3

Operation

3.1 INTRODUCTION

This section contains a complete, detailed description of each front and rear panel aspect of the Model 707. The section is arranged as follows:

- 3.2 **Setup Data Paths:** Describes the paths for setup data within a Model 707 and to/from a bus controller.
- 3.3 **Power-up Procedure:** Details how to connect the instrument to line power and turn it on, including power-up self-test and default conditions.
- 3.4 **Displays and Messages:** Covers the uses of the alphanumeric display, crosspoint display, and make/break and break/make row LEDs. Also lists display messages that may be encountered during front panel operations.
- 3.5 **Selecting Crosspoint Display:** Describes how to select the source of relay setup data (current relay setup or a stored setup).
- 3.6 **Modifying Crosspoint Display:** Discusses the operations to open/close crosspoint display LEDs.
- 3.7 **Copying Crosspoint Display:** Describes copying a display to the relays and to memory.
- 3.8 **Inserting and Deleting Stored Setups:** Covers how to insert a blank setup in memory and how to delete a stored setup from memory.
- 3.9 **Menu Operations:** Details menu item selection and operation, including digital I/O, external trigger, matrix ready, master/slave, IEEE-488 bus address, relay settling time, card identification, self-test, and factory defaults.
- 3.10 **Selecting Switching Parameters:** Covers the programmed settling time and make/break, break/make operations.
- 3.11 **Triggering:** Details selecting the trigger source and describes the operation of the rear panel trigger input and output jacks.
- 3.12 **Resetting:** Discusses the reset operation of the Model 707.

- 3.13 **Backplane Jumpers:** Describes how to remove backplane jumpers.

3.2 SETUP DATA PATHS

The design of the Model 707 is optimized for high speed switching of relay setups for matrices with a maximum of 8 rows by 72 columns (one unit) to 8 rows by 360 columns (five units). If no rows are selected for make/break or break/make operation, previously stored setups can be switched to the relays at a rate of up to 200 setups per second.

Besides the triggering of stored setup data to the relays, setup data can be routed to/from the sources and destinations shown in Figure 3-1. The data paths are selected by the front panel or IEEE-488 bus operations listed in Table 3-1.

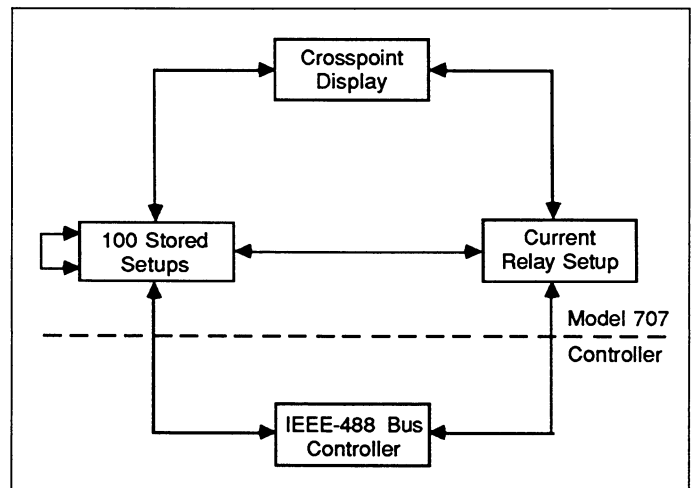


Figure 3-1. Paths for Relay Setup Data

In addition to other front and rear panel operations, this section describes setup data transfers that are performed from the Model 707 front panel. Section 4 will describe the bus operations that transfer setup data.

Table 3-1. Setup Data Paths

Setup Data Path	Action Required
Display → Memory Display → Relays	Front panel keystroke Front panel keystroke or an automatic copy (Note 1)
Memory → Display Memory → Relays Memory → Controller Memory → Memory	Front panel keystroke or an automatic operation (Note 2) Bus command or any valid trigger Bus command Bus command
Relays → Display Relays → Memory Relays → Controller	Front panel keystroke or an automatic operation (Note 3) Bus command Bus command
Controller → Memory Controller → Relays	Bus command Bus command

Notes:

1. Generation of the automatic copy is selected by a front panel key. (AUTOMATIC LED is lit.)
2. The automatic operation is generated if the displayed setup has been changed by a bus command and has not been modified from the front panel. (MEMORY LED is lit.)
3. The automatic operation is generated if the displayed setup has been changed by a trigger or bus command and has not been modified from the front panel. (RELAYS LED is lit.)
4. Controller modifications to setups are reflected on the crosspoint display if the affected setup is currently being displayed.
5. Front panel keystrokes can be generated by bus commands.

3.3 POWER-UP PROCEDURE

The steps in the following paragraphs take you through the basic procedures for selecting the line voltage, connecting the instrument to line power, and turning on the instrument.

3.3.1 Line Voltage Selection

The Model 707 can be operated from either 105-125V or 210-250V, 50 or 60Hz sources. The transformer can be rewired at the factory for 90-110V and 180-220V ranges. The instrument was shipped from the factory set for an operating voltage that corresponds to the normally available line voltage in your area.

CAUTION

Do not operate the instrument on a line voltage outside the indicated range, or instrument damage can occur.

To check the line voltage setting, look at the position of the slide switch located on the rear panel (see Figure 3-2). If the switch is in the wrong position, the line fuse will probably have to be replaced with one that has a different rating. Refer to paragraph 6.2 in the maintenance section for the line voltage selection procedure.

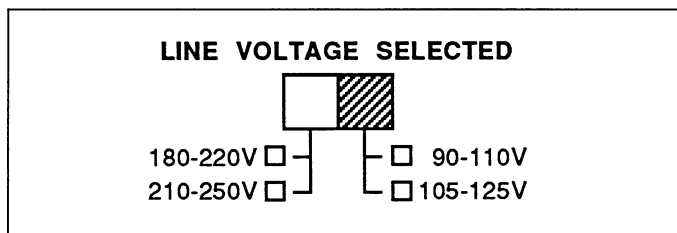


Figure 3-2. Line Voltage Switch

3.3.2 Line Power Connections

Using the supplied power cord, connect the instrument to an appropriate 50 or 60Hz ac power source. The female end of the cord connects to the ac receptacle on the rear panel of the instrument. The other end of the cord should be connected to a grounded ac outlet.

WARNING

The Model 707 must be connected to a grounded outlet to maintain continued protection against possible shock hazards. Failure to use a grounded outlet may result in personal injury or death due to electric shock.

3.3.3 Power Switch

To turn on the power, simply push in the front panel POWER switch. Power is on when the switch is at the inner (1) position. To turn power off, press POWER a second time.

3.3.4 Power-up Self-test and Messages

During the power-up cycle, the instrument performs the following tests. The first five operations are transparent to the user unless an error occurs.

1. A checksum test is performed on ROM and a read/write test on RAM. If an error is found, the self-test continues and the unit displays either ROM FAIL or RAM FAIL when the test has completed. You can override either type of error with a front panel keypress. The Model 707 will attempt normal operation.

NOTE

If a problem develops while the instrument is still under warranty (less than one year from shipment date), return it to Keithley Instruments, Inc. for repair. For units out of warranty, refer to Section 6, Maintenance.

2. The Model 707 reads identity information from each card and performs a checksum test on the data. If the checksum test fails on one or more matrix cards, the instrument displays CARD ID ERROR and lights all cross-point LEDs of that card. Any keypress will allow the unit to continue. An empty slot will not produce an error.
3. A checksum test is performed on all setups in memory. If the instrument detects a checksum error in one or more stored setups, it displays the message SETUP ERROR and clears the crosspoints bits of the setup(s) in error. The message remains displayed until a key is pressed.

NOTE

The SETUP ERROR message may be an indication of a low battery. Cycle power off and on. If the message reappears, see paragraph 6.6 for battery replacement procedure.

4. The present card configuration (i.e., which cards are installed in which slots) is compared with the unit's previous configuration. If there is a change, the 100 setups in memory are reformatted. (The front panel display is blanked out during this time.) Crosspoint closures are not affected, just the way a setup is stored for the different cards.
5. If the unit was previously programmed as a stand-alone or slave unit, it powers up as a stand-alone. If the unit was programmed as a master the last time it was on, it checks for additional units in a serial looped configuration and tries to make them slave units. (Refer to paragraph 3.3.6 for information concerning turning on a master/slave configuration.)
 The message M/S ERROR is displayed if there is not a closed loop (the Model 707 can be looped back to itself). Any keypress or IEEE-488 bus operation will allow the unit to continue as a stand-alone unit.
6. The instrument performs the display test, where it lights all segments of the alphanumeric display, all crosspoint LEDs, and all other LED indicators. Then it briefly

displays the software revision level and the programmed primary IEEE-488 address as in this example:

A01 IEEE 18

Knowing the software revision level is useful when discussing problems with Keithley Instruments. In this example, the factory default primary IEEE-488 address is displayed. The actual address depends on the programmed value.

3.3.5 Power-up Configuration

After the power-up tests and display messages are completed, the Model 707 assumes specific operating states:

- All relays are opened.
- The RELAYS indicator is lit (crosspoint display shows current relay setup).
- The RELAY STEP field is set to 000 and the MEMORY STEP field is set to 001.
- Triggers are disabled.

Table 3-2 summarizes the power-up configuration for the unit. The entire power-up process takes approximately five seconds to complete.

Table 3-2. Power-up, Reset, and Factory Defaults

Parameter	Power-up/Reset Default	Factory Default
Relays	All opened	All opened
Stored Setups	Unchanged	All cleared
RELAY STEP	000	000
MEMORY STEP	001	001
Digital Output	000	000
External Trigger	Falling edge	Falling edge
Matrix Ready	Active low	Active low
Master/Slave	Unchanged	Unchanged
IEEE-488 Address	Unchanged	18
Programmed Settling Time	0msec	0msec
Make/Break Rows	Unchanged	None selected
Break/Make Rows	Unchanged	None selected
Trigger Enable	Disabled	Disabled
Trigger Source	External	External

3.3.6 Master/Slave Power-up

The power-up sequence for Model 707 mainframes can be summarized as follows:

Each unit connected in the master/slave loop displays M/S LOOP DOWN until all units are powered up.

- Units previously programmed as stand-alones or slaves power up as stand-alones.
- A unit previously programmed as a master powers up as a master and tries to initiate a loop connection. If it is successful, other units in the loop become slaves. If it is not successful, the message M/S ERROR is displayed and the unit reverts to stand-alone operation.

To connect and power up a master/slave configuration for the first time, follow these steps:

1. Connect up to five mainframes in a daisy chain (Master/Slave Out of one unit to Master/Slave In of next unit) as previously shown in Figure 2-15 for two units.
2. Power up each unit. Since there is no master in the loop as yet, all units will power up as stand-alones. The units will display the message M/S LOOP DOWN until all are turned on.
3. From the front panel of the desired master unit, press the MENU key until the alphanumeric display shows:
STANDALONE

Press one of the SCROLL keys to change the display to MASTER, then press ENTER. This action initiates a loop connection, making this unit the master and the other units slaves, and exits menu mode.

During subsequent power-ups of master/slave configura-

tions, all connected mainframes wait for all units to get power before initializing. Thus, it is not necessary to turn on the master unit last.

CAUTION

When it is necessary to cycle power on a slave unit, turn off all units in the master/slave configuration. This procedure prevents the open communication and control loop from putting the slave unit in an undesirable state.

3.4 DISPLAYS AND MESSAGES

3.4.1 Alphanumeric Display

The alphanumeric display is a 14-character display that can show error messages, informational messages (e.g., menu item parameters), last setup sent to relays, last setup recalled to crosspoint display, trigger source, programmed settling time, and alphanumeric key presses. See Figure 3-3.

The 3-digit RELAY STEP field of the alphanumeric display shows the location of the last setup sent from memory to the relays. A trigger causes the next setup (RELAY STEP +1) to be sent to the relays.

The 3-digit MEMORY STEP field of the alphanumeric display shows the location of the last setup recalled from memory to the crosspoint display.

The 6-digit data entry scratchpad field reflects alphanumeric key presses by the user, such as row and column addresses and setup locations.

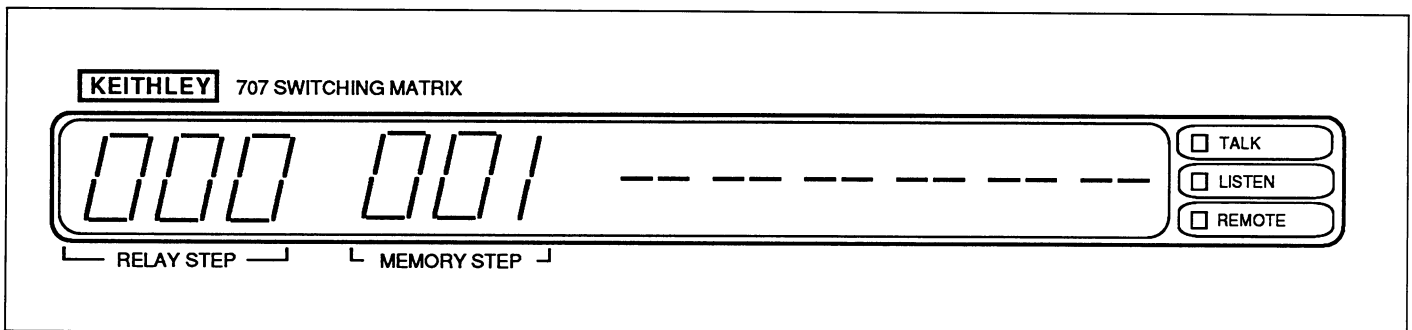


Figure 3-3. Alphanumeric Display

3.4.2 Display Messages

During Model 707 operation and programming, you will encounter a number of front panel messages on the alphanumeric display. Typical messages will be either of error or informational variety, as discussed in the following paragraphs.

Error Messages

Error messages are divided into two categories: those which stay on the display until a keypress or some other operation changes the display, and those which appear for two seconds and then the display returns to its previous state.

Table 3-3 lists Model 707 error messages. Many of these messages are also covered in pertinent paragraphs of the manual. Where applicable, the necessary corrective action is also given in the table.

Informational Messages

Informational messages are included as a programming aid. No corrective action is necessary, but you still may be required to enter a parameter at the prompt. Table 3-4 lists Model 707 informational messages. Again, most of these are covered in other parts of the manual.

Table 3-3. Error Messages

Message	Description	Corrective Action
CARD ID ERROR*	Checksum test failed on one or more matrix cards.	Remove card identified by all crosspoint LEDs lit.
IDDC	Invalid device-dependent command.	Send only valid commands (see Section 4).
IDDCO	Invalid device-dependent command option.	Send only valid command options (see Section 4).
INVALID INPUT	Invalid crosspoint address, setup location, make/break or break/make row, or parameter out of range.	Enter valid data.
LIGHT PEN????	Light pen button pressed when pen was not pointed at crosspoint LED or make/break or break/make LED.	Press button with pen perpendicular to LED.
M/S ERROR*	Error in master/slave communication loop (overrun, parity, framing, count imbalance, or time-out).	Check for a closed loop of MASTER/SLAVE OUT to MASTER/SLAVE IN.
M/S LOOP DOWN	One or more units connected in master/slave loop are not powered up.	Turn on all units or reconfigure master/slave loop.
NOT IN REMOTE	"X" character received over IEEE-488 bus but Model 707 is not in remote.	Put Model 707 in remote.
RAM FAIL*	Self-test detected error in RAM.	See troubleshooting in Section 6.
ROM FAIL*	Self-test detected checksum error in ROM.	See troubleshooting in Section 6.
SETUP ERROR*	Self-test detected checksum error in stored setup. Battery may be low.	Affected setup is cleared, then Model 707 proceeds normally.
TRIG OVERRUN	An additional trigger was received before the Model 707 asserts the READY signal.	Check the READY bit in the serial poll byte.

* Message remains displayed until next operation.

Table 3-4. Informational Messages

Message	Key(s)	Description
IN 255 OUT 000	MENU	Digital input status and digital output parameter (decimal values).
EXT TRIG FALL	MENU	Falling edge external trigger pulse.
EXT TRIG RISE	MENU	Rising edge external trigger pulse.
MATRIX RDY LO	MENU	Matrix Ready pulse active low.
MATRIX RDY HI	MENU	Matrix Ready pulse active high.
STANDALONE	MENU	Stand alone operation of Model 707.
MASTER	MENU	Master unit in master/slave configuration.
IEEE-488 18	MENU	IEEE-488 bus address of 18.
HWSETL 015 mS	MENU	Longest relay settling time of present card configuration (Model 7073 is shown).
1 7071	MENU	Card configuration by slot and model number (Model 7071 in slot 1 is shown).
SELF TEST	MENU	Item to select self-test execution.
FACTORY INIT	MENU	Item to select factory defaults (setups cleared).
SETL 00000 mS	SETTLING TIME	Programmed settling time (added to relay settling time).
TRIG ON EXT	SOURCE	External trigger pulse triggering.
TRIG ON KEY	SOURCE	Front panel key triggering.
TRIG ON TALK	SOURCE	IEEE talk command triggering.
TRIG ON GET	SOURCE	IEEE GET command triggering.
TRIG ON X	SOURCE	IEEE X command triggering.
NOT SETTLED	—	Additional trigger received before programmed settling time expired (trigger is processed).

3.4.3 IEEE-488 Status Indicators

The TALK, LISTEN, and REMOTE LEDs (shown in Figure 3-3) indicate these modes when the Model 707 is being programmed over the IEEE-488 bus. The TALK and LISTEN indicators show when the unit has been addressed to talk or listen. These talk and listen commands are derived from the unit's primary address. REMOTE turns on to show when the unit is placed in remote by addressing it to listen with the REN line true. (All front panel controls except LOCAL and POWER are inoperative when REMOTE is on.) Local operation is restored by pressing LOCAL unless the IEEE-488 LLO (Local Lockout) command is in effect. See Section 4 for details of IEEE-488 bus operation.

3.4.4 Crosspoint Display LEDs

As shown in Figure 3-4, the crosspoint display has six blocks of LEDs (one per card slot). Each block has 8 rows (A-H) by 12 columns (1-12, 13-24, etc.) of LEDs. The display LEDs can show the current open/closed relay states, the on/off states of a setup from in memory, or the on/off states of a setup currently being edited. The on/off states of crosspoint LEDs can be changed by front panel keys, commands over the bus, or an optional light pen. Modified displays can be stored in memory or sent to the relays.

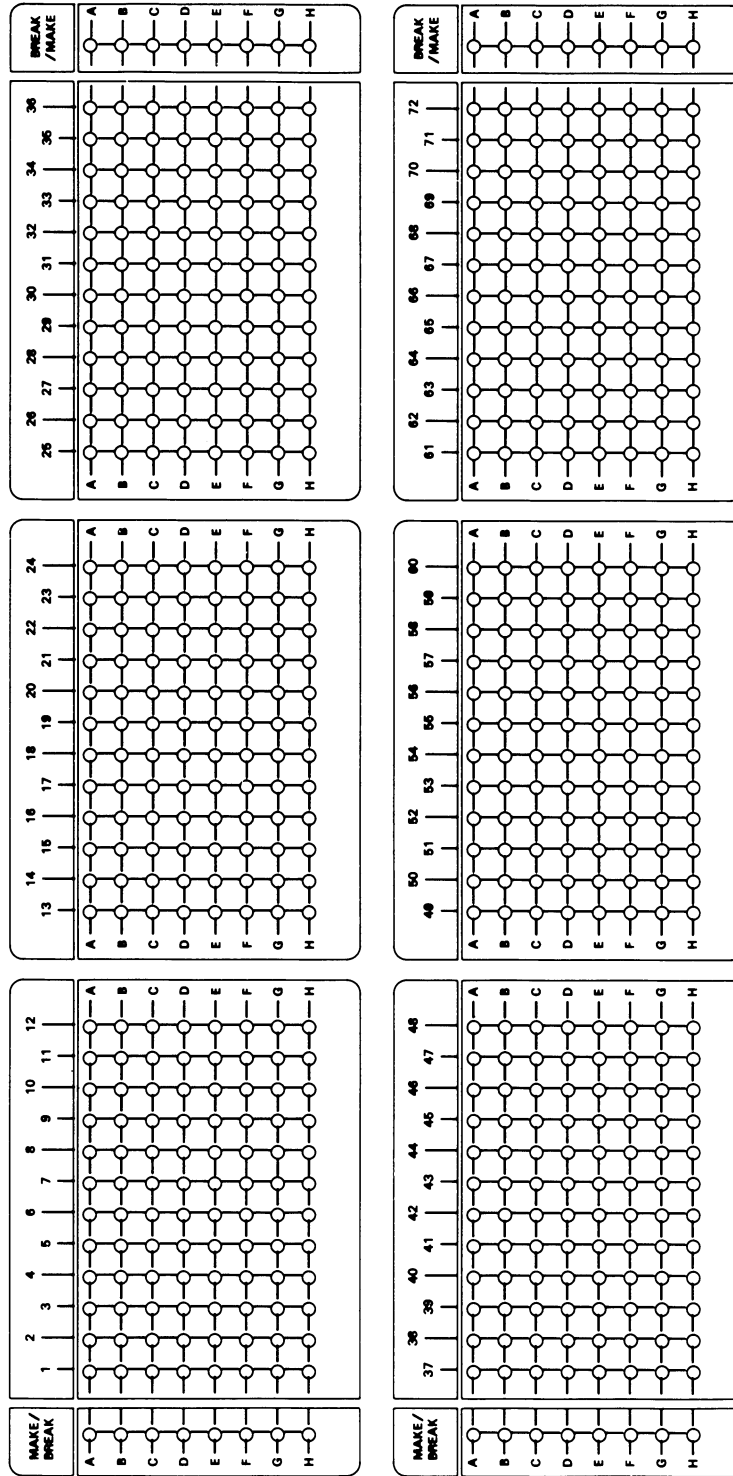


Figure 3-4. Crosspoint Display

3.4.5 Make/Break and Break/Make LEDs

The MAKE/BREAK and BREAK/MAKE displays each have two blocks of LEDs labeled A-H (one for columns 1-36, the other for columns 37-72). Refer to Figure 3-4. Each block shows which rows have been selected for make/break or break/make operation. When switching current sources, use make/break operation to keep current flowing and eliminate switching transients. When switching voltage sources, use break/make operation to avoid momentary shorting of two paths together.

The LEDs can be turned on or off by pressing a row letter key and the MAKE/BREAK or BREAK/MAKE key, or with an optional light pen. Note that selecting a row for break/make de-selects it for make/break and vice versa.

3.4.6 Light Pen

The light pen is an optional input device for toggling the on/off states of crosspoint display LEDs, MAKE/BREAK row LEDs, and BREAK/MAKE row LEDs. One light pen

is used to control the LEDs of all units in a master/slave system.

As seen in Figure 3-5, the light pen plugs into the front panel of stand-alone or master units. (Remove the light pen by pressing the button on the connector plug while pulling out the plug.) Mount the light pen holder on the left handle of the Model 707 by tightening the allen-head screw shown in Figure 3-5.

To toggle the state of a crosspoint LED or MAKE/BREAK, BREAK/MAKE LED with the light pen, follow these steps:

1. Hold the light pen as you would an ordinary pen.
2. With the light pen perpendicular to the front panel overlay at the desired LED, press the button on the pen's barrel.
3. Proper usage will toggle the state of the LED. If the button is pressed while not on an LED, the message LIGHT PEN???? is displayed briefly, then the Model 707 reverts to its previous display.

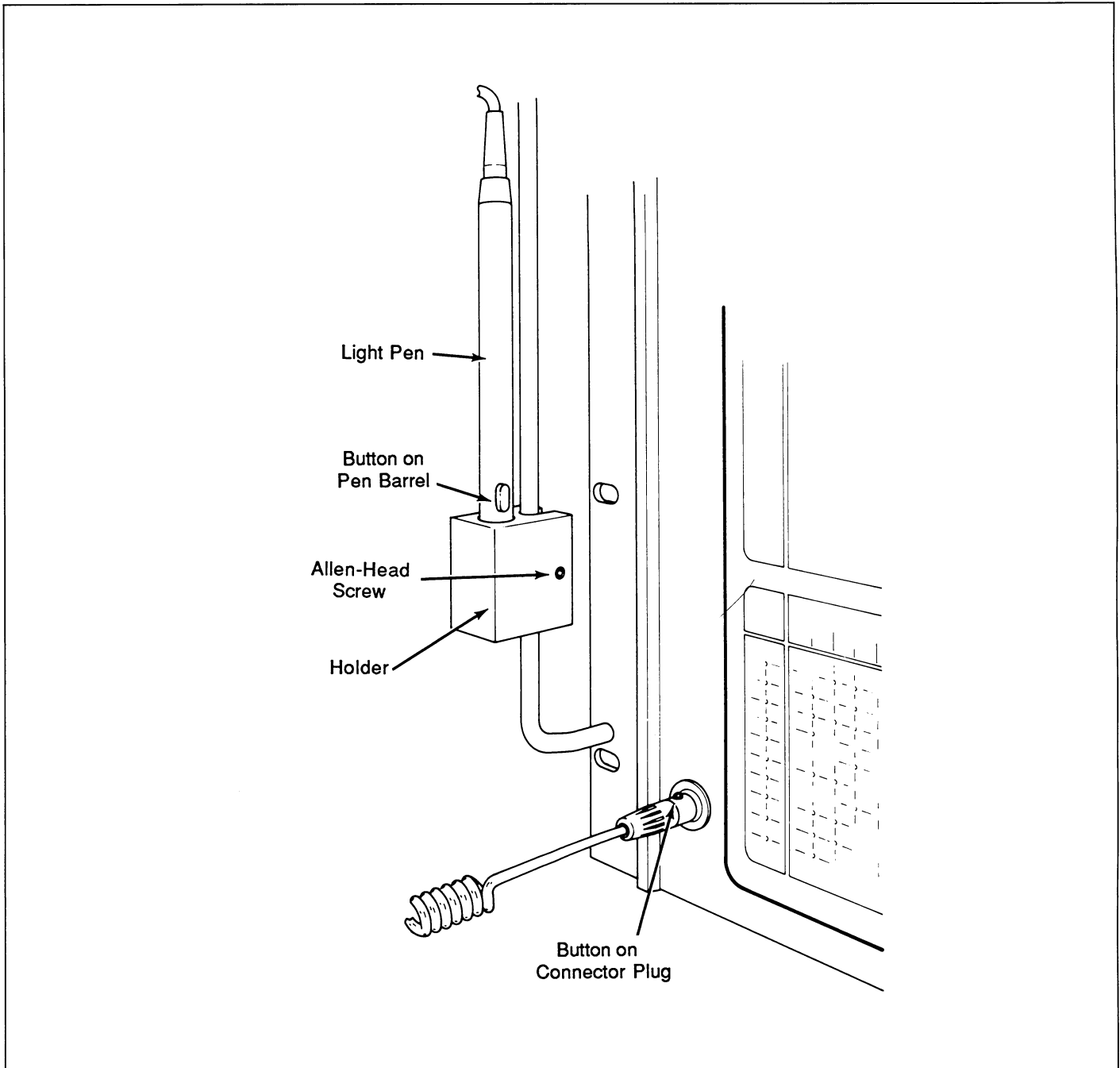


Figure 3-5. Light Pen

3.5 SELECTING CROSSPOINT DISPLAY

In the CROSSPOINT DISPLAY key group on the front panel are two keys that are used to bring setups to the crosspoint display. See Figure 3-6. Three LEDs in the group indicate the source of setup data. Only one of these LEDs is lit at a time:

- **MEMORY LED**—When lit, the crosspoint display shows a setup stored in memory.
- **RELAYS LED**—When lit, the crosspoint display shows the current relay setup.
- **CROSSPOINT DISPLAY MODIFIED LED**—When lit, the crosspoint display shows a modified setup that was previously from memory or from the relays.

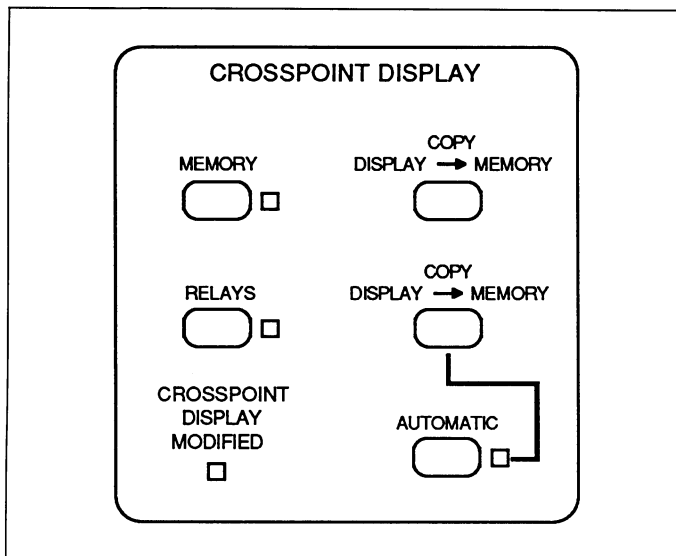


Figure 3-6. Crosspoint Display Keys

Pressing the MEMORY key displays a stored relay setup (location shown in MEMORY STEP field) on the crosspoint display and lights the MEMORY indicator. If a valid location (1-100) is entered first from the data entry keypad, that setup is displayed on the crosspoints and in the MEMORY STEP field. (The INVALID INPUT message is displayed briefly for locations out of range.) The CANCEL key can be used to remove incorrect entries from the alphanumeric display.

When the MEMORY indicator is lit, a setup can also be displayed by entering a valid location and pressing ENTER,

or by pressing a SCROLL key to display the setup at MEMORY STEP ± 1 . If you press and hold a SCROLL key, the MEMORY STEP field is updated continuously. As location 000 is invalid for MEMORY STEP, the SCROLL keys skip this location when incrementing or decrementing.

If the displayed setup is modified by IEEE-488 commands, the crosspoint display changes if the MEMORY indicator is lit. In other words, if you are editing a setup, changes to its source do not appear.

Pressing the RELAYS key displays the current relay setup on the crosspoint display and lights the RELAYS indicator. If a valid location (0-100) is entered first, that setup is sent to the relays and displayed on the crosspoints and in the RELAY STEP field. When location 000 is selected in this manner, the relay states do not change.

When the RELAYS indicator is lit, a setup can also be sent to the relays and displayed by entering a valid location and pressing ENTER, or by pressing a SCROLL key to send and display the setup at RELAY STEP ± 1 . If you press and hold a SCROLL key, the RELAY STEP field is updated continuously. As the SCROLL keys increment and decrement the RELAY STEP field through location 000, there is no effect on the relays. That is, when the RELAY STEP is decremented from 001 to 000, or when incremented from 100 to 000, the relays do not change state.

If the relays change due to a trigger or IEEE-488 commands, the crosspoint display changes only if the RELAYS indicator is lit.

3.6 MODIFYING CROSSPOINT DISPLAY

After choosing the source of the setup, a crosspoint display can be modified by turning on/off crosspoint LEDs with front panel keys or the light pen. As discussed in the next paragraph, if the AUTOMATIC (COPY DISPLAY \rightarrow RELAYS) indicator is lit, these actions open/close relays immediately.

The alphabetic data entry keys (A-H) are for entering the row part of a crosspoint address. The numeric keys (0-9) are for entering column numbers. Use the CANCEL key to remove incorrect entries from the alphanumeric display. Refer to Figure 3-7.

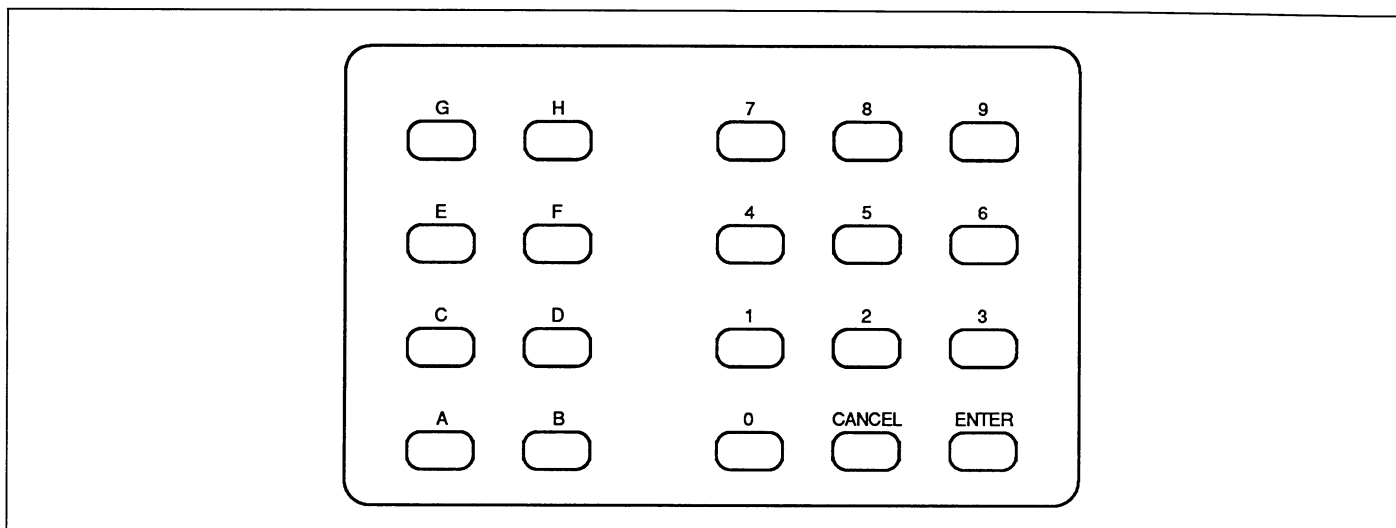


Figure 3-7. Data Entry Keys

The maximum valid column number with a single unit is 72. If several mainframes are connected and programmed for master/slave operation, the maximum column can be up to 360 (with five units).

When a valid crosspoint address (row and column) is in the alphanumeric display, pressing the OPEN key turns off the crosspoint display LED. (The message INVALID INPUT is displayed for addresses out of range.) If the AUTOMATIC (COPY DISPLAY → RELAYS) indicator is lit, the corresponding relay opens immediately. The CLOSE key performs the same action as the OPEN key except that it turns on crosspoint display LEDs and relays.

Pressing the CLEAR key turns off all crosspoint display LEDs. If the AUTOMATIC (COPY DISPLAY → RELAYS) indicator is lit, all relays are opened immediately.

The CROSSPOINT DISPLAY MODIFIED indicator lights and the MEMORY or RELAYS indicator go out when changes are made to the crosspoint display, making it different from the configuration of its source. It also lights when opening an already open crosspoint and closing an already closed crosspoint. The SCROLL keys are not active when the CROSSPOINT DISPLAY MODIFIED indicator is lit.

The optional light pen can also be used to turn on and off crosspoint LEDs. Just hold it perpendicular to the front panel overlay at the desired LED and press the button on its barrel. This action toggles the state of the LED.

The maximum number of simultaneously closed crosspoints depends on the specified drive current per crosspoint of each card. The total relay drive current required per mainframe cannot exceed 4.5A, since the 6V/6A power supply also provides 1.5A for the front panel display.

Table 3-5 lists the maximum crosspoint closures for each card. Since the Model 7072 has three different types of relays, its drive current is row specific. If relays from other cards (or other Model 7072 rows) in the same unit are energized at the same time, their drive currents will reduce this quantity.

Table 3-5. Maximum Number of Closed Crosspoints

Card	Rows	Relay Drive Current (per crosspoint)	Maximum Closed Crosspoints (per mainframe)
7071	A-H	15mA	300
7072	A-B	40mA	112
	C-F	60mA	75
	G-H	80mA	56
7073	A-H	20mA	225

3.7 COPYING CROSSPOINT DISPLAY

The setup data displayed on the crosspoint LEDs can be stored in non-volatile memory of the Model 707 or can be

sent directly to the relays by pressing either the COPY DISPLAY→MEMORY or the COPY DISPLAY→RELAYS key, seen previously in Figure 3-6.

With the COPY DISPLAY→MEMORY key, the displayed crosspoint configuration is stored at the setup location shown in MEMORY STEP field. It overwrites the present setup data at that location. If a valid location is keyed in first, pressing this key stores the crosspoint configuration at that setup and sets the MEMORY STEP field to that location. If the CROSSPOINT DISPLAY MODIFIED indicator is lit, it goes out and the MEMORY LED lights. The INVALID INPUT message is displayed briefly if you try to copy to a setup location below one or above 100.

In master/slave configurations, each unit stores its own portion of each stored setup.

When the COPY DISPLAY→RELAYS key is pressed, the displayed crosspoint configuration is sent to the relays:

- If the MEMORY indicator is lit (i.e., the crosspoint display shows an unmodified setup from memory), the RELAY STEP field is set to the MEMORY STEP field. In effect, this copies a setup from memory to the relays.
- If the RELAYS indicator is lit, the RELAY STEP field is not affected, as it reflects the last stored setup sent to the relays.
- If CROSSPOINT DISPLAY MODIFIED is lit, it goes out and the RELAYS LED lights.

Copying the crosspoint display to the relays can be performed automatically with the toggle-action AUTOMATIC (COPY DISPLAY→RELAYS) key. When the AUTOMATIC indicator is lit, any change to the crosspoint display is also sent to the relays at the same time. This action is apparent when scrolling through unmodified stored setups, as the MEMORY STEP and RELAY STEP fields will sequence together. Changes to the crosspoint display while the AUTOMATIC LED and RELAY LEDs are lit, cause the CROSSPOINT DISPLAY MODIFIED LED to blink and the RELAYS LED to remain lit.

3.8 INSERTING AND DELETING STORED SETUPS

The two keys in the MEMORY group (see Figure 3-8) operate on setups stored in Model 707 memory. These keys are active only when the MEMORY LED is lit.

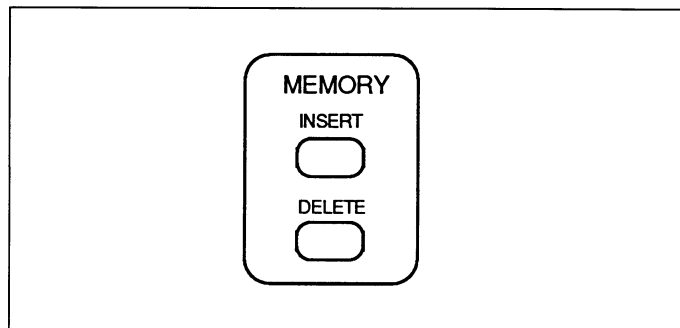


Figure 3-8. Memory Keys

Use the INSERT key to place a blank setup at the memory location shown in the MEMORY STEP field. While the insert is taking place, the alphanumeric display is blank. All setups from the selected setup through 99 are moved up one location. (Setup 100 is deleted by overwriting it with setup 99.) After the insert operation, the crosspoint LEDs display a blank setup.

If a valid location is keyed in first, the MEMORY STEP field is set there and then the insert operation takes place.

Pressing the DELETE key removes the stored setup at the location shown in the MEMORY STEP field. While the delete is taking place, the alphanumeric display is blank. All setups higher than the selected setup are moved down one location. (Setup 100 is cleared.) After the delete operation, the crosspoint LEDs display the new setup "nnn", which previously was setup "nnn+1".

If a valid location is keyed in first, the MEMORY STEP field is set there and then the deletion takes place.

In master/slave configurations, the insert blank setup and delete stored setup operations perform similarly, except on all units of the system.

3.9 MENU OPERATIONS

The Model 707 has several operations that are performed by front panel menu items. Select the first item by pressing the MENU key, subsequent presses of MENU display the remaining items (see Table 3-6). To view all current menu selections, just press and hold the MENU key.

OPERATION

Status items are displayed with no user action. Numeric items are modified by keying in the desired value with the data entry keys and pressing ENTER. Multiple choice items are selected by scrolling through the choices until the desired one is displayed, then pressing ENTER. Immediate action items are invoked by pressing ENTER.

If no modifications are made with the SCROLL or data entry keys, pressing CANCEL exits from the menu without changing any values; otherwise CANCEL restores the current value of the parameter. Pressing ENTER exits from the menu (with changes), except when programming the digital output status.

Table 3-6. Menu Operations

Message	Item Description	Type
IN iii OUT ooo	View digital input, program digital output.	status/numeric
EXT TRIG FALL EXT TRIG RISE	Select which edge of external trigger pulse triggers Model 707 (falling or rising).	multiple choice
MATRIX RDY LO MATRIX RDY HI	Select matrix ready output level (active LO or HI).	multiple choice
STANDALONE MASTER	Select stand-alone or master/slave operation.	multiple choice
IEEE-488 nn	Program IEEE-488 bus address.	numeric
HWSETL nnn mS	View longest relay (hardware) settling time of cards in system.	status
n cccc	View slot number (n) and card label (cccc).	status
SELF TEST	Execute self test.	immediate action
FACTORY INIT	Return to factory defaults. (All stored setups are cleared.)	immediate action

3.9.1 Digital I/O

The TTL-compatible DIGITAL I/O port has eight data lines for inputs, eight data lines for outputs, and two control lines for handshaking. The pinout for the rear panel DB-25 connector is shown in Figure 3-9. Status of the input lines is viewed and states of the output lines are programmed through the first menu item. With no input connections and power-up default conditions for the output, the alphanumeric display will read the following decimal values:

IN 255 OUT 000

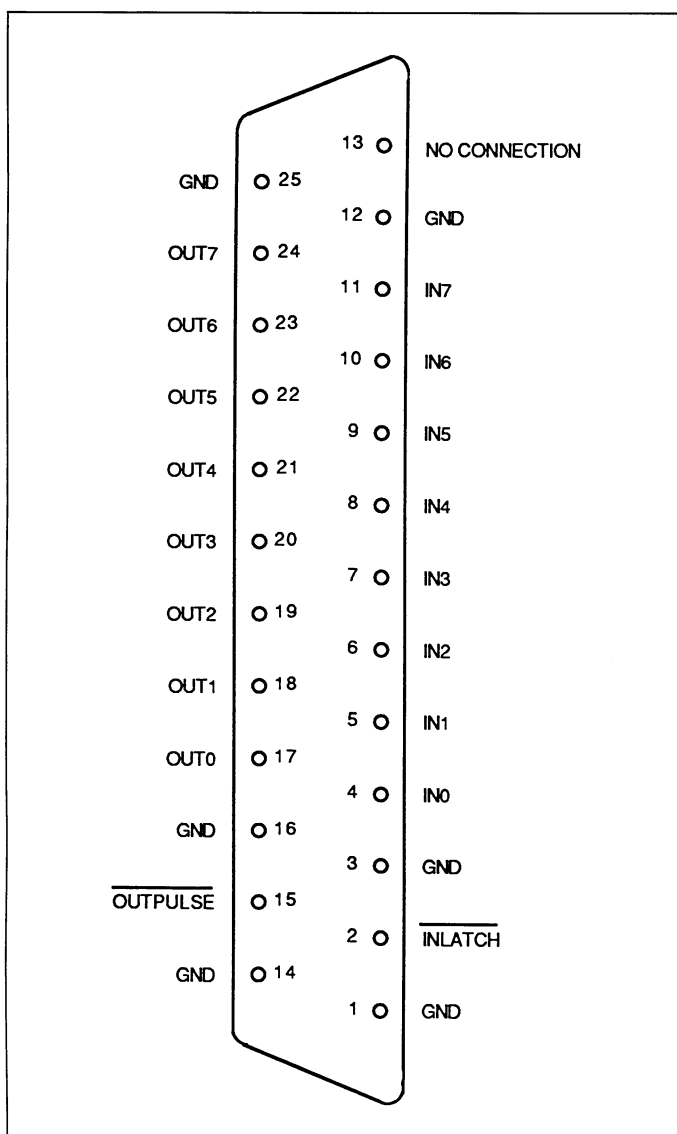


Figure 3-9. Digital I/O Port

The digital inputs are logic high with no connections. Use the control line INLATCH (low true) to latch in the digital inputs when changing an input state.

To program the digital output states, select the desired decimal value with the data entry keys and press ENTER. You can now key in another value and press ENTER, or press CANCEL to exit menu mode, or press MENU to continue to the next item. Each time the digital outputs are programmed, even if the states are not changed, the control line OUTPULSE is brought low.

With master/slave configurations, only the DIGITAL I/O port of the master unit is available for viewing and programming.

3.9.2 External Trigger

If triggers are enabled, and external trigger is selected as a source, a TTL-compatible pulse of at least 600nsec duration at the rear panel EXTERNAL TRIGGER INPUT jack triggers the Model 707. The input BNC jack is shown in Figure 3-10.

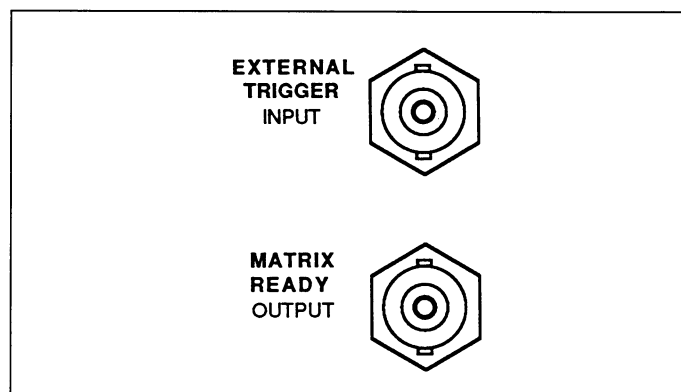


Figure 3-10. Rear Panel BNC Jacks

The unit can be programmed with a menu item for which edge (falling or rising) of the external trigger pulse causes a transfer of stored setup data to the relays. Sample trigger pulses are shown in Figure 3-11. To select which pulse edge triggers, use the MENU key to choose the menu item for external trigger. The power-up default display will read:

EXT TRIG FALL

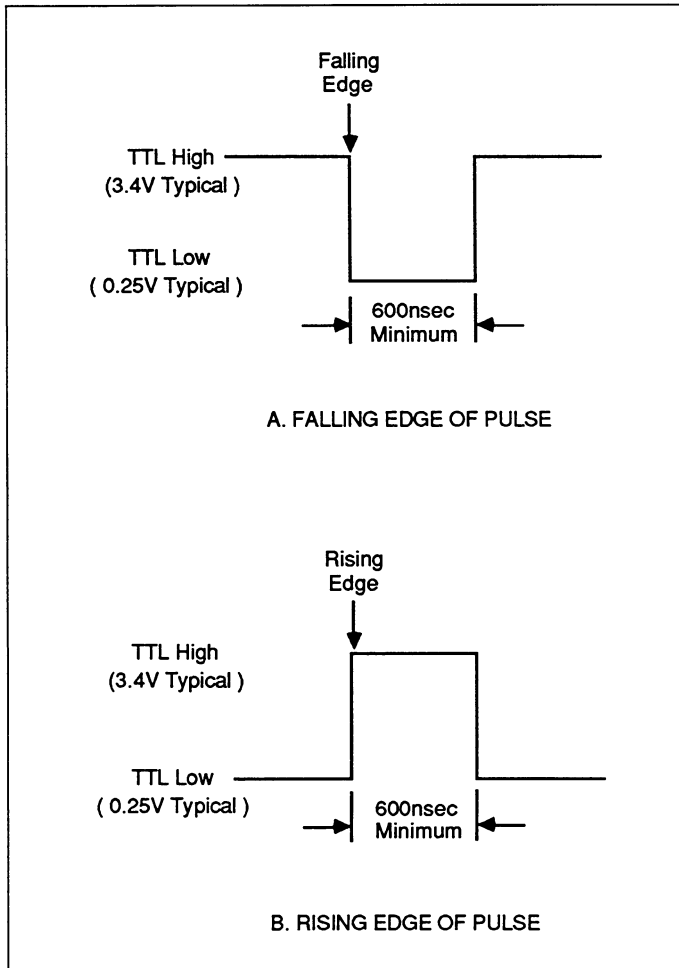


Figure 3-11. Sample External Trigger Pulses

To choose the alternate external trigger state, use the SCROLL ▲ or ▼ keys, then press ENTER. This action also exits from the menu mode. (Pressing CANCEL instead of ENTER returns external trigger to its previous state and the Model 707 remains in menu mode.)

In master/slave configurations, only the EXTERNAL TRIGGER INPUT port of the master unit is active.

See paragraph 3.11 for more information on triggering the Model 707.

3.9.3 Matrix Ready

The Model 707 provides a TTL-compatible signal at its rear

panel MATRIX READY OUTPUT jack. The output BNC jack was shown in Figure 3-10. The MATRIX READY signal goes false when relays are switched and goes true at the end of the programmed settling time. (As described in paragraph 3.11, this is also after the relay settling time.)

Through a menu item, the unit can be programmed for a high- or low-true MATRIX READY signal, as seen in Figure 3-12. To select the active state of the signal, press the MENU key until the MATRIX RDY item is displayed. The power-up default display will read:

MATRIX RDY LO

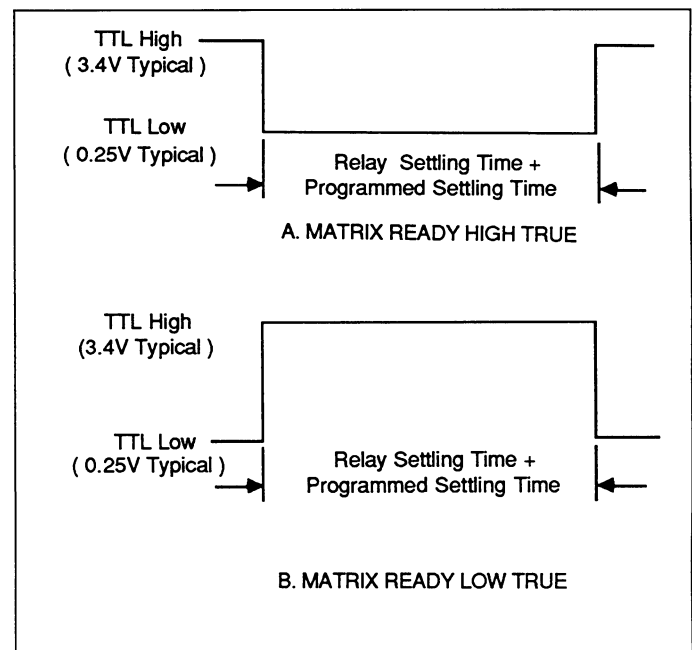


Figure 3-12. Sample Matrix Ready Pulses

To choose the other active state, use the SCROLL ▲ or ▼ keys, then press ENTER. This action also exits from the menu mode. (Pressing CANCEL instead of ENTER returns matrix ready to its previous state and the Model 707 remains in menu mode.)

In master/slave configurations, the MATRIX READY signals of all units function, but only that of the master is to be considered accurate.

3.9.4 Stand-alone and Master/Slave

One method to expand system size is to connect up to five mainframes in a master/slave configuration, where all units are daisy-chained for serial communication and control. System operations are performed through the master unit, either over the IEEE-488 bus or the master's front panel (including the light pen). A master/slave system appears as a single unit (and IEEE-488 address) with a maximum size of 8 rows by 360 columns. Selection of stand-alone or master/slave operation is done with a menu item.

As previously described in paragraph 2.6, the MASTER/SLAVE OUT and MASTER/SLAVE IN rear panel connectors are used to connect DIN cables in a closed loop. The connector pinouts are defined in Figure 3-13.

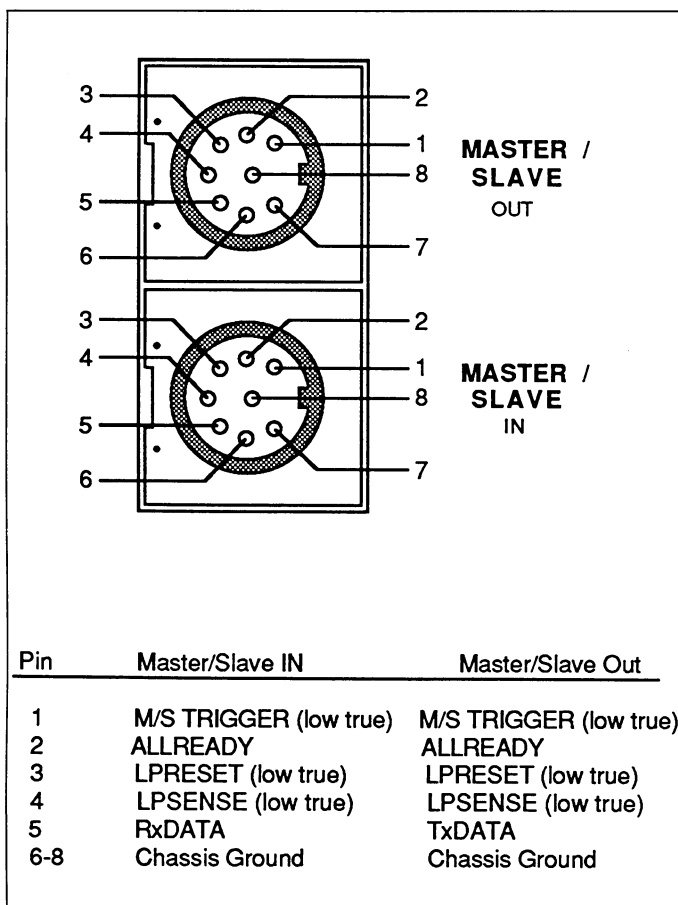


Figure 3-13. Master/Slave Connectors

After interconnecting and powering up all units, one unit is selected to be a master.

This action is performed by pressing the MENU key of the desired master mainframe until the display reads STAND ALONE. Next, scroll up or down to the MASTER message. Then, press the ENTER key to initiate the master/slave loop. If the loop is complete (MASTER/SLAVE OUT to MASTER/SLAVE IN in a daisy chain among all units), the master unit will exit menu mode and the other units will display SLAVE 1, SLAVE 2, etc. (The slave number is determined by the unit's position in the loop.) If the loop of DIN cables is not closed, the master will display the message M/S ERROR and all units will remain as stand-alones.

When the units are powered up one at a time, they will display the message M/S LOOP DOWN until all units are powered.

CAUTION

When it is necessary to cycle power on a slave unit, turn off all units in the master/slave configuration. This procedure prevents the open communication and control loop from putting the slave unit in an undesirable state.

During master/slave operation, most front and rear panel controls of the slave units are inactive. Table 3-7 shows which controls and indicators remain active for slave units.

The master unit communicates with the slaves only when necessary; it does not continuously monitor the status of the closed-loop configuration. Hence, a disconnected master/slave loop cable is not detected, and the message M/S ERROR is not displayed, until the master attempts to send or receive data around the loop. The steps taken by a master unit to recover from an M/S ERROR are outlined below:

1. The master stops processing IEEE-488 bus commands, returns to stand-alone operation, and disables.
2. The slave units remain the same as before the error occurred.
3. To re-initialize the loop, ensure that master/slave cables are secure, and select master/slave operations from the mainframe that previously was master.

Table 3-7. Status of Slave Unit Controls

Control, Indicator or Connector	Status
POWER	active
CROSSPOINT DISPLAY Group:	
MEMORY key and LED	only LED active
RELAYS key and LED	only LED active
CROSSPOINT DISPLAY MODIFIED LED	active
COPY DISPLAY→MEMORY	inactive
COPY DISPLAY→RELAYS	inactive
AUTOMATIC key and LED	only LED active
SCROLL ▲ and SCROLL ▼	inactive
MEMORY Group:	
INSERT	inactive
DELETE	inactive
MENU key and LED	inactive
SWITCHING Group:	
SETTLING TIME	inactive
MAKE/BREAK	inactive
BREAK/MAKE	inactive
LOCAL	inactive
TRIGGER Group:	
ENABLE key and LED	only LED active
SOURCE	inactive
MANUAL	inactive
Data Entry (A-H, 0-9)	inactive
CANCEL, ENTER	inactive
RESET	inactive
CLEAR, OPEN, CLOSE	inactive
Alphanumeric Display	active (Note 1)
TALK, LISTEN, REMOTE LEDs	inactive
Crosspoint Display LEDs	active
MAKE/BREAK and BREAK/MAKE	active
Row LEDs	
LIGHT PEN	inactive
Rear Panel Connectors:	
MASTER/SLAVE IN	active
MASTER/SLAVE OUT	active
EXTERNAL TRIGGER INPUT	inactive
MATRIX READY OUTPUT	active (Note 2)
RELAY TEST	not used
DIGITAL I/O	inactive (Note 3)
IEEE-488 INTERFACE	not used

Notes:

1. Messages only.
2. Timing accuracy not guaranteed.
3. Outputs set to all low.

3.9.5 IEEE-488 Bus Address

The Model 707 communicates over the IEEE-488 bus through the rear panel connection shown in Figure 3-14. When connected to a bus controller, instrument operating modes can be programmed. Note that IEEE-488 common is always grounded. IEEE-488 interface function codes are marked adjacent to the connector.

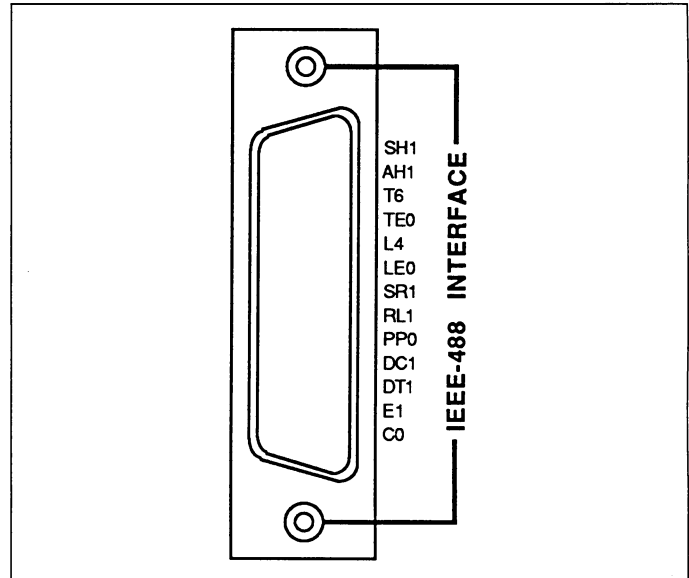


Figure 3-14. IEEE-488 Bus Connector

A menu item is used to set the primary address of the Model 707 for bus operation. The primary address of the Model 707 is factory set to 18, but it may be set to any value between 0 and 30 as long as address conflicts with other instruments or the bus controller are avoided.

To check the present primary address or to change to a new one, perform the following procedure:

1. Press the MENU key until the current primary address is displayed. For example, if the instrument is set to primary address 18, the following message is displayed:
IEEE-488 18
2. Press CANCEL to retain the present address and exit menu mode.
3. To change the primary address, use the data entry keys to key in a new value, then press ENTER. This action will also exit menu mode. The new address will be stored in memory so that the instrument powers up to that address.

NOTE

Each device on the bus must have a unique primary address. Failure to observe this precaution will probably result in erratic bus operation.

Section 4 contains detailed information on operating the Model 707 over the IEEE-488.

3.9.6 Relay (Hardware) Settling Times

The card specification "relay settling time" is the time needed for the relays to actuate or release (including contact bounce time) and pass a clean signal. Since this specification is card dependent, the Model 707 must identify on power-up which cards are installed to determine the longest relay settling time in the system (stand-alone or master/slave). This value is not user-modified, but the total settling time for a switching operation can be lengthened by using the programmed settling time, as explained in paragraph 3.10.

To view the relay (hardware) settling time of the system, press the MENU key until the display reads:

HWSETL 015 mS

In this example, there is a Model 7072 or 7073 matrix card present in the system. Press CANCEL to exit the menu mode.

See paragraph 3.11 for a discussion of settling times and triggers.

3.9.7 Card Labels

Each matrix card can be identified by the Model 707. You can view the card labels of the present configuration by using a menu item. Press the MENU key until the alphanumeric display reads:

1 7072

In this case, a Model 7072 is in slot #1. Use the SCROLL ▲ to view the card label of the next slot. If no card is present, the display will be:

2 NONE

Continue pressing the SCROLL ▲ or ▼ keys for the remaining slots or press CANCEL to exit the menu mode.

In master/slave configurations, all units display card labels simultaneously.

3.9.8 Self-test

The self-test program is intended to check ROM, RAM, and the front panel LED indicators. This test is also part of the power-up sequence. If you want to run the test without cycling power, use the following procedure to select and run it:

1. Press the menu key until the display reads SELF TEST.
2. Press the ENTER key to initiate the test. The unit's ROM and RAM are checked. Next, all the front panel LEDs are lit for your inspection. If no errors are detected, menu mode is exited.

If there is an error in ROM or RAM, a ROM FAIL or RAM FAIL message is displayed until a key press or bus operation. See Section 6 for troubleshooting procedures.

For master/slave configurations, all units are tested simultaneously, so you might have to run the test more than once to inspect all LED indicators.

3.9.9 Factory Defaults

A menu item can be used to return the Model 707 to the factory default conditions previously listed in Table 3-2. To initiate this action, follow these steps:

1. Press the MENU key until the display shows FACTORY INIT.
2. Press the ENTER key. The display will read ENTER IF SURE. (This additional keypress is to prevent unintentional initialization.) At this step you can press CANCEL to exit menu mode, or you can continue with the next step.
3. Press the ENTER key again. The Model 707 will return to factory settings and exit menu mode.

In master/slave configurations, all units return to factory defaults when this menu item is selected from the master unit.

3.10 SELECTING SWITCHING PARAMETERS

The Model 707 has three switching parameters that are user-modified: the programmed settling time, make-before-break rows, and break-before-make rows. These values of these parameters are in effect for all relay switching until they are changed. Figure 3-15 shows the front panel keys of the switching group.

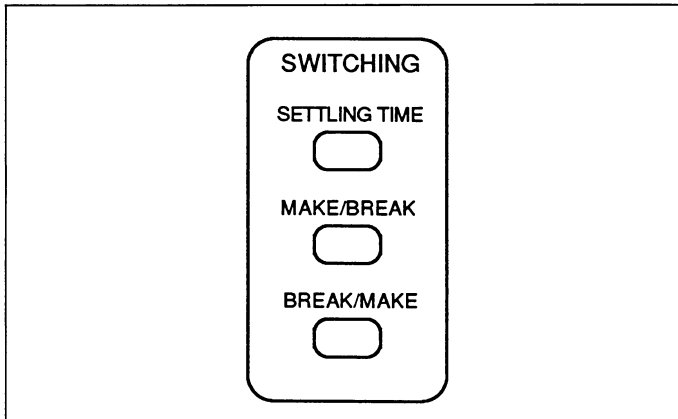


Figure 3-15. Switching Keys

3.10.1 Programmed Settling Time

The programmed settling time is a variable switching delay that can be used to lengthen the fixed delay of the relay (hardware) settling time. You can select, in 1msec increments, up to 65 seconds of an additional switching delay.

If an additional trigger is received during this time, it is processed and the message NOT SETTLED is displayed. At the end of the programmed settling time, the Model 707 sets the MATRIX READY output true.

To view or change the programmed settling time, press the SETTling TIME key in the SWITCHING key group. The value of the programmed settling time value is displayed as:

SETL 00000 mS

To exit the display without changing the value, just press CANCEL. To change the value, enter between 0-65000 with the data entry keys and press ENTER. This action also returns the display to the RELAY STEP and MEMORY STEP.

The programmed settling time is in effect for all crosspoint relay open/close operations until it is reprogrammed. Its effect on trigger response times is described in paragraph 3.11.

3.10.2 Make/Break and Break/Make Rows

Make-before-break switching of relays is defined as connecting a new circuit before disconnecting the present circuit. It is used to eliminate transients caused by switching between current sources. Break-before-make switching means to disconnect the present circuit before connecting a new circuit. It is used to avoid momentary shorting of two voltage sources. Both of these switching operations are supported by the Model 707.

Rows of crosspoint relays are user-selectable for make/break (make-before-break), break/make (break-before-make), or "don't care" operation. The selections will be in effect for all switching until new choices are made. When make/break or break/make operation is chosen, the Model 707 automatically switches the crosspoint relays through intermediate setups to perform the following steps:

1. Crosspoints in break/make rows are opened.
2. Crosspoints in make/break rows are closed.
3. Crosspoints in make/break rows are opened.
4. Crosspoints in break/make rows are closed; crosspoints in "don't care" rows are opened or closed accordingly.

These steps are transparent to the user except for the increased settling time. If either make/break or break/make rows are not selected, the appropriate steps in the previous list are deleted and the total settling time decreases. As make/break and break/make operations affect settling times and trigger response, these operations are further discussed in paragraph 3.11.

The front panel MAKE/BREAK and BREAK/MAKE keys are used in conjunction with the data entry keys to select rows for operation as make/break or break/make. When a row designation (A-H) is selected and displayed by itself, the MAKE/BREAK and BREAK/MAKE keys toggle the state of the MAKE/BREAK or BREAK/MAKE LED for that row and immediately reprograms the Model 707 for the new operation. The INVALID INPUT message is displayed briefly if you press the MAKE/BREAK or BREAK/MAKE key without first selecting a row or if a row/column address is displayed instead of just a row.

Note that selecting a row for make/break de-selects it for break/make and vice versa. The various front panel operations are listed in Table 3-8.

Table 3-8. Make/Break and Break/Make Front Panel Operation

Present State	Action	Next State
Don't Care	Select Make/Break Select Break/Make	Make/Break Break/Make
Make/Break	Select Break/Make De-select Make/Break	Break/Make Don't Care
Break/Make	Select Make/Break De-select Break/Make	Make/Break Don't Care

The optional light pen can be used to toggle the LED states directly. The light pen can also select rows for make/break or break/make operation from slave units. The row selection is in effect for all units connected in a master/slave configuration.

3.11 TRIGGERING

When a Model 707 stand-alone or master unit is triggered, the stored relay setup from RELAY STEP+1 is sent to the relays. Triggers are enabled with the front panel ENABLE key of the TRIGGER group (see Figure 3-16). This key toggles between triggers enabled and triggers disabled. When triggers are enabled, the ENABLE LED is lit.

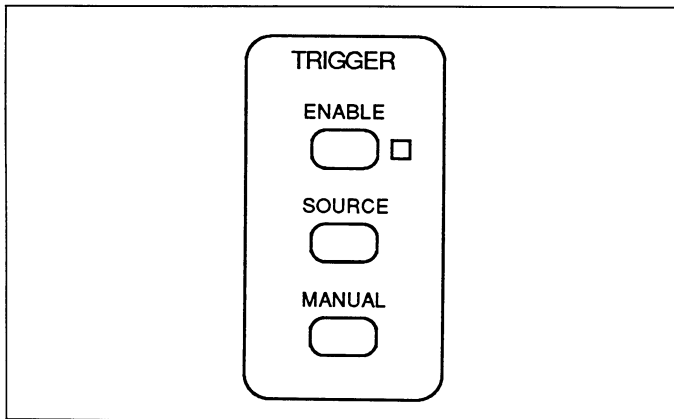


Figure 3-16. Trigger Keys

The maximum trigger rate is specified with no make/break or break/make rows selected. As will be described in paragraph 3.11.3, additional switching delays are necessary with make/break or break/make operation.

3.11.1 Trigger Sources

The programmed trigger source provides the stimulus to increment to the next stored setup. Trigger sources include:

- Front panel MANUAL key—When triggers are enabled, this key is always operational (on stand-alone and master units) regardless of the selected source (unless the unit is placed in remote over the IEEE-488 bus).
- External trigger pulse—An appropriate pulse, applied to the EXTERNAL TRIGGER INPUT jack on the rear panel, provides the trigger stimulus.
- IEEE command triggers—IEEE-488 GET, X, or talk commands provide the stimulus when the appropriate source is selected.

Select the trigger source as follows:

1. Press SOURCE and note that the current trigger source is displayed:

TRIG ON EXT

- This is the display for external triggering (the power-up default). Table 3-9 lists the displays for all trigger sources.
2. Press the SCROLL ▲ or ▼ keys until the desired trigger source is displayed. Then press ENTER to select it and exit menu mode.
 3. If another source is scrolled to before pressing ENTER, pressing CANCEL once returns the old selection, pressing it again exits menu mode.

Table 3-9. Front Panel Messages for Trigger Sources

Message	Description
TRIG ON TALK	IEEE talk command
TRIG ON GET	IEEE GET command
TRIG ON X	IEEE X command
TRIG ON EXT	External trigger pulse
TRIG ON KEY	Front panel MANUAL key only*

*If triggers are enabled, pressing MANUAL emulates the selected trigger source.

3.11.2 Front Panel Triggering

To trigger the Model 707 from the front panel, simply press the MANUAL key. (Press and hold for auto-repeat.) If trig-

gers are enabled, this key is always operational regardless of the selected trigger source (unless the unit is placed in remote over the IEEE-488 bus, in which case all front panel keys except LOCAL are locked out). Thus, front panel trigger source selection (TRIG ON KEY) provides a means to lock out all other trigger sources when only front panel triggering is desired.

Triggering will stop when the RELAY STEP field increments to 100. If you press MANUAL and the unit is not ready, an error message will be displayed, as discussed in the following paragraph.

3.11.3 Trigger Overrun Conditions

Once the instrument is triggered, it begins transferring relay setup data from mainframe memory to the matrix cards. If a second trigger is received while the unit is still transferring data, a trigger overrun condition will occur. In this case, the second trigger is not processed and the unit will display the following error message:

TRIG OVERRUN

After the time required for transferring relay data has elapsed, the Model 707 is able to process another trigger. If a trigger is received before the programmed settling time has elapsed, the following message is displayed:

NOT SETTLED

Figure 3-17 shows an example setup change and a timing diagram of the READY (for trigger) pulse and a high true MATRIX READY pulse when the Model 707 is processing the trigger. (The status of these signals is available in the serial poll byte, see Section 4.) This timing is for setups with no make/break or break/make rows.

When either make/break or break/make operation is selected, but not both, the Model 707 switches through an intermediate setup to ensure proper relay operation. If only make/break rows are selected, the Model 707 takes these steps:

1. Closes crosspoints in make/break rows yielding an intermediate setup.
2. Opens crosspoints in make/break rows and opens/closes crosspoints in "don't care" rows yielding the desired setup.

If only break/make rows are selected, the Model 707 takes these steps:

1. Opens crosspoints in break/make rows yielding an intermediate setup.
2. Closes crosspoints in break/make rows and opens/closes crosspoints in "don't care" rows yielding the desired setup.

An example of these operations is shown in Figure 3-18 with its corresponding timing diagram. By comparing Figures 3-17 and 3-18, you can see that the intermediate setup needed for make/break or break/make causes a delay in the assertion of READY and MATRIX READY equal to the relay settling time.

When a combination of make/break and break/make rows are selected, the Model 707 must switch through three intermediate setups to ensure proper relay operation. The steps taken by the unit are as follows:

1. Opens crosspoints in break/make rows yielding the first intermediate setup.
2. Closes crosspoints in make/break rows yielding the second intermediate setup.
3. Opens crosspoints in make/break rows yielding the third intermediate setup.
4. Closes crosspoints in break/make rows and opens/closes crosspoints in "don't care" rows yielding the desired setup.

Figure 3-19 shows an example setup change with the necessary intermediate setups. As the timing diagram shows, three additional relay settling time intervals are needed for the intermediate setups.

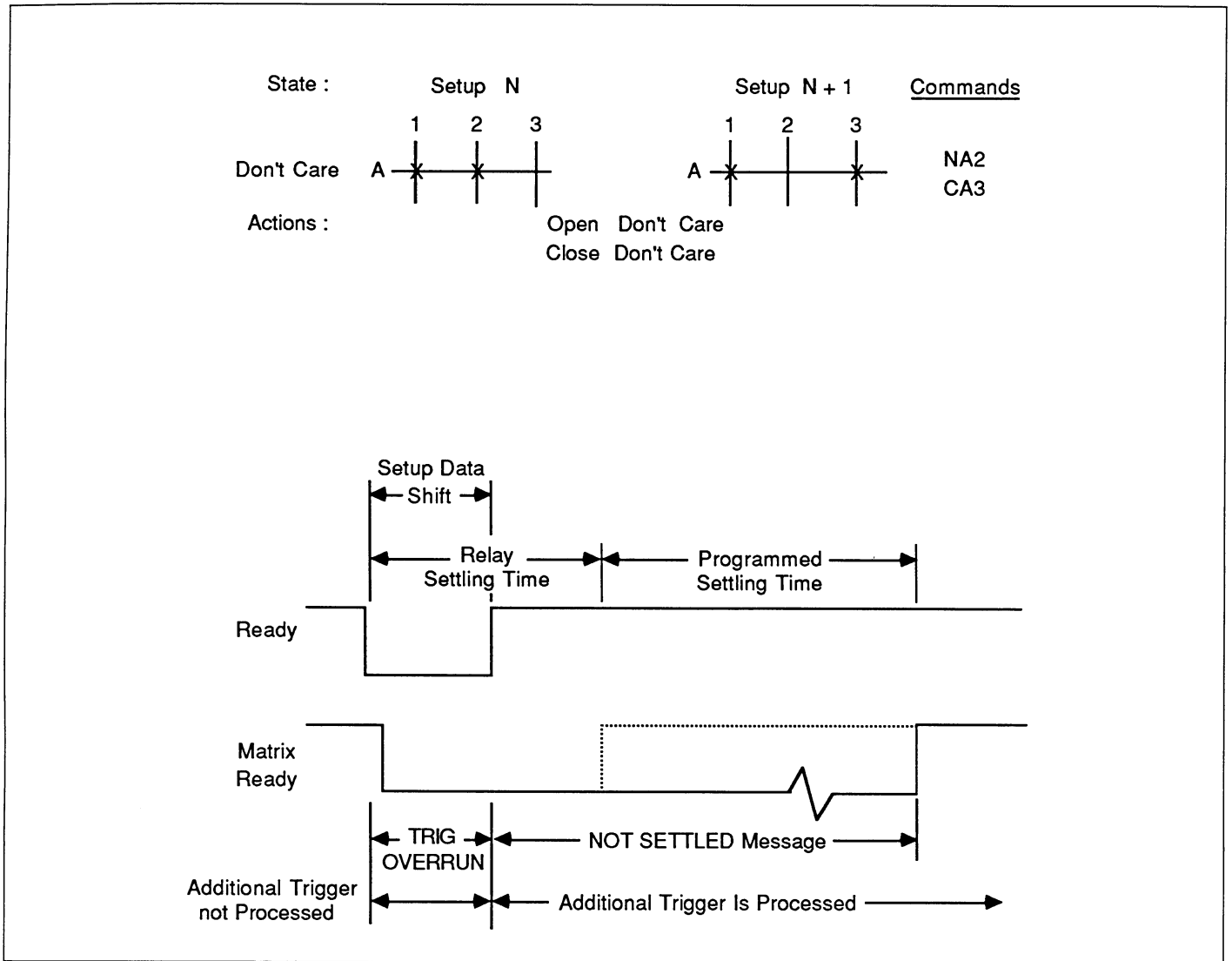


Figure 3-17. Timing without Make/Break or Break/Make Rows

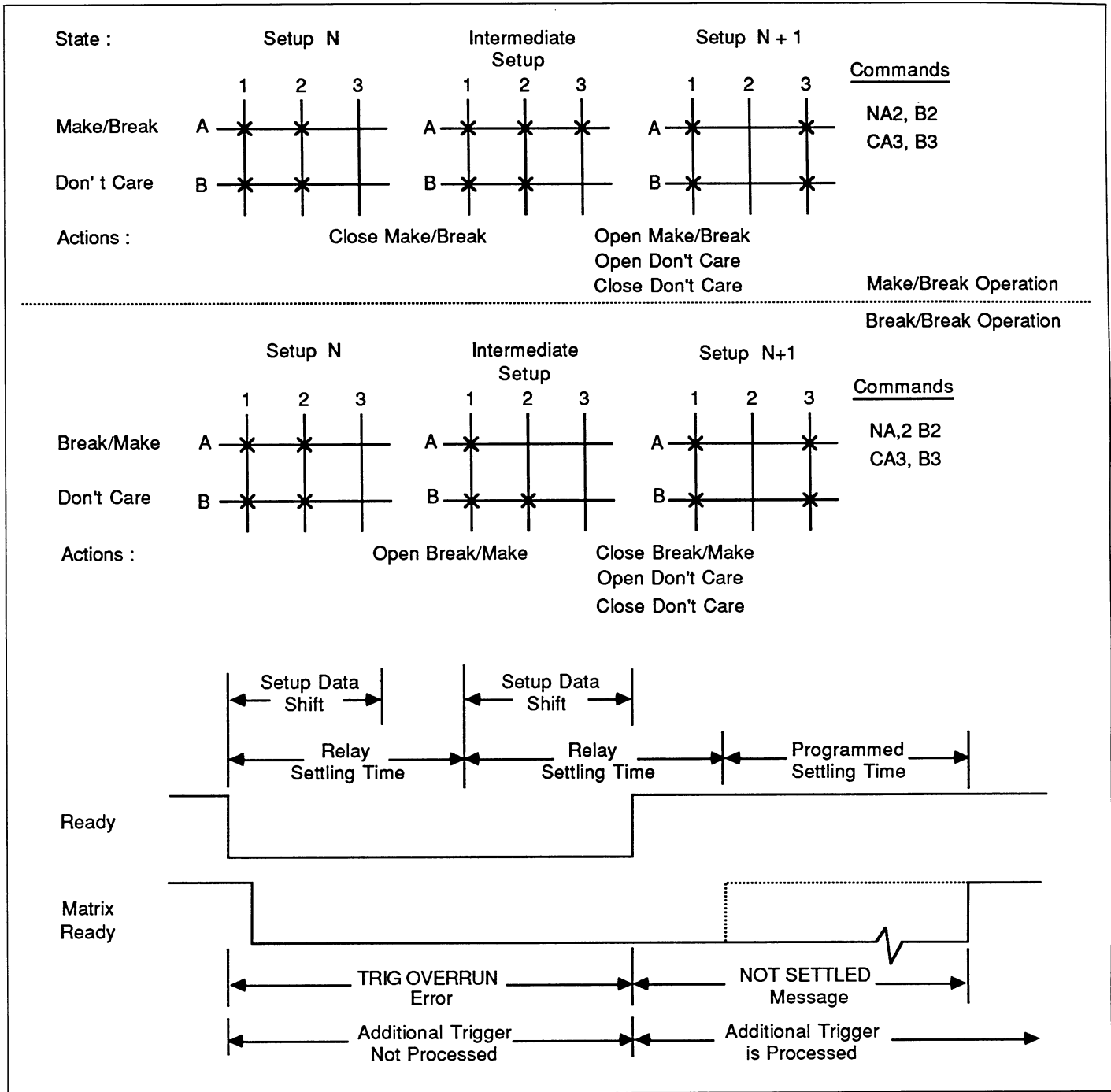


Figure 3-18. Timing with Either Make/Break or Break/Make Rows

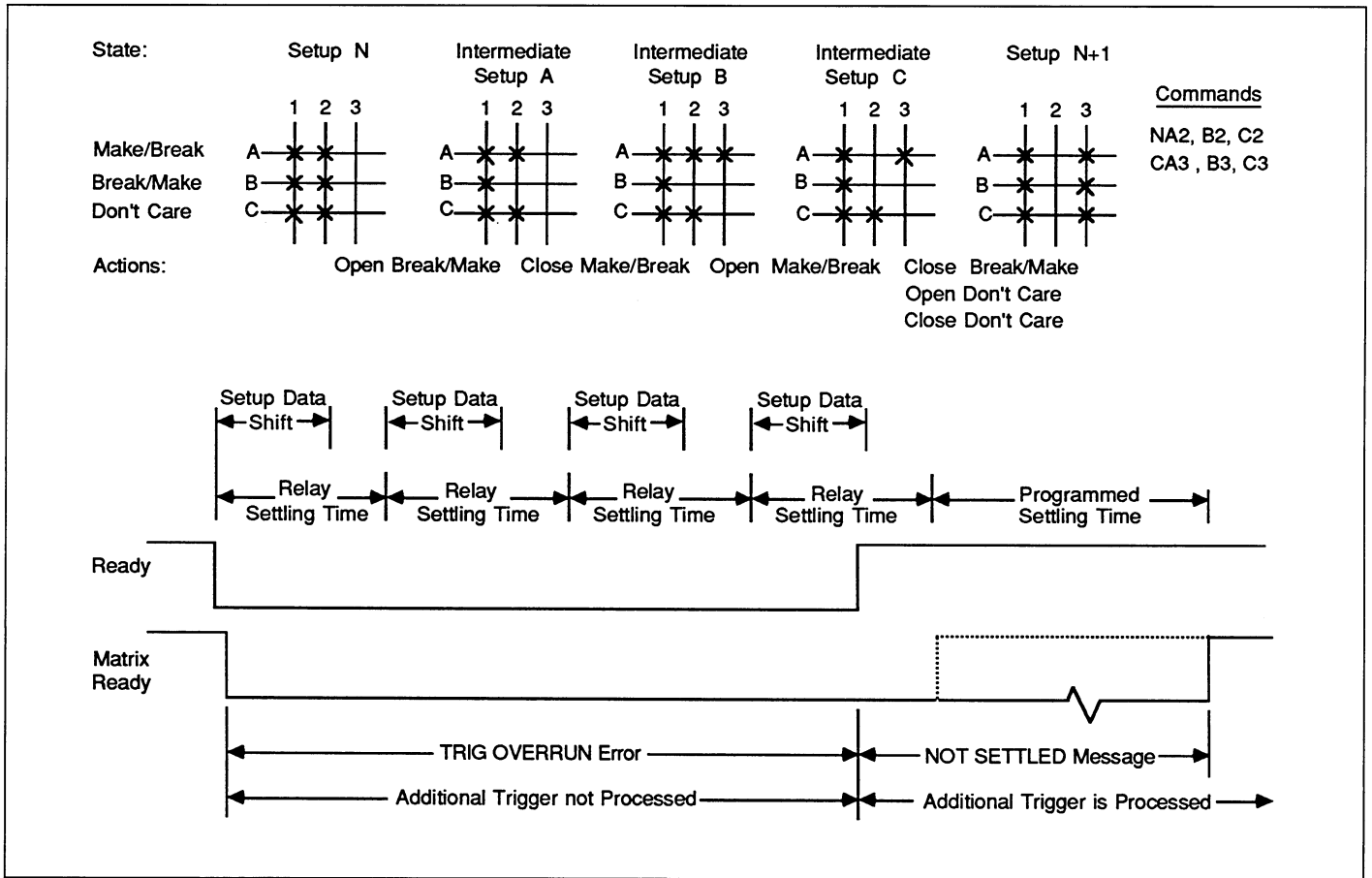


Figure 3-19. Timing with Both Make/Break and Break/Make Rows

3.11.4 External Trigger Input

To use external triggering, first select that source with the MENU and SCROLL keys as described in paragraph 3.11.1. With triggers enabled, the unit will then be triggered when an input pulse with the specifications previously shown in Figure 3-11 is applied to the EXTERNAL TRIGGER INPUT jack. The unit is triggered on either the falling (leading) or rising (trailing) edge of the pulse, as selected by a menu item.

3.11.5 Matrix Ready Output

The matrix ready output provides a TTL-compatible signal, as shown previously in Figure 3-12. This signal can be used to inform other instruments when the total settling time is complete. It is programmable by a menu item for high or low true. The leading edge of the "true" level indicates the end of the total settling time (relay settling time plus programmed settling time).

3.11.6 IEEE-488 Bus Triggering

To trigger a setup change with an IEEE-488 trigger source, you must send the appropriate IEEE-488 command over the bus: X, talk, or GET, depending on the selected source. Trigger on GET allows the fastest IEEE-488 triggering response. See Section 4 for details on bus triggering.

If one of these commands has been selected as the trigger source, you can also trigger the unit by pressing the MANUAL key unless the unit is in remote.

3.12 RESETTING

The reset operation performs the same functions as cycling power except power-up self-checking. If a master/slave error is detected during reset, the unit will revert to stand-alone operation. The front panel RESET key is used to initiate a reset operation.

Reset, power-up, and factory default conditions were previously listed in Table 3-2.

3.13 BACKPLANE JUMPERS

The Model 707 backplane has jumpered connections between slots 3 and 4 for the following general purpose signals:

- HI and LO of analog bus #1 (e.g., rows C-F of Model 7072).
- HI, LO, and Guard of analog bus #2 (e.g., rows A-H of Model 7071).

These jumpers can be removed to selectively isolate the signals of slots 1-3 from slots 4-6. If all jumpers are removed, there will be two 3-slot general purpose analog backplanes in one mainframe. An example system configuration using this isolation method is described in Section 2.

The following steps outline the procedure for removing backplane jumpers:

WARNING

Turn off mainframe power and disconnect the line cord. Ensure no voltage is applied from user circuits, then remove all cards.

1. Set the mainframe on a bench. (Slightly raise the bottom edge of the front panel off the bench surface by placing a thin book under the mainframe.) Remove the eight pan-head screws holding the handles to the unit. See Figure 3-20.
2. Remove the six flat-head screws attaching the front panel.
3. Grasping the right side of the front panel, swing it to the left approximately 45° and rest it on its edge.

CAUTION

Do not touch the surface of the backplane to prevent possible contamination from body oil and dirt, which could degrade insulation resistance.

4. The backplane jumpers are called out in Figure 3-21. Remove them by snipping with diagonal cutters.

If it is necessary to clean the connector side of the backplane or re-insert backplane jumpers (Keithley part number J-15) continue with these steps:

5. Remove the two pan-head screws attaching the backplane support bracket, as shown in Figure 3-20, and remove the bracket.
6. Grasping the backplane by its edges, lift it off the backplane support tabs. Swing the backplane to the left and rest it on its edge as shown in Figure 3-22.
7. After soldering on the backplane, remove the flux with Freon® TMS or TE and clean cotton swabs. Take care

- not to spread the flux to other areas. Once the flux has been removed, swab only the affected area with methanol, then blow-dry the card with dry nitrogen gas.
8. If the backplane connectors become contaminated, clean with methanol or distilled water and allow to dry several hours.
 9. Reassemble the unit taking care to align the backplane with its supports before attaching the front panel and handles.

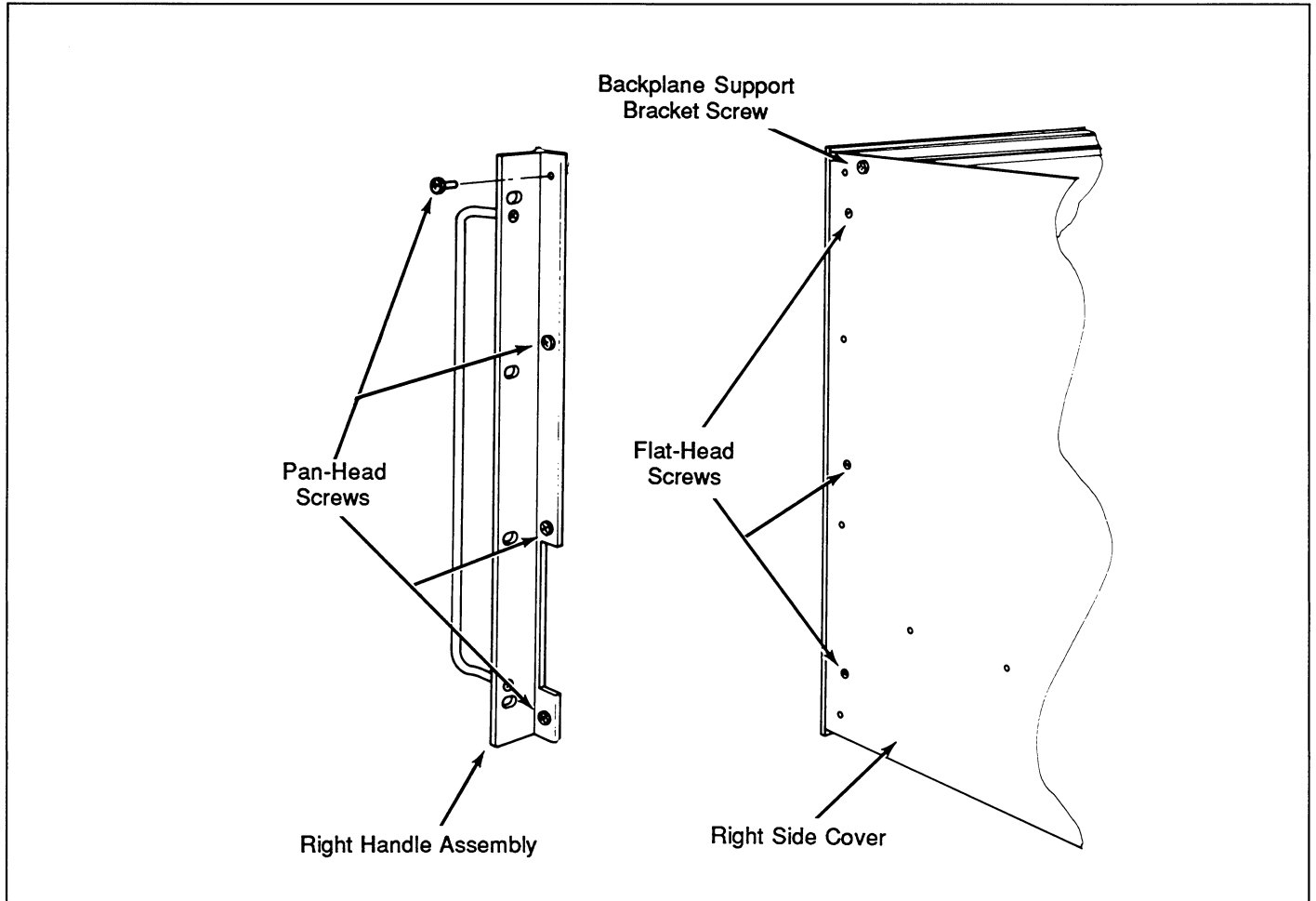


Figure 3-20. Handle and Front Panel Screws

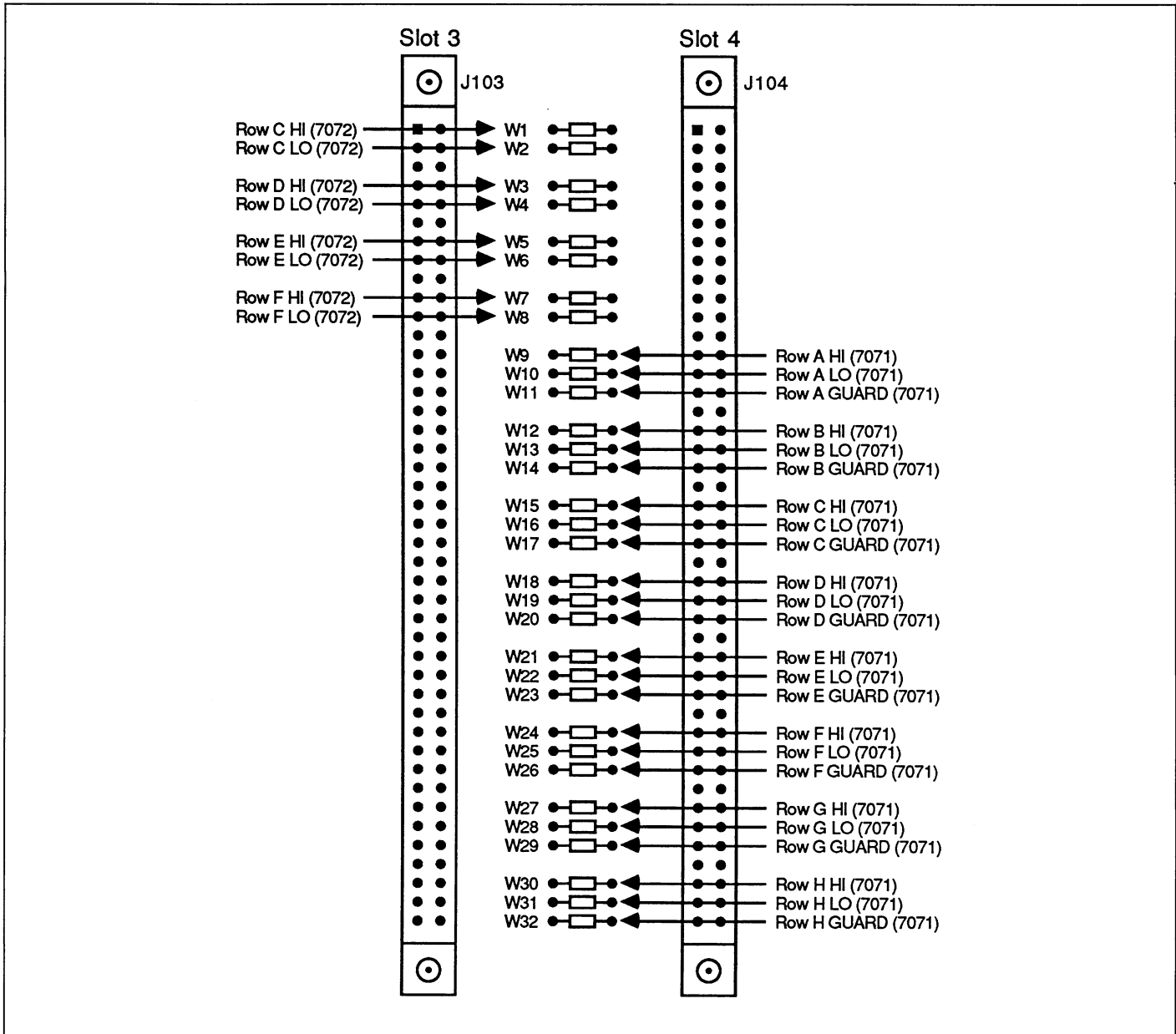


Figure 3-21. Backplane Jumpers

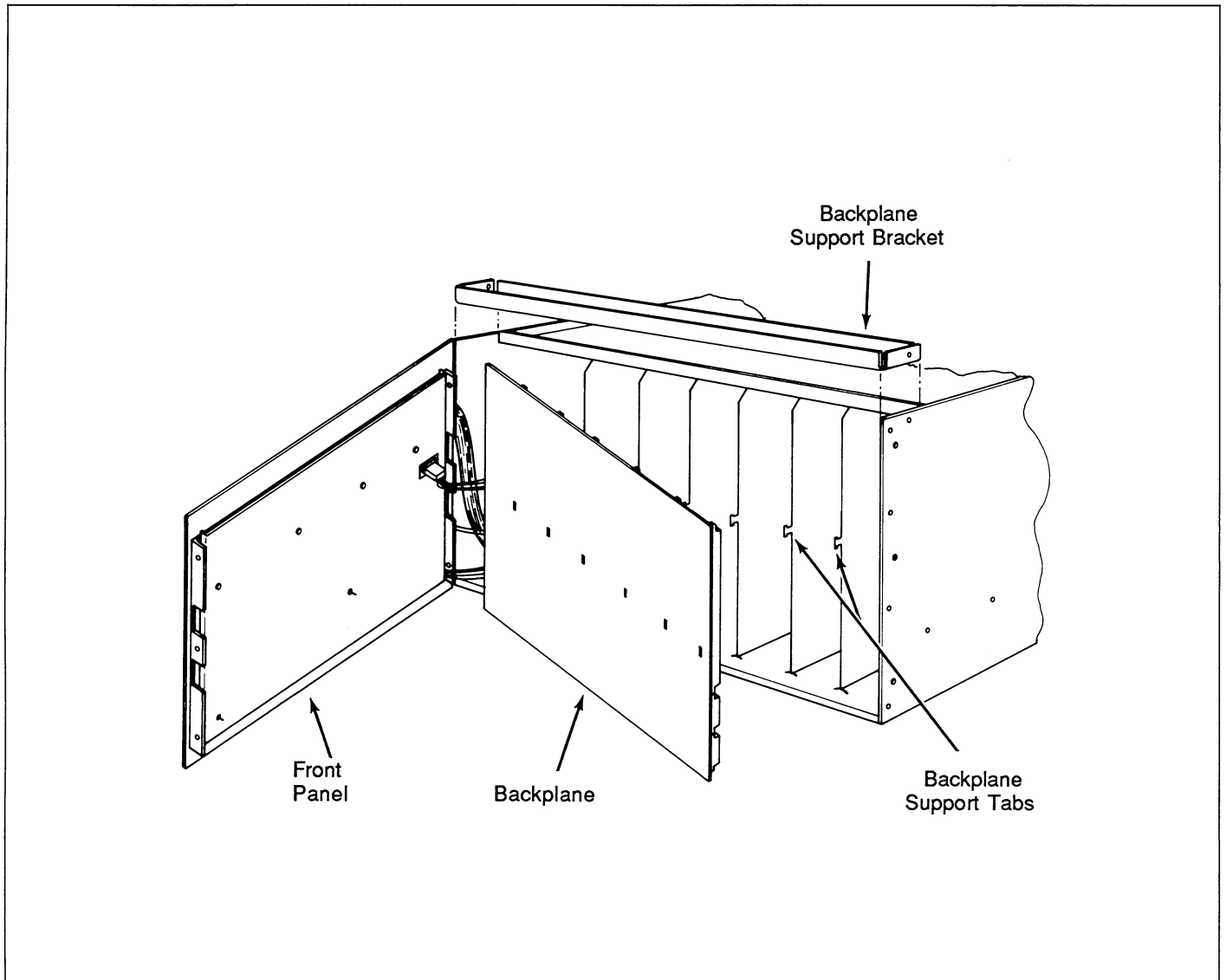


Figure 3-22. Backplane Removal

SECTION 4

IEEE-488 Programming

4.1 INTRODUCTION

This section contains information on programming the Model 707 over the IEEE-488 bus. Detailed instructions for all programmable functions are included, however, information concerning operating modes presented elsewhere is not repeated. Refer to Sections 2 and 3 for information not found in this section.

General information on the IEEE-488 bus is located in Appendix C.

4.2 A SHORT-CUT TO IEEE-488 OPERATION

The paragraphs below take you through a step-by-step procedure to get a Model 707 on the bus to program some basic commands.

Step 1: Connect Your Model 707 to the Controller

With power off, connect the Model 707 to the IEEE-488 interface of the controller using a standard interface cable. Some controllers include an integral cable, while others require a separate cable. Paragraph 4.3 discusses bus connections in detail.

Step 2: Select the Primary Address

The primary address is a way for the controller to refer to each device on the bus individually. Consequently, the primary address of your Model 707 must be the same as the primary address specified in the controller's programming language, or you cannot program the instrument. Keep in mind that each device on the bus must have a different primary address.

The primary address of your Model 707 is set to 18 at the factory, but you can program other values between 0 and 30 by pressing MENU until the present address is displayed and then using the numeric keys to change the primary address. Once the desired value is displayed, press ENTER to program the value. (More detailed information on primary address selection is in paragraph 4.5.)

Step 3: Write Your Program

Even the most basic operations require a simple program to send commands to the instrument. Figure 4-1 shows a flowchart of a program to select make/break and break/make rows, modify crosspoints of a setup stored in memory, send the setup to the relays, and then request data of the current relay setup.

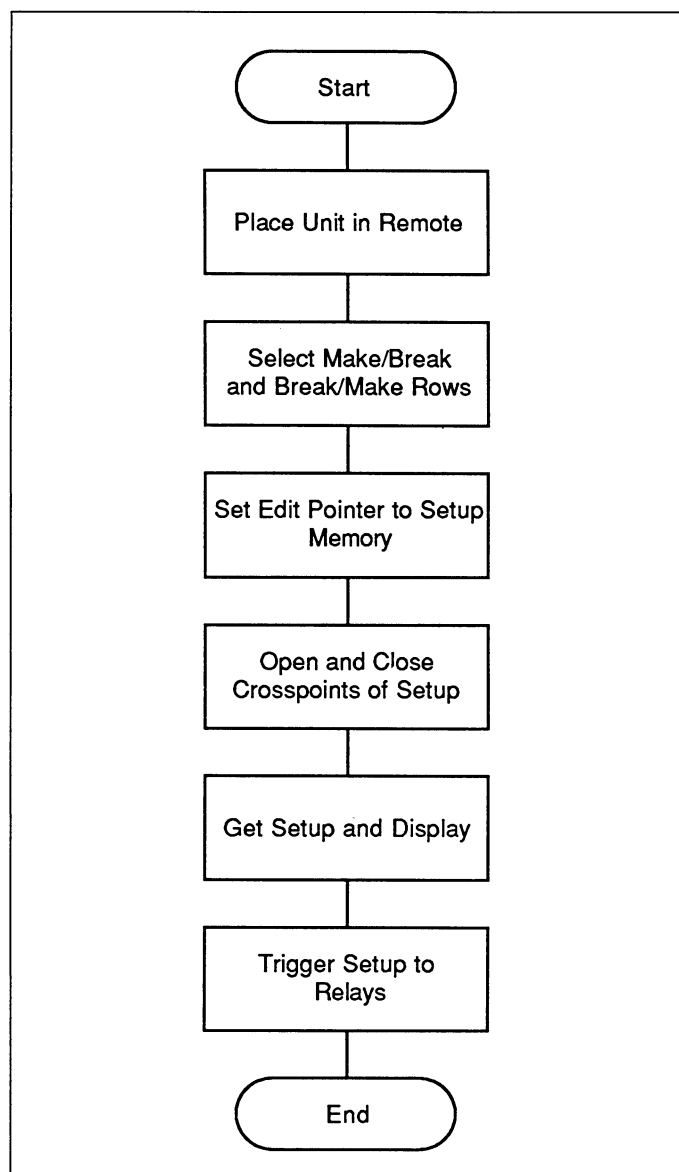


Figure 4-1. Flowchart of Example Program

The corresponding program, written in HP BASIC 4.0 for an HP-9000, Model 310 computer, is written in three parts for this example. The program assumes a primary IEEE-488 address of 18 for the Model 707 and that power-up default conditions exist in the unit.

PROGRAM	COMMENTS
10 DIM A#[200],C#[200]	! Dimension crosspoint input and display
20 REMOTE 718	! Tell 707 to listen over bus
30 OUTPUT 718;''U11000000W00000011X''	! Select A and B for make/break, G and H for break/make
40 OUTPUT 718;''E1H1X''	! Set edit pointer, display setup #1
50 INPUT ''CROSSPOINTS COMMAND'',C#	! Get crosspoint data
60 IF LEN(C#)=0 THEN 90	! Check for null string
70 OUTPUT 718;C#&''X''	! Send command string to 707
80 GOTO 50	! Get more crosspoint data
90 END	

Step 4: Open and Close Crosspoints

You can open, close, and clear crosspoints by sending the appropriate command, which is made up of an ASCII letter representing the command, followed by one or more characters for the command options. A number of commands can be grouped together in one string, if desired. The command strings are not opening and closing relays unless the edit pointer of statement 40 is set to zero.

To open and close crosspoints over the bus, simply run the previous program and type in a command string when prompted. Some example strings are given below:

''P1''	Clear (open) all crosspoints of setup #1
''CA55,A56,B49,B50''	Set (close) crosspoints A55, A56, B49, B50
''NA55,A56''	Clear (open) crosspoints A55, A56
''CA1,A2NB49,B50''	Set (close) A1, A2 and clear (open) B49, B50

NOTE

Since HP BASIC 4.0 uses a comma to delimit its

INPUT statement, and a comma is also used to delimit multiple-option 707 commands, place quotes around the input string when typing in multiple-option commands. An alternative is to use the BASIC statement LINE INPUT rather than INPUT.

Terminate each string by pressing RETURN on the controller keyboard. If a null string is entered, the program ends.

Step 5: Modify Program for Requesting Data

To display or print setup data, you must specify one of the data output formats that sends ASCII characters. Note that a variety of data formats are available, as discussed in paragraph 4.9. The data can be of a setup stored in memory or the current relay setup. Modify the previous program with these statements:

90 OUTPUT 718;''U2,1G2X''	! Set data format for setup #1
100 ENTER 718;A#	! Get stored setup data
110 PRINT A#	! and print
120 END	

When the program is run with these statements, it lists the closed crosspoints that you have entered.

Step 6: Modify Program for Triggering

Triggers provide a quick means of copying relay data from a pre-programmed setup to the relays. Each valid trigger causes the next sequential setup to be copied to the relays and the relay pointer to be updated.

Modify the previous program with the following statements:

120 PAUSE	! Wait for controller's Continue key
130 OUTPUT 718;''F1T2X''	! Enable triggers, select trigger on GET
140 TRIGGER 718	! Trigger setup #1 to relays
150 END	

When the Continue key on the controller keyboard is pressed, this program modification triggers setup #1 to the relays. This is because the relay step pointer, which is different from the edit pointer, was set to zero by power-up.

4.3 BUS CONNECTIONS

The following paragraphs provide information needed to connect instrumentation to the IEEE-488 bus.

4.3.1 Cable Connections

The Model 707 is connected to the IEEE-488 bus through a cable equipped with standard IEEE-488 connectors. See Figure 4-2. Two screws are located on each connector to ensure that connections remain secure. Current standards call for metric threads, as identified by dark colored screws. (Earlier versions had silver colored screws. Do not use these connectors with the Model 707.)

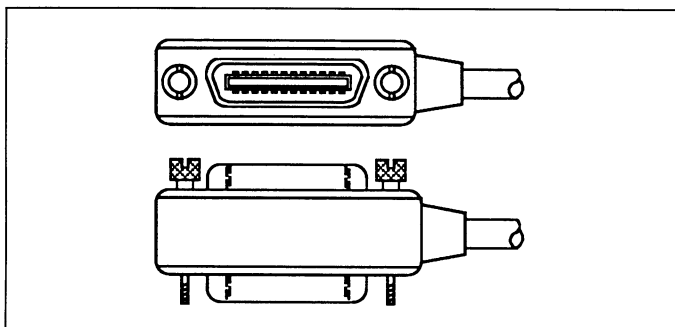


Figure 4-2. IEEE-488 Connector

A typical connecting scheme is shown in Figure 4-3. Each cable normally has a standard connector on each end. These connectors are designed to be stacked to allow a number of parallel connections on one instrument. To avoid possible damage, do not stack more than three connectors on any one instrument.

NOTE

To minimize interference caused by electromagnetic radiation, it is recommended that only shielded IEEE-488 cables be used. The Model 7007-1 and 7007-2 shielded IEEE-488 cables are available from Keithley Instruments.

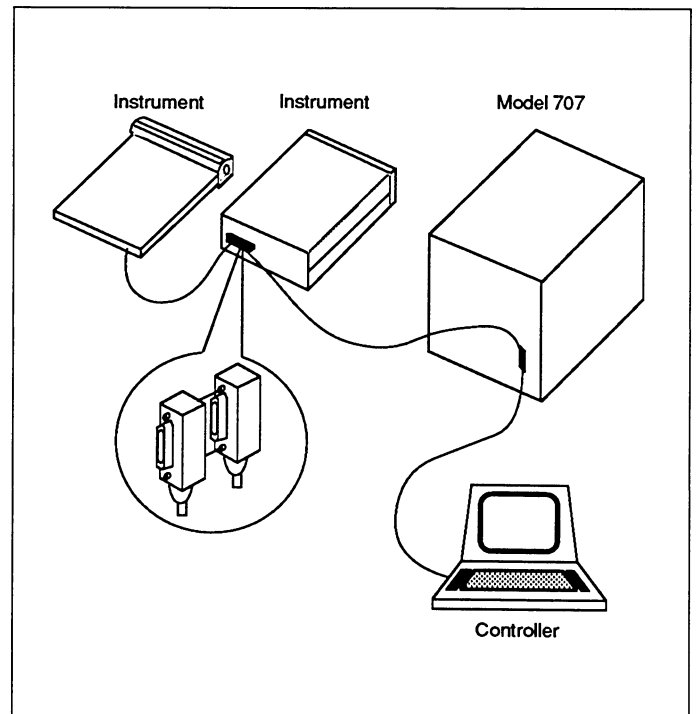


Figure 4-3. IEEE-488 Connections

Connect the cable to the Model 707 as follows:

1. Line up the connector on the cable with the connector on the rear panel of the instrument. Figure 4-4 shows the IEEE-488 connector location.
2. Tighten the screws securely, but do not overtighten them. They break rather easily.
3. Add additional connectors from other instruments, as required.
4. Make sure the other end of the cable is properly connected to the controller. Some controllers have an IEEE-488 type connector, while others do not. Consult the instruction manual of your controller for the proper connecting method.

NOTE

The IEEE-488 bus is limited to a maximum of 15 devices, including the controller. Also, the maximum cable length is limited to 20 meters, or 2 meters times the number of devices, whichever is less. Failure to heed these limits may result in erratic bus operation.

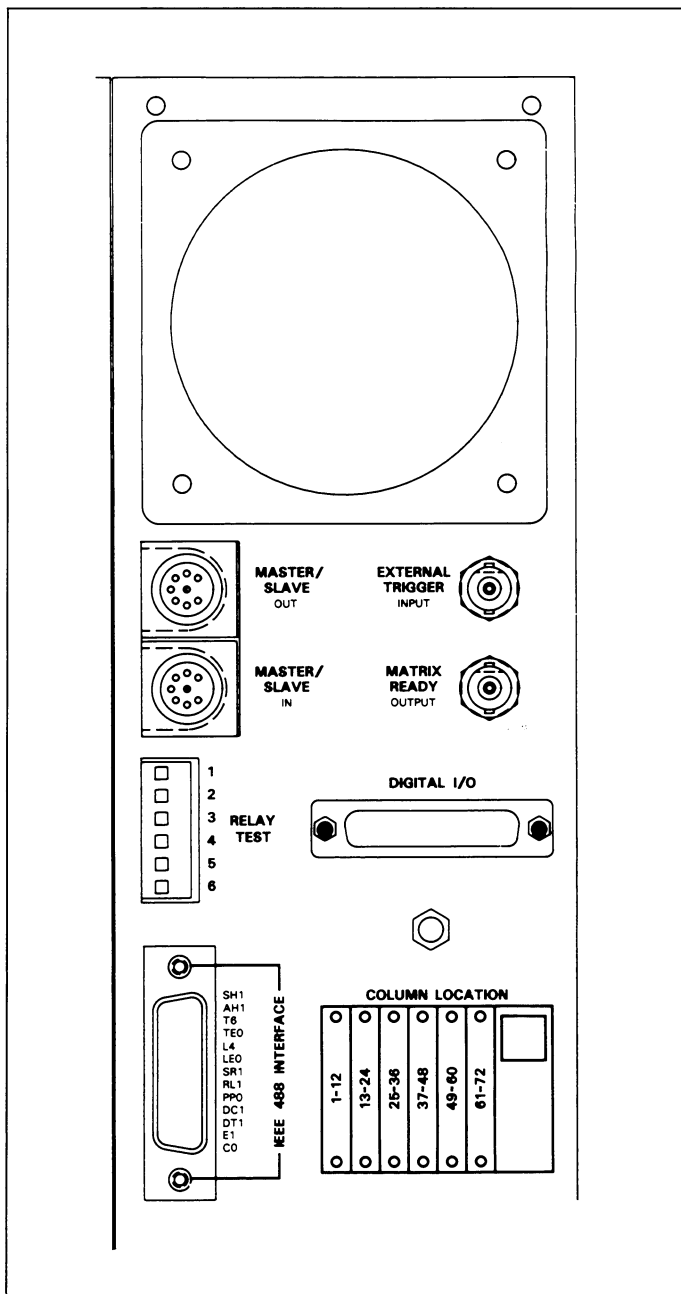


Figure 4-4. IEEE-488 Connector Location

NOTE

In master/slave configurations, only the master unit is connected to the IEEE-488 bus. If slave units are also connected, erratic bus operation results.

Custom cables may be constructed by using the contact assignments listed in Table 4-1 and shown in Figure 4-5.

Table 4-1. IEEE-488 Contact Designation

Number	Contact Designation	IEEE-488 Type
1	DIO1	Data
2	DIO2	Data
3	DIO3	Data
4	DIO4	Data
5	EOI (24)*	Management
6	DAV	Handshake
7	NRFD	Handshake
8	NDAC	Handshake
9	IFC	Management
10	SRQ	Management
11	ATN	Management
12	SHIELD	Ground
13	DIO5	Data
14	DIO6	Data
15	DIO7	Data
16	DIO8	Data
17	REN (24)*	Management
18	Gnd, (6)*	Ground
19	Gnd, (7)*	Ground
20	Gnd, (8)*	Ground
21	Gnd, (9)*	Ground
22	Gnd, (10)*	Ground
23	Gnd, (11)*	Ground
24	Gnd, LOGIC	Ground

*Number in parentheses refers to signal ground return of reference contact number. EOI and REN signal lines return on contact 24.

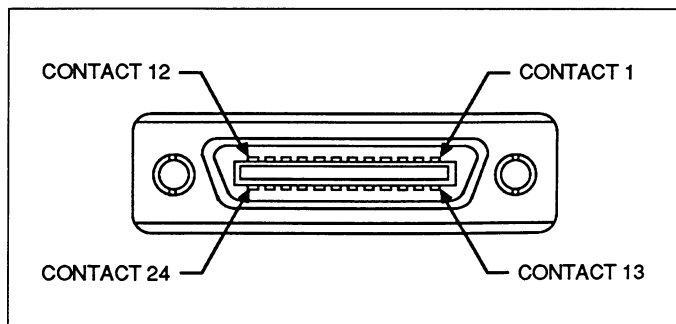


Figure 4-5. Contact Assignments

4.4 INTERFACE FUNCTION CODES

The interface function codes, which are part of the IEEE-488 standards, define an instrument's ability to support various interface functions. They should not be confused with programming commands found elsewhere in this manual. Interface function codes for the Model 707 are listed in Table 4-2 and are listed for convenience on the rear panel adjacent to the IEEE-488 connector. The codes define Model 707 capabilities as follows:

SH1 (Source Handshake)—SH1 defines the ability of the Model 707 to properly handshake data or command bytes when the unit is a source.

AH1 (Acceptor Handshake)—AH1 defines the ability of the Model 707 to properly handshake the bus when it is an acceptor of data or commands.

T6 (Talker)—The ability of the Model 707 to send data over the bus to other devices is defined by the T6 function. Model 707 talker capabilities exist only after the instrument has been addressed to talk. T6 means that the Model 707 is a basic talker, has serial poll capabilities, and is unaddressed to talk when it receives its own listen address.

TE0 (Extended Talker)—The Model 707 does not have extended talker capabilities.

L4 (Listener)—The L4 function defines the ability of the Model 707 to receive device-dependent data over the bus. Listener capabilities exist only after the instrument has been addressed to listen. L4 means that the Model 707 is a basic listener and is unaddressed to listen when it receives its own talk address.

LE0 (Extended Listener)—The Model 707 does not have extended listener capabilities.

SR1 (Service Request)—The SR1 function defines the ability of the Model 707 to request service from the controller.

RL1 (Remote Local)—The RL1 function defines the capabilities of the Model 707 to be placed in the remote or local states.

PP0 (Parallel Poll)—PP0 means that the Model 707 does not have parallel polling capabilities.

DC1 (Device Clear)—The DC1 function defines the ability of the Model 707 to be cleared (initialized).

DT1 (Device Trigger)—The ability for the Model 707 to have its setups triggered is defined by the DT function.

C0 (Controller)—The Model 707 has no controller capabilities.

E1 (Bus Driver Type)—The Model 707 has open-collector bus drivers.

Table 4-2. Model 707 Interface Function Codes

Code	Interface Function
SH1	Source handshake capability
AH1	Acceptor Handshake capability
T6	Talker (basic talker, serial poll, unaddressed to talk on MLA)
TE0	No Extended Talker capabilities
L4	Listener (basic listener, unaddressed to listen on MTA)
LE0	No Extended Listener capabilities
SR1	Service Request capability
RL1	Remote Local capability
PP0	No Parallel Poll capability
DC1	Device Clear capability
DT1	Device Trigger capability
C0	No Controller capability
E1	Open collector bus drivers

4.5 PRIMARY ADDRESS PROGRAMMING

The Model 707 must receive a listen command before it responds to addressed commands. Similarly, the unit must receive a talk command before it transmits its data. The Model 707 is shipped from the factory with a programmed primary address of 18. The programming examples included in this manual assume that address.

The primary address may be set to any value between 0 and 30 as long as address conflicts with other instruments are avoided. Note that controllers are also given a primary address, so do not use that address either. Most frequently, controller addresses are 0 or 21, but you should consult the controller's instruction manual for details. Whatever primary address you choose, you must make certain that it corresponds with the value specified as part of the controller's programming language.

To check the present primary address or to change to a new one, perform the following procedure:

1. Press the MENU button until the current primary address is displayed. For example, if the instrument is set to primary address 18, the following message is displayed:

IEEE-488 18

2. To retain the current address, press CANCEL to exit from the menu.
3. To change the primary address, use the numeric buttons and press ENTER. This stores the new address in memory so that the instrument powers up to that address.

NOTE

Each device on the bus must have a unique primary address. Failure to observe this precaution will probably result in erratic bus operation.

4.6 CONTROLLER PROGRAMMING

The programming instructions covered in this section use examples written with Hewlett Packard BASIC version 4.0. This language was chosen because of its versatility in controlling the IEEE-488 bus. This section covers those statements that are essential to Model 707 operation.

NOTE

Controller programming information for using the IBM-PC interfaced through a Capital Equipment Corporation (CEC) IEEE-488 interface is located in Appendix B.

A partial list of HP BASIC 4.0 statements is shown in Table 4-3.

Table 4-3. HP BASIC 4.0 IEEE-488 Statements

Action	BASIC Statement
Transmit string to device 18.	OUTPUT 718;A\$
Obtain string from device 18.	ENTER 718;A\$
Send GTL to device 18.	LOCAL 718
Send SDC to device 18.	CLEAR 718
Send DCL to all devices.	CLEAR 7
Send remote enable.	REMOTE 7
Cancel remote enable.	LOCAL 7
Serial poll device 18.	SPOLL (718)
Send local lockout.	LOCAL LOCKOUT 7
Send GET to device 18.	TRIGGER 718
Send IFC.	ABORT 7

4.7 FRONT PANEL ASPECTS OF IEEE-488 OPERATION

The following paragraphs discuss aspects of the front panel that are part of IEEE-488 operation, including front panel error messages, IEEE-488 status indicators, and the LOCAL key.

4.7.1 Front Panel Error Messages

The Model 707 has a number of front panel messages associated with IEEE-488 programming. These messages, which are listed in Table 4-4, inform you of certain conditions that occur when sending device-dependent commands to the instrument.

The following paragraphs describe the front panel error messages associated with IEEE-488 programming. Note that the instrument may be programmed to generate an SRQ, and U command status words can be checked for specific error conditions if any of these errors occur. See paragraphs 4.9.13 and 4.9.21.

Table 4-4. Front Panel IEEE-488 Messages

Message	Description
CARD ID ERROR	Power up routine cannot identify one or more matrix cards.
IDDC	Illegal device-dependent command received.
IDDCO	Illegal device-dependent command option received.
M/S ERROR	Master/slave communication or timing error.
NOT IN REMOTE	X received while unit is in LOCAL state.
NOT SETTLED	Unit triggered before total settling time expired.
RAM FAIL	Power-up routine or self-test detected RAM error.
ROM FAIL	Power-up routine or self-test detected program ROM checksum error.
SETUP ERROR	Power-up routine detected checksum errors in one or more setups. (Affected setups are cleared.)
TRIG OVERRUN	Unit triggered before Ready bit is set.

Card Identification Error

A card ID error occurs when the instrument's power-up routine detects a checksum error in the information from one or more matrix cards. The cards in error are indicated by all LEDs in their crosspoint display blocks being lit.

The message remains displayed until the next front panel operation.

IDDC (Illegal Device-Dependent Command) Error

An IDDC error occurs when the unit receives an illegal device-dependent command over the bus. For example, the command string 1X includes an illegal command because the "1" is not part of the instrument's programming language. When an illegal command is received, the instrument briefly displays the IDDC error message.

NOTE

When an IDDC error is detected in a command string, all commands in the string, up to and including the next X, are ignored.

To correct the error condition, send only valid commands. Refer to paragraph 4.9 for device-dependent command programming details. Note that an IDDC error is also flagged in the U1 word, as discussed in paragraph 4.9.21.

Programming Example—To demonstrate an IDDC error, use the following statements:

```
REMOTE 718
OUTPUT 718; '*1X'
```

The IDDC error message is briefly displayed when the second statement above is executed.

IDDCO (Illegal Device-Dependent Command Option) Error

Sending the instrument a legal command with an illegal option results in the IDDCO error message being displayed.

For example, the command K9X has an illegal option (9) that is not part of the instrument's programming language. Thus, although the command K is valid, the option is not, and the IDDCO error results.

NOTE

When an IDDCO error is detected in a command string, all commands in the string, up to and including the next X, are ignored.

To correct this error condition, use only valid command options, as discussed in paragraph 4.9. Note that an IDDCO error is also flagged in the U1 word, as discussed in paragraph 4.9.21.

Programming Example—Demonstrate an IDDCO error with the following statements:

```
REMOTE 718
OUTPUT 718; '*K9X'
```

Note that the IDDCO error message is briefly displayed when the second statement above is executed.

Master/Slave Error

A master/slave error occurs when a communication or timing error is detected in the closed loop of units. If one or more errors are detected, the message M/S ERROR is displayed.

To simulate the error condition, disconnect a DIN cable from either of the MASTER/SLAVE connectors. The message is displayed when the Model 707 performs the next operation that requires communication among the units.

Not in Remote Error

A not in remote error occurs if the instrument receives an "X" while it is in the local state. The NOT IN REMOTE error message is displayed on the alphanumeric display. This is caused by failing to set the REN line true before addressing the Model 707 to listen. (The HP9000 default is to set REN true.) The statement "REMOTE 7" sets the REN line true; the statement "REMOTE 718" sets the REN line true and addresses unit 18 to listen.

Programming Example—To demonstrate the NOT IN REMOTE error message, type in the following lines:

```
LOCAL 7
OUTPUT 718; '*A1X'
```

Note that the NOT IN REMOTE error message is briefly displayed when the program times out.

Not Settled Error

A not settled error occurs when the instrument receives an additional trigger before the settling time has expired. This time period is after assertion of the READY signal and before assertion of the MATRIX READY signal. (See paragraph 3.11 for a complete discussion of trigger timing.) Both READY and MATRIX READY are bits in the SPOLL byte; MATRIX READY is also a rear panel signal.

A trigger during this time period is processed normally. When such a trigger condition occurs, the instrument briefly displays the NOT SETTLED error message.

Programming Example—To demonstrate a not settled situation, type in the following statements:

```
REMOTE 718
OUTPUT 718: 'S10X' ! Set programmed settle
                    ! time of 10msec
OUTPUT 718: 'F1T4XXX' ! Enable triggers, select
                    ! trigger on X, trigger 3
                    ! times
```

Note that the NOT SETTLED message is briefly displayed when the third statement above is executed.

RAM or ROM Failure

A RAM or ROM failure occurs when the power-up routine detects an error, either a RAM error, or a checksum error in program ROM. If an error is detected, a RAM FAIL or ROM FAIL message is displayed and the Model 707 waits for a key press.

Setup Error

A setup error occurs when the Model 707 power-up routine detects a checksum error in one or more setups stored in non-volatile memory. While the message SETUP ERROR is displayed, the affected setups are cleared to all open, then the Model 707 waits for a key press.

Trigger Overrun (Hardware) Error

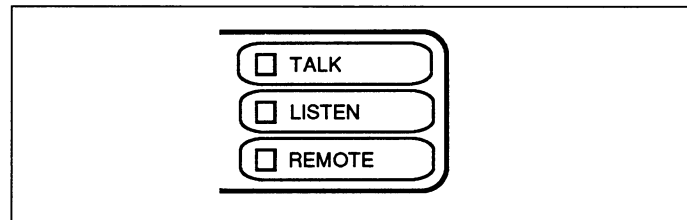
A trigger overrun occurs when the instrument is triggered while it is still processing a setup change from a previous

trigger and before the READY (for trigger) signal is asserted. (READY is a bit in the SPOLL byte. See paragraph 3.11 for a complete discussion of trigger timing.) The exact trigger stimulus depends on the selected trigger source, as discussed in paragraph 4.9.

Overrun triggers do not affect the instrument except to generate the error message. In other words, the present setup change is not aborted by the overrun trigger stimulus and the trigger is ignored. When such a trigger overrun condition occurs, the instrument briefly displays the TRIG OVERRUN message.

4.7.2 IEEE-488 Status Indicators

The TALK, LISTEN, and REMOTE indicators show the present IEEE-488 status of the instrument. Each of these indicators is briefly described below.



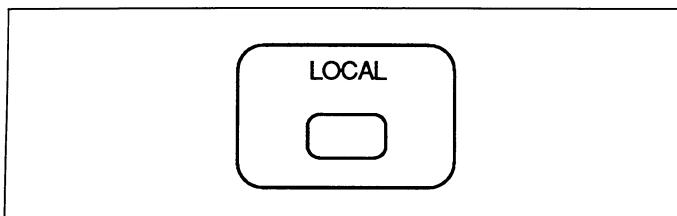
TALK—This indicator is on when the instrument is in the talker active state. The unit is placed in this state by addressing it to talk with the correct MTA (My Talk Address) command. TALK is off when the unit is in the talker idle state. The instrument is placed in the talker idle state by sending it an UNT (Untalk) command, addressing it to listen, or with the IFC (Interface Clear) command.

LISTEN—This indicator is on when the Model 707 is in the listener active state, which is activated by addressing the instrument to listen with the correct MLA (My Listen Address) command. Listen is off when the unit is in the listener idle state. The unit can be placed in the listener idle state by sending UNL (Unlisten), addressing it to talk, or by sending IFC (Interface Clear) over the bus.

REMOTE—As the name implies, this indicator shows when the instrument is in the remote state. Note that REMOTE does not necessarily indicate the state of the REN line, as the instrument must be addressed to listen with REN true before the REMOTE indicator turns on. When the instrument is in remote, all front panel keys except for the LOCAL key are locked out. When REMOTE is turned off, the instrument is in the local state, and front panel operation is restored.

4.7.3 LOCAL key

The LOCAL key cancels the remote state and restores local operation of the instrument.



Since all front panel keys except LOCAL are locked out when the instrument is in remote, this key provides a convenient method of restoring front panel operation. Pressing LOCAL also turns off the REMOTE indicator, and returns the display to normal if a message was displayed with the D command. (See paragraph 4.9.4.)

Note that the LOCAL key is also inoperative if the LLO (Local Lockout) command is in effect.

4.7.4 Simultaneous Front Panel and Bus Operation

Fundamentally, there is no reason why you cannot con-

trol the instrument simultaneously from both the front panel and over the IEEE-488 bus. However, the following points should be kept in mind.

1. All front panel keys except for LOCAL are inoperative while the Model 707 is in remote (REMOTE on). The unit is placed in remote by addressing it to listen with the REN line true. Thus, to control the unit from the front panel, it is necessary to press LOCAL after programming over the bus. Note that LOCAL is also inoperative if the LLO (Local Lockout) command is in effect.
2. Front panel parameter modification should always be completed before attempting to use bus control. For example, you should not attempt to program a setup over the bus while editing a setup from the front panel.

4.8 GENERAL BUS COMMAND PROGRAMMING

General bus commands are those commands such as DCL that have the same general meaning regardless of the instrument. Commands supported by the Model 707 are listed in Table 4-5 which also lists BASIC statements necessary to send each command. Note that commands requiring that a primary address be specified assume that the Model 707 primary address is set to 18 (its factory default address).

Table 4-5. General Bus Commands and Associated BASIC Statements

Command	HP BASIC 4.0 Statement	Effect on Model 707
REN	REMOTE 7	Goes into effect when next addressed to listen.
IFC	ABORT 7	Goes into talker and listener idle states.
LLO	LOCAL LOCKOUT 7	LOCAL key locked out.
GTL	LOCAL 718	Cancel remote, restore front panel operation.
DCL	CLEAR 7	Returns to default conditions.
SDC	CLEAR 718	Returns to default conditions.
GET	TRIGGER 718	Triggers setup change with GET source.

4.8.1 REN (Remote Enable)

The remote enable command is sent to the Model 707 by the controller to set up the instrument for remote operation. Generally, the instrument should be placed in the remote state before you attempt to program it over the bus. Simply setting REN true does not actually place the instrument in the remote state. Instead the instrument must be addressed to listen after setting REN true before it goes into remote.

Note that the instrument need not be in remote to be a talker.

Programming Example—This sequence is automatically performed by computer when the following is typed into the keyboard:

```
REMOTE 718
```

After the RETURN key is pressed, the instrument is in the remote state, as indicated by the REMOTE light. If not, check to see that the instrument is set to the proper primary address 18, and check to see that the bus connections are properly made.

Note that all front panel controls except LOCAL (and, of course, POWER) are inoperative while the instrument is in remote. You can restore normal front panel operation by pressing the LOCAL key.

4.8.2 IFC (Interface Clear)

The IFC command is sent by the controller to place the Model 707 in the local, talker and listener idle states. The unit responds to the IFC command by cancelling front panel TALK or LISTEN lights, if the instrument was previously placed in one of those states.

Programming Example—Before demonstrating the IFC command, turn on the TALK indicator with the following statements:

```
ENTER 718;A#
```

At this point, the TALK light should be on. The IFC command can be sent by entering the following statement into the computer:

```
ABORT 7
```

After the RETURN key is pressed, the TALK indicator turns off, indicating that the instrument has gone into the talker idle state.

4.8.3 LLO (Local Lockout)

The LLO command is used to prevent local operation of the instrument. After the unit receives LLO, all of its front panel controls except POWER are inoperative.

Programming Example—The LLO command is sent by entering the following statement:

```
REMOTE 718  
LOCAL LOCKOUT 7
```

After the second statement is executed, the instrument's front panel controls are locked out, including the LOCAL key.

To restore front panel operation after asserting LLO, as in the following example:

```
LOCAL 7
```

4.8.4 GTL (Go To Local) and Local

The GTL command is used to take the instrument out of the remote state. Operation of the front panel keys will also be restored by GTL unless LLO is in effect. To cancel LLO, you must set REN false.

Programming Example—Place the instrument in the remote state with the following statement:

```
REMOTE 718
```

Now send GTL with the following statement:

```
LOCAL 718
```

Note that the instrument goes into the local state, and that operation of the front panel keys has now been restored (assuming that LLO is not in effect).

4.8.5 DCL (Device Clear)

The DCL command may be used to clear the Model 707 and return it to its power-up default conditions (see Table 4-6). Note that the DCL command is not an addressed command, so all instruments equipped to implement DCL will do so simultaneously. When the Model 707 receives a DCL command, it returns to the power-up default conditions. DCL does not affect the programmed primary address.

Programming Example—Place the unit in an operating mode that is not a power-up default condition. Now enter the following statement from the controller keyboard:

```
CLEAR 7
```

When the RETURN key is pressed, the instrument returns to the power-up default conditions.

4.8.6 SDC (Selective Device Clear)

The SDC command is an addressed command that performs essentially the same function as the DCL command. However, since each device must be individually addressed, the SDC command provides a method to clear only selected instruments instead of clearing all instruments simultaneously, as is the case with DCL. Any devices on the bus that are addressed to listen are cleared. When the Model 707 receives the SDC command, it returns to the power-up default conditions.

Programming Example—Place the unit in an operating mode that is not a power-up default condition. Now enter the following statement from the controller keyboard:

```
CLEAR 718
```

This sends the following IEEE-488 commands: UNT UNL LISTEN 18 SDC. When the statement is executed, the instrument(s) returns to the default configuration.

Table 4-6. Factory Default, Power-up, and DCL/SDC Conditions

Parameter	Factory Default	Power-up, DCL/SDC	Description
Relays	All opened	All opened	—
Stored Setups	All cleared	Not affected	—
Relay Step	000	000	Point to relays
Memory Step	001	001	Point to setup 1
Master/Slave	Stand-alone	(Notes 2, 3)	—
IEEE-488 Address	18 (Note 1)	Not affected	—
External Trigger	A0	A0	Falling edge triggers
Matrix Ready	B0	B0	Negative true
Display	DX	DX	Normal alphanumeric display
Edit Pointer	E0	E0	Point to relays
Trigger Enable	F0	F0	Triggers disabled
Data Format	G0	G0	Full output, all data sent in one talk
EOI/Hold-off	K0	K0	Both enabled
SRQ	M0	M0	Disabled
Digital Output	O000	O000	Output lines low
Programmed Settling Time	S0	S0	0msec
Trigger Source	T7	T7	External trigger
Make/Break Rows	V00000000	Not affected	None selected
Break/Make Rows	W00000000	Not affected	None selected
Terminator	Y0	Y0	<CR> <LF>

Notes:

1. The IEEE-488 address is not affected by the Restore (R0) command.
2. Units that previously were stand-alone or slave power up as stand-alone. They become slave units when a master unit initializes a master/slave loop, either upon power up or with a menu selection.
3. DCL/SDC does not affect master/slave state. DCL/SDC does clear slaves.

4.8.7 GET (Group Execute Trigger)

GET may be used to initiate a Model 707 setup change if the instrument is placed in the appropriate trigger source. More information on triggering is located in paragraph 4.9.

Programming Example—With triggers enabled, type in the following statements to place the instrument in the correct trigger source for purposes of this demonstration:

```
REMOTE 718
OUTPUT 718; 'T2F1X'
```

Now trigger the setup change by sending GET with the following statement:

```
TRIGGER 718
```

This sends IEEE-488 commands UNT UNL LISTEN 18 GET. When the RETURN key is pressed, the setup change is triggered. (The command TRIGGER 7 just sends GET. Any other listeners are triggered when the RETURN key is pressed.)

4.8.8 SPE, SPD (Serial Polling)

The serial polling sequence is used to obtain the Model 707 serial poll byte. The serial poll byte contains important information about internal functions, as described in paragraph 4.9.13. Generally, the serial polling sequence is used by the controller to determine which of several instruments has requested service with the SRQ line. However, the serial polling sequence may be performed at any time to obtain the serial poll byte from the Model 707.

Programming Example—The SPOLL statement automatically performs the serial poll sequence. To demonstrate serial polling, enter the following statements into the computer:

```
S=SPOLL (718)
PRINT 'SPOLL BYTE=';S
```

After the first statement, the controller conducts the serial polling sequence. After the second statement is executed, the decimal value of the serial poll byte is displayed on the controller CRT.

4.9 DEVICE-DEPENDENT COMMAND PROGRAMMING

IEEE-488 device-dependent commands are the most important commands associated with instrument programming because they control most instrument operating modes. All front panel modes (such as trigger source and settling time), as well as some modes not available from the front panel (like SRQ and terminator) can be programmed with these commands. Equivalent device-dependent commands for front panel keys are listed in Appendix D.

Command Syntax Each command is made up of a single ASCII capital letter followed by one or more numbers or letters representing an option or options of that command. For example, the trigger source can be set over the bus by sending the letter "T" followed by a number representing the trigger option. T1X would be sent to trigger on talk. The IEEE-488 bus actually treats these commands as data, in that they are sent with the ATN line false.

Some commands permit more than one option; these must be separated with commas. As an example, the close crosspoints commands has the general format as shown:

$$\text{Crc}(,rc)\dots(rc)$$

Here the "rc" options are row and column addresses, while the commas indicate the necessary delimiters. The parentheses indicate that the option and associated delimiter are optional.

NOTE

Do not include parentheses in actual command strings.

Multiple Commands A number of commands can be grouped together in one command string, which is generally terminated by the "X" character. This character tells the instrument to execute the command or command string as described in paragraph 4.9.24. Commands sent without the X character are not executed at that particular time, but they are stored within an internal command buffer for later execution when the X character is finally received.

If a particular command occurs "n" times in a command string, then the "nth" occurrence is the only one used, i.e., T0T2T4X appears to the Model 707 as T4X.

Invalid Commands If an invalid command is sent as part of the command string, no commands in the string are executed. Under these conditions, the instrument displays a front panel error message (IDDC or IDDCO), as covered in paragraph 4.7, and it can be programmed to generate an SRQ (Service Request), as discussed in paragraph 4.9.13. Checking is done as syntactical groups of characters are received.

Some typical examples of valid command strings include:

A0X	Single command string.
A0T6X	Multiple command string.
P 0X	Space is ignored.
Z15,0X	Multiple-option command string (options separated by commas).

Typical invalid command strings include:

1X	Invalid command as 1 is not a valid command.
K7X	Invalid command option as 7 is not a valid option of the K command.
CA400X	Invalid option (maximum column address is 360).
Z0100X	Multiple-option command without the necessary separating commas.

Order of Command Execution Device-dependent commands are not necessarily executed in the order received. Rather, each instrument always executes them in a specific order. The order of execution for the Model 707 is summarized in Table 4-7. Note that the X command is listed first since it is the character that forces the execution of the rest of the commands.

If you wish to force a particular order of execution, simply include the execute (X) character after each command option grouping in the command string. For example, the following string would be executed in the received order:

T6XA1XR0X

Table 4-7. Order of Command Execution

Order	Command	Description
1	X	Execute DDCs
2	R	Restore factory default conditions
3	L	Download setups from controller to Model 707
4	E	Set the edit pointer
5	I	Insert a blank setup in memory
6	Q	Delete a setup from memory
7	P	Clear all crosspoints at specified setup
8	Z	Copy a setup from memory or relays to memory or relays
9	V	Select rows from make/break
10	W	Select rows for break/make
11	N	Open crosspoints of setup indicated by edit pointer
12	C	Close crosspoints of setup indicated by edit pointer
13	A	Select trigger edge of External Trigger pulse
14	B	Select logic sense of Matrix Ready signal
15	D	Display a user message
16	F	Enable/disable triggers
17	G	Select data output format
18	J	Execute ROM/RAM/display self-test
19	K	Select EOI and hold-off on X
20	M	Set the SRQ mask
21	O	Set the digital output
22	S	Program the settling time
23	T	Select the trigger source
24	U	Request status
25	Y	Select terminator characters
26	H	Hit a front panel key

Device-dependent Command Summary All Model 707 device-dependent commands are summarized in Table 4-8, which also lists respective paragraphs where more detailed information on each command may be found.

Table 4-8. Device-Dependent Command Summary

Mode	Command	Description	Para.
External Trigger	A0 A1	Falling edge triggers Model 707 Rising edge triggers Model 707	4.9.1
Matrix Ready	B0 B1	Negative true Matrix Ready output Positive true Matrix Ready output	4.9.2
Close Crosspoint	Crc(rc)...(,rc)	Close crosspoints of setup indicated by edit pointer (rows A-H, cols. 1-360)	4.9.3
Display	Dcccccccccccc DX	Display ASCII characters (14 max.) Return alphanumeric display to normal	4.9.4
Edit Pointer	E0 En	Point to current relay setup Point to stored relay setup (1-100)	4.9.5
Enable/Disable Triggers	F0 F1	Disable triggers Enable triggers	4.9.6
Data Format	G0 G1 G2 or G3 G4 G5 G6 G7	Full output, all data in one talk Full output, one mainframe row per talk Inspect output, all data in one talk Condensed output, all data in one talk Condensed output, one mainframe per talk Binary output, all data in one talk Binary output, one mainframe per talk	4.9.7
Hit Key	Hn	Emulate front panel key press (1-41)	4.9.8
Insert Blank Setup	In	Insert blank setup in memory (1-100)	4.9.9
Self-test	J0	Perform self-test	4.9.10
EOI and Hold-off	K0 K1 K2 K3 K4 K5	Send EOI, hold-off on X until Ready No EOI, hold-off on X until Ready Send EOI, do not hold-off on X No EOI, do not hold-off on X Send EOI, hold-off on X until Matrix Ready No EOI, hold-off on X until Matrix Ready	4.9.11
Download Setups	Lbbb...X	Download setups from controller to Model 707	4.9.12
SRQ	M0 M1 M2 M4 M8 M16 M32 M128	SRQ disabled not used Front panel key press Digital I/O interrupt Matrix Ready Ready for trigger Error not used	4.9.13
Open Crosspoint	Nrc(rc)...(,rc)	Open crosspoints of setup indicated by edit pointer (rows A-H, cols. 1-360)	4.9.14
Digital Output	Onnn	Set states of digital output lines (000-255)	4.9.15

Table 4-8. Device-Dependent Command Summary (Cont.)

Mode	Command	Description	Para.
Clear Crosspoints	P0 Pn	Open all crosspoint relays Clear all crosspoints of stored setup (1-100)	4.9.16
Delete Setup	Qn	Delete setup from memory (1-100)	4.9.17
Restore Defaults	R0	Restore factory defaults	4.9.18
Programmed Settling Time	Sn	Program settling time in msec (0-65000)	4.9.19
Trigger	T0 or T1 T2 or T3 T4 or T5 T6 or T7 T8 or T9	Trigger on talk Trigger on GET Trigger on X Trigger on External Trigger pulse Trigger on front panel MANUAL key only	4.9.20
Status	U0 U1 U2,n U3 U4 U5,u U6 U7 U8	Send machine status word Send error status word Output setup "n" (0-100) with current G format Send RELAY STEP pointer Send number of slaves Send model number of each card in unit "u" (0-4) Send relay settling time Send digital input of unit Send RELAY TEST input	4.9.21
Make/Break	Vabcdefgh	Select rows for make/break operation (00000000 - 11111111)	4.9.22
Break/Make	Wabcdefgh	Select rows for break/make operation (00000000 - 11111111)	4.9.23
Execute	X	Execute commands	4.9.24
Terminator	Y0 Y1 Y2 Y3	<CR> <LF> <LF> <CR> <CR> <LF>	4.9.25
Copy Setup	Z0,n Zn,0 Zm,n	Copy current relay setup to memory location "n" (1-100) Copy setup from memory location "n" (1-100) to relays Copy setup from location "m" (0-100) to location "n" (0-100)	4.9.26

4.9.1 A - External Trigger

Purpose To select which edge of an external trigger pulse initiates a trigger.

Format An

Parameters A0 = Falling edge triggers Model 707
A1 = Rising edge triggers Model 707

Default Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to A0 (falling edge).

Description The A command allows you to program the Model 707 for triggering on a TTL-compatible falling or rising edge signal at the External Trigger input jack. A trigger signal increments the RELAY STEP pointer and copies the setup indicated by the new value from memory to the relays.

Figure 4-6 shows example trigger pulses. Trigger on external must be the selected source (T command) and triggers must be enabled (F command).

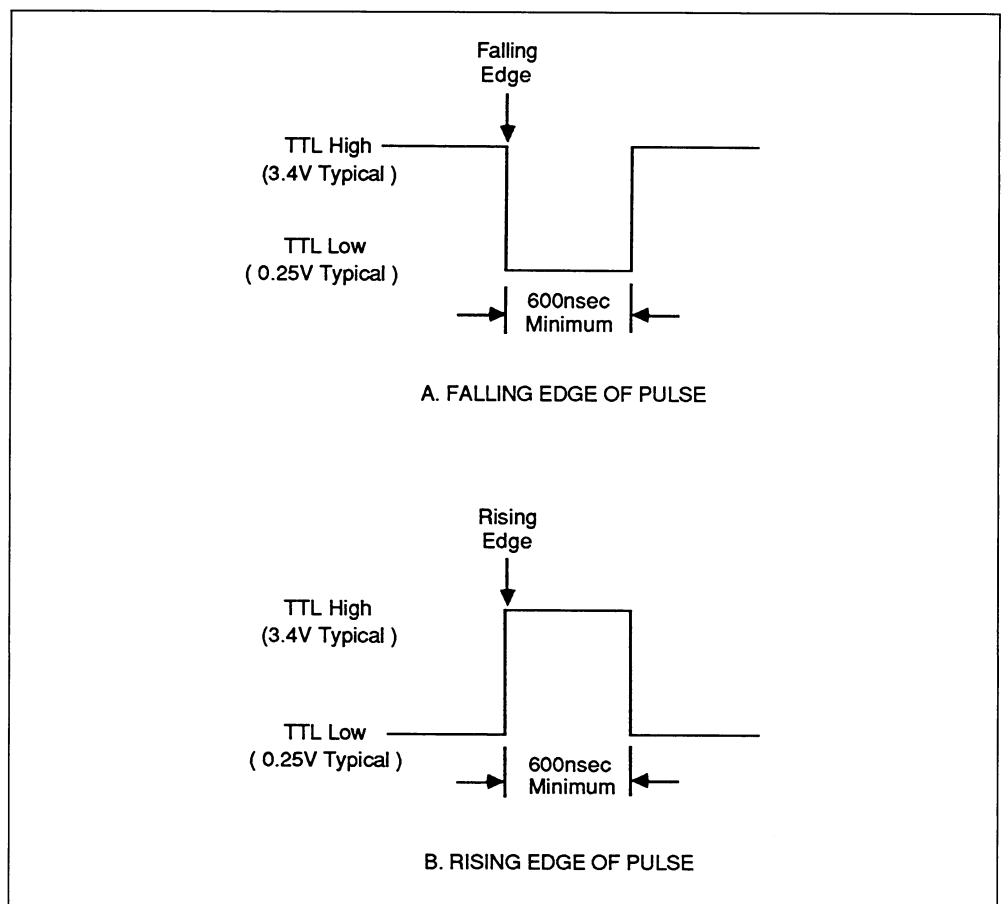


Figure 4-6. External Trigger Pulse

Programming Note This command is equivalent to a front panel menu selection.

Programming Examples

10	OUTPUT 718;''A1X''	! Select rising edge pulse to trigger
20	OUTPUT 718;''A0X''	! Select falling edge to trigger

4.9.2 B - Matrix Ready

- Purpose** To select the logic sense of the rear panel Matrix Ready signal.
- Format** **Bn**
- Parameters** B0 = Negative true Matrix Ready output
B1 = Positive true Matrix Ready output
- Default** Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to B0 (negative true).
- Description** The B command allows you to program the TTL-compatible Matrix Ready output as a high- or low-true signal. This signal goes false when the relays are switched; it goes true after completion of the (hardware) relay settling time and (user) programmed settling time. Figure 4-7 shows example Matrix Ready signals.

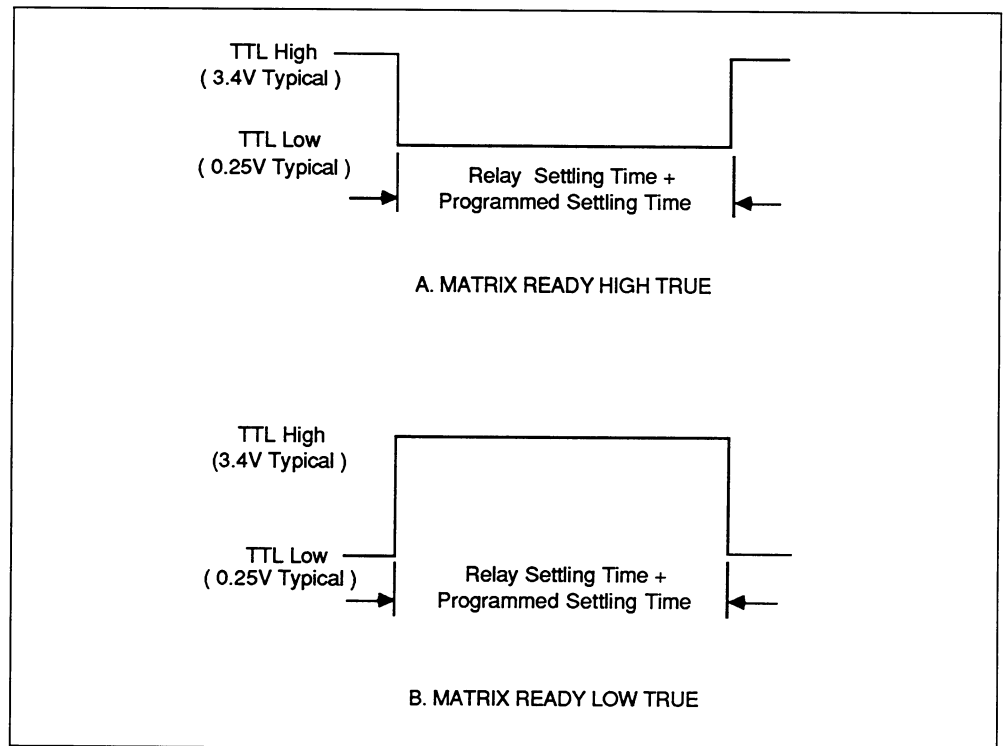


Figure 4-7. Matrix Ready Pulse

- Programming Notes**
1. The Matrix Ready signal is negated by anything that causes a change to a relay state even if no relays actually change state (e.g., closing an already closed relay).
 2. Changing the logic sense of the Matrix Ready signal does not change the logic sense of the Matrix Ready bit in the serial poll byte.
 3. This command is equivalent to a front panel menu selection.

Programming Examples

```
10 OUTPUT 718; 'B1X' ! Select positive true Matrix Ready
20 OUTPUT 718; 'B0X' ! Select negative true Matrix Ready
```

4.9.3 C - Close Crosspoint

Purpose To close crosspoints in a setup.

Format **Crc(rc)...(rc)**

Parameters r = Row A to row H
c = Column 1 to column 360

Description The C command closes crosspoints in the setup indicated by the edit pointer. If the edit pointer indicates the current relay setup (zero), the specified crosspoint relays are closed immediately. If the edit pointer indicates a setup stored in memory (1-100), the specified crosspoints are set.

Programming Notes

1. Do not include parentheses in command strings. They indicate that the option and associated comma delimiter are optional.
2. Up to 25 crosspoints per mainframe can be specified in one close command (with a master and four slaves, the limit is 125 crosspoints). In the same command string, up to 25 crosspoints per mainframe can be opened. If either limit is exceeded, an IDDCO results.
3. The maximum value of the column parameter depends on the configuration (72 for stand-alone, 360 for master with four slave units). An IDDCO results if the maximum value is exceeded.
4. This command is equivalent to multiple front panel key presses or light pen operation(s).

Programming Examples

```
10 OUTPUT 718; 'CA55X' ! Close one crosspoint
20 OUTPUT 718; 'CA55,A56,B49,B50X' ! Close multiple crosspoints
```

4.9.4 D - Display

Purpose To write messages on the front panel alphanumeric display of a stand-alone or master unit.

Format **Dcccccccccccccc**

Parameters c = ASCII character (14 maximum)

Default Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to DX (return alphanumeric display to normal operation).

Description The D command allows you to display messages on the front panel alphanumeric display of a stand-alone or master Model 707. To send a message, simply follow the D command with appropriate ASCII characters. Many displayable ASCII characters can be sent, including numbers or upper case characters. Characters that can be displayed include: 0-9, A-Z, arithmetic and most punctuation symbols.

Programming Notes

1. As with other device-dependent commands, the D command string should be terminated with the X.
2. Spaces in the command string are displayed.
3. The maximum number of characters is 14; any extra characters in the string are ignored. If there are fewer than 14 characters between the D and X, the characters are left-justified and the rest of the display is blank.
4. To return the alphanumeric display to normal, send DX, perform device clear, or return the Model 707 to the local state.

Programming Example `10 OUTPUT 718; 'DMODEL 707X'` ! Display MODEL 707 message

4.9.5 E - Edit Pointer

Purpose	To set the edit pointer.
Format	En
Parameters	n = 0 Current relay setup n = 1 to 100 Stored relay setup
Default	Upon power-up, or after receiving a DCL, SDC or R0X command, the instrument defaults to E0 (current relay setup).
Description	With the edit pointer, you can select which setup is affected by subsequent close (C) and open (N) commands. This can be the current relay setup (zero) or one of the stored setups (1-100).
Programming Notes	<ol style="list-style-type: none">1. The edit pointer is not reflected on the front panel display (i.e., MEMORY or RELAYS LED). The edit pointer value is also independent of the RELAY STEP and MEMORY STEP values.2. When using the edit pointer, it is not necessary to use the COPY keys, as you are closing/opening crosspoints relays or setting/clearing stored crosspoints directly and not just turning on/off crosspoint LEDs.
Programming Examples	10 OUTPUT 718; 'E0X' ! Point to relays 20 OUTPUT 718; 'E50X' ! Point to stored setup 50

4.9.6 F - Enable/disable Triggers

Purpose	To enable/disable triggers.
Format	Fn
Parameters	F0 = Disable triggers F1 = Enable triggers
Default	Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to F0 (triggers disabled).
Description	With the F command you control whether the Model 707 responds to a trigger (from the external trigger connection, over the IEEE-488 bus, or a front panel key). A trigger increments the RELAY STEP pointer and copies the setup indicated by the new value from memory to the relays.
Programming Notes	<ol style="list-style-type: none">1. The ENABLE indicator of the TRIGGER group lights when triggers are enabled.2. It is good programming practice to disable triggers before changing the trigger source.3. This command is equivalent to a single front panel key press.
Programming Example	<code>10 OUTPUT 718; 'F1X' ! Enable triggers</code>

4.9.7 G - Data Format

Purpose	To select the output format of the data sent from the current relay setup or a setup stored in memory.
Format	Gn
Parameters	G0 = Full output format, all data sent in one talk G1 = Full output format, one row of one mainframe per talk G2 or G3 = Inspect output format, all data sent in one talk G4 = Condensed output format, all data sent in one talk G5 = Condensed output format, one mainframe per talk G6 = Binary output format, all data sent in one talk G7 = Binary output format, one mainframe per talk
Default	Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to G0 (full output format, all data sent in one talk).
Description	The G command specifies the format of crosspoint data sent by the Model 707 over the IEEE-488 bus in response to the U2 command. Data is sent about the setup indicated by "U2,n" command (either the current relay setup or a stored setup). You can control the data format and quantity sent at a time. The full, condensed, and binary formats list the open/closed states of every crosspoint in the setup; the inspect format shows only closed crosspoints.

G0, G1 = Full output format

With the G0/G1 full output formats, the open/closed states of all crosspoints in a setup are sent in printable ASCII. An ASCII "-" represents an open crosspoint and an ASCII "X" represents a closed crosspoint. For G0, all data is sent in one talk; for G1, the data from one row of one mainframe is sent per talk. An example of these formats is shown in Figure 4-8 for the example setup of Table 4-9.

G2, G3 = Inspect output format

With the G2/G3 inspect output formats, the row/column address of each closed crosspoint in a setup is sent in printable ASCII. An ASCII letter (A-H) represents a row and an ASCII string of up to three numbers (0-360) represents a column. Successive crosspoints are separated with a comma. All data is sent in one talk. These formats are shown in Figure 4-9 for the example setup of Table 4-9.

G4, G5 = Condensed output format

The G4/G5 condensed output formats specify the states of all crosspoints with eight bits representing the eight crosspoints of a column. A set bit indicates a closed crosspoint. The hexadecimal representation of the binary value formed by these eight bits is sent as two printable ASCII characters. For G4, all data is sent in one talk; for G5, the data from one mainframe is sent per talk. An example of these formats is shown in Figure 4-10 for the example setup of Table 4-9.

G6, G7 = Binary output format

The G6/G7 binary output formats specify the states of all crosspoints with an 8-bit group of bits representing the eight crosspoints of a column. A set bit indicates a closed crosspoint. For G6, all data is sent in one talk; for G7, the data from one mainframe is sent per talk. These formats are shown in Figure 4-11 for the example setup of Table 4-9.

Table 4-9. Example Setup of Master/Slave Configuration

Unit	Closed Crosspoints
Master	A1, A2, B19, B20, C27, C28, D37, D38, F61, F62
Slave 1	A73, A74, C85, C86, E121, E122
Slave 2	A187, A188, H205, H206
Slave 3	A223, A224, H265, H266
Slave 4	G301, G302, A313, A314, H337, H338, E355, E356

Obtaining Data

Generally, data is placed into a string or numeric variable. For example, a typical input sequence for an HP controller with BASIC 4.0 is:

```
ENTER 718;CROSSPOINT$
```

In this instance, the complete crosspoint string is placed in the Crosspoint\$ variable.

Programming Notes

1. Table 4-10 lists the number of bytes that are transmitted for the various data formats.
2. As the data is transmitted in continuous strings (without carriage returns or line feeds), you must format the data for display or printing legibility.

Programming Example

```
10 DIM SETUP$(650)           ! Dimension for stand-alone unit
20 REMOTE 718                 ! Set 707 for IEEE-488 communication
30 OUTPUT 718;''G0U2,1X''    ! Full output, all setup #1 data sent
40 ENTER 718;SETUP$          ! Get data from 707
50 PRINT SETUP$(1,9)         ! Print setup number
60 PRINT SETUP$(10,88)       ! Print setup data row by row
70 PRINT SETUP$(89,167)
80 PRINT SETUP$(168,246)
90 PRINT SETUP$(247,325)
100 PRINT SETUP$(326,404)
110 PRINT SETUP$(405,483)
120 PRINT SETUP$(484,562)
130 PRINT SETUP$(563,641)
140 END
```

Table 4-10. Byte Counts for Data Format

Format	Stand-alone			Master with 4 Slaves		
	Bytes per Talk	Talks per Setup	Total Bytes	Bytes per Talk	Talks per Setup	Total Bytes
G0	641	1	641	3205	1	3205
G1	79 (note 1)	9	641	79 (Note 1)	45	3205
G2	(Note 2)	1	(Note 2)	(Note 2)	1	(Note 2)
G3	(Note 2)	1	(Note 2)	(Note 2)	1	(Note 2)
G4	154	1	154	770	1	770
G5	154	1	154	154	5 (Note 3)	770
G6	76	1	76	380	1	380
G7	76	1	76	76	5 (Note 3)	380

Notes:

1. In addition, each unit is identified with a 9-byte ASCII string.
2. This value depends on the number of closed crosspoints.
3. Maximum of 5 talks, depending on number of slaves in the system.

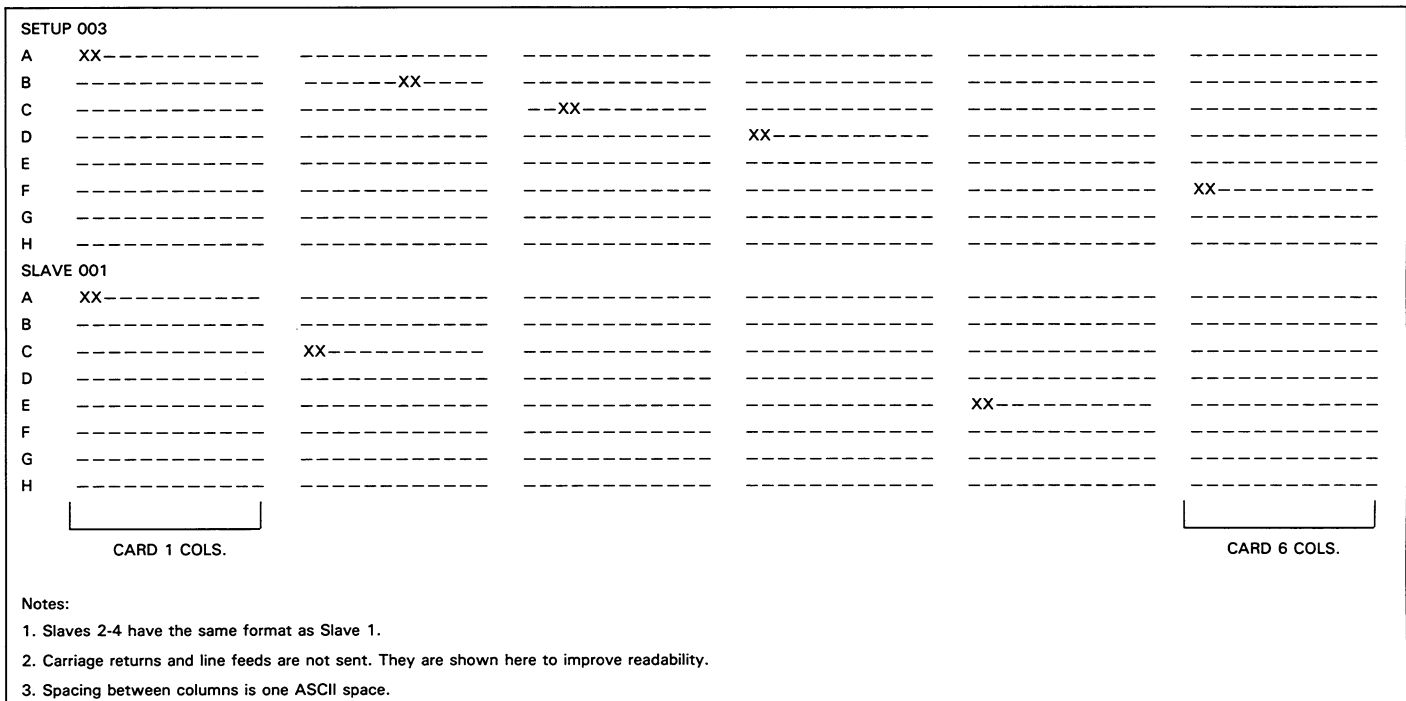


Figure 4-8. G0 and G1 Full Output Formats

A001,A002,B019,B020,C027,C028,D037,D038,F061,F062,A073,A074,C085,C086,E121,E122,A187,A188,H205,H206,A223,A224,H265,H266,G301,G302,A313,A314,H337,H338,E355,E356

Note: Carriage returns and line feeds are not sent. They are shown here to improve readability.

Figure 4-9. G2 and G3 Inspect Output Formats

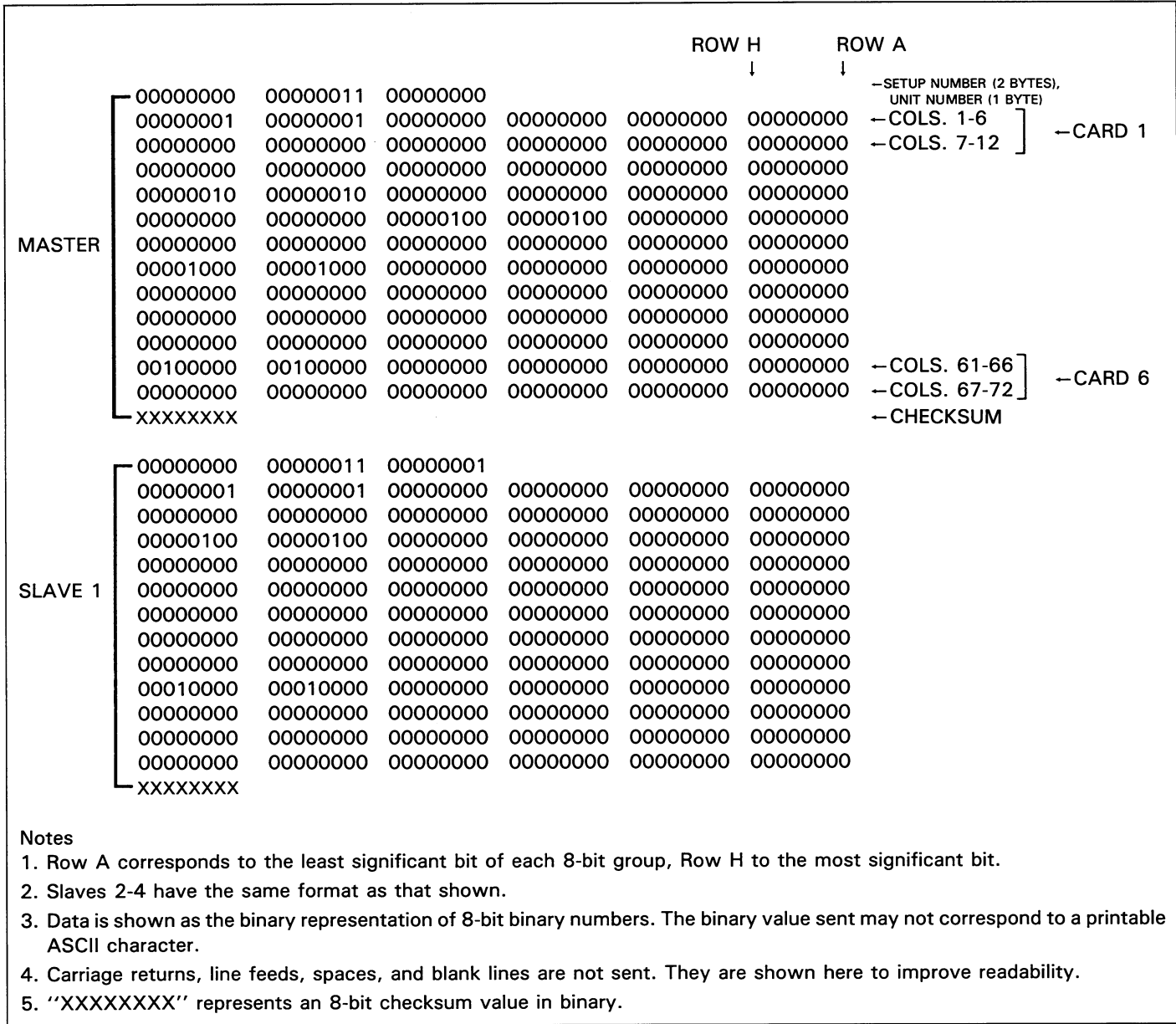
MASTER	[0003 00		--SETUP NUMBER (2 BYTES), UNIT NUMBER (1 BYTE)
		010100000000	000000000000	--CARD 1, COLS. 1-12
		000000000000	020200000000	
		000004040000	000000000000	
		080800000000	000000000000	
		000000000000	000000000000	
		202000000000	000000000000	--CARD 6, COLS. 61-72
		XX		--CHECKSUM
SLAVE 1	[0003 01		
		010100000000	000000000000	
		040400000000	000000000000	
		000000000000	000000000000	
		000000000000	000000000000	
		101000000000	000000000000	
		000000000000	000000000000	
		XX		

Notes

1. Slaves 2-4 have the same format as that shown.
2. Carriage returns, line feeds, spaces, and blank lines are not sent. They are shown here to improve readability.
3. "XX" represents a 1-byte checksum value (hexadecimal) in printable ASCII.
4. The rows that correspond to the G4/G5 data are as follows:

G4/G5 Data	Corresponding Row
00	none
01	A
02	B
04	C
08	D
10	E
20	F
40	G
80	H

Figure 4-10. G4 and G5 Condensed Output Formats



- Notes
1. Row A corresponds to the least significant bit of each 8-bit group, Row H to the most significant bit.
 2. Slaves 2-4 have the same format as that shown.
 3. Data is shown as the binary representation of 8-bit binary numbers. The binary value sent may not correspond to a printable ASCII character.
 4. Carriage returns, line feeds, spaces, and blank lines are not sent. They are shown here to improve readability.
 5. "XXXXXXXX" represents an 8-bit checksum value in binary.

Figure 4-11. G6 and G7 Binary Output Formats

4.9.8 H - Hit Key

Purpose To allow emulation of front panel key press sequence.

Format Hn

Parameters The parameter "n" represents the number of the front panel key as shown in the following table.

Command	Key	Command	Key
H1	MEMORY	H22	H
H2	RELAYS	H23	F
H3	COPY DISPLAY→MEMORY	H24	D
H4	COPY DISPLAY→RELAYS	H25	B
H5	AUTOMATIC	H26	7
H6	SCROLL ▲	H27	4
H7	SCROLL ▼	H28	1
H8	INSERT	H29	0
H9	DELETE	H30	8
H10	MENU	H31	5
H11	SETTLING TIME	H32	2
H12	MAKE/BREAK	H33	CANCEL
H13	BREAK/MAKE	H34	9
H14	LOCAL	H35	6
H15	ENABLE	H36	3
H16	SOURCE	H37	ENTER
H17	MANUAL	H38	RESET
H18	G	H39	CLEAR
H19	E	H40	OPEN
H20	C	H41	CLOSE
H21	A		

Description The H command and its options allow you to emulate front panel keystroke sequences. To emulate any such sequence, simply send the appropriate commands in the necessary order.

Programming Notes

1. The X character must follow each command in a multiple command string.
2. The H command is functional even if LLO (Local Lockout) is in effect.

Programming Examples

```

10 OUTPUT 718;''H5X''          ! Emulate AUTOMATIC key press
10 ! CHANGE PRIMARY ADDRESS OVER
   BUS
20 REMOTE 718                  !
30 CLEAR 718                   !
40 OUTPUT 718;''H10XH10XH10XH10 ! Menu option for IEEE-488 address
   XH10X''
50 OUTPUT 718;''H32XH29XH37X'' ! Set address to 20
60 WAIT .2                     ! Delay 0.2sec
70 REMOTE 720                  !
80 CLEAR 720                   !
90 ENTER 720;A#               ! Test communication to device 20
100 PRINT A#                   ! by requesting software revision
110 END

```

4.9.9 I - Insert Blank Setup

Purpose To insert a blank setup in memory.

Format In

Parameters n = 1 to 100 Stored relay setup

Description During execution of this command, setups "n" through 99 are shifted up to the next higher location in memory (99 to 100, 98 to 99 ... n to n+1). Then, all cross-points of setup "n" are cleared. The front panel display is blanked during an insert operation.

Programming Note This command is equivalent to a single or multiple front panel key press(es).

Programming Example 10 OUTPUT 718; '*I50X*' ! Insert blank setup at location 50

4.9.10 J - Self-test

Purpose To test ROM, RAM, and the front panel display segments.

Format Jn

Parameters J0 = Perform self-test

Description The self-test command starts execution of the ROM, RAM, and display tests. If an error is detected in ROM or RAM, a "ROM FAIL" or "RAM FAIL" message is displayed. Any front panel keypress or bus command overrides the message display. The display test lights all segments of the alphanumeric display for your inspection.

Programming Notes

1. The value "n", if sent, must be zero.
2. Allow approximately four seconds for the instrument to complete the self-test.
3. The instrument holds off bus operation with the NRFD line during self-test operation. Thus, no commands can be sent during the self-test if hold-off on X is enabled.
4. This command is equivalent to a front panel menu selection.

Programming Example 10 OUTPUT 718;''J0X'' ! Perform self-test

4.9.11 K - EOI and Hold-off

Purpose	To enable/disable EOI and bus hold-off on X.
Format	Kn
Parameters	<p>K0 = Send EOI with last byte, hold-off on X until Ready K1 = No EOI, hold-off on X until Ready K2 = Send EOI with last byte, do not hold-off on X K3 = No EOI, do not hold-off on X K4 = Send EOI with last byte, hold-off on X until Matrix Ready K5 = No EOI, hold-off on X until Matrix Ready</p>
Default	Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to K0 (send EOI with last byte, hold-off on X until ready).
Description	The EOI line provides one method to positively identify the last byte in the data string sent by the instrument. When enabled, EOI is asserted with the last byte the instrument sends over the bus.

Bus hold-off allows the instrument to temporarily hold up bus operation via the NRFD line when it receives the X character until all commands are processed. The advantage of using bus hold-off is that no commands are missed while the instrument is processing previously received commands. Typical hold-off times are discussed in paragraph 4.10.

Programming Notes	<ol style="list-style-type: none"> 1. Some controllers rely on EOI to terminate their input sequences. Suppressing EOI may cause the controller input sequence to hang. 2. When reading a buffer, EOI is asserted only at the end of the entire buffer transmission. 3. When enabled, EOI is asserted with the last byte in the terminator. 4. When bus hold-off is enabled, all bus activity is held up for the duration of the hold-off period — not just communication with the Model 707.
--------------------------	---

Programming Example	<pre>10 OUTPUT 718; 'K1X' ! No EOI, hold-off on X until Ready 20 OUTPUT 718; 'K2X' ! Send EOI with last byte, do not hold-off on X</pre>
----------------------------	--

4.9.12 L - Download Setups

- Purpose** To download setups from the controller to the Model 707.
- Format** **Lbbbb...X**
- Parameters** bbbb... represents the G4/G5 or G6/G7 output string.
- Description** This command downloads setup information in a G4/G5 or G6/G7 data format. It is used in conjunction with the U2 command (output setup data) to back up or expand the setups stored in the Model 707.

G formats are discussed in paragraph 4.9.7; see paragraph 4.9.21 for U commands.

- Programming Notes**
1. The data format for downloading is specified by the G format currently in effect.
 2. The setup data string begins with a setup number and unit number and ends with a checksum value.
 3. The setup number is specified in a U2,n command (output setup "n"), as shown in the following programming example.

Programming Example

```

10 DIM SETUP$(200)           ! Dimension for stand-alone
20 REMOTE 718
30 OUTPUT 718;''G4U2,1X'' ! Setup #1 in G4 format
40 ENTER 718;SETUP#         ! Get setup data
50 PRINT SETUP$(1,6)       ! Print setup and unit numbers
60 PRINT SETUP$(7,30)      ! Print setup data card by card
70 PRINT SETUP$(31,54)
80 PRINT SETUP$(55,78)
90 PRINT SETUP$(79,102)
100 PRINT SETUP$(103,126)
110 PRINT SETUP$(127,150)
120 PRINT SETUP$(151,152)  ! Print checksum
130 PRINT ''PRESS CONTINUE ! Inspect setup data
    TO DOWNLOAD''
140 PAUSE                   ! Wait for controller's CONTINUE key
150 OUTPUT 718;''L''&SETUP ! Download setup back to 707
    $&''X''
160 END

```

4.9.13 M - SRQ and Serial Poll Byte

Purpose To program which conditions generate an SRQ (service request).

Format Mn

Parameters M0 = SRQ disabled
 M1 = not used
 M2 = Front panel key press
 M4 = Digital I/O interrupt
 M8 = Matrix Ready
 M16 = Ready for trigger
 M32 = Error
 M128 = not used

Default Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to M0 (SRQ disabled).

Description The SRQ command selects which conditions cause the Model 707 to generate an SRQ (service request). Once an SRQ is generated, the serial poll byte can be checked to determine if the Model 707 was the instrument that generated the SRQ, and, if so, what conditions caused it.

The general format of the SRQ mask used to generate SRQs is shown in Figure 4-12. By sending the appropriate M command, you can set the appropriate bit or bits to enable SRQ generation if those particular conditions occur. Possible conditions include:

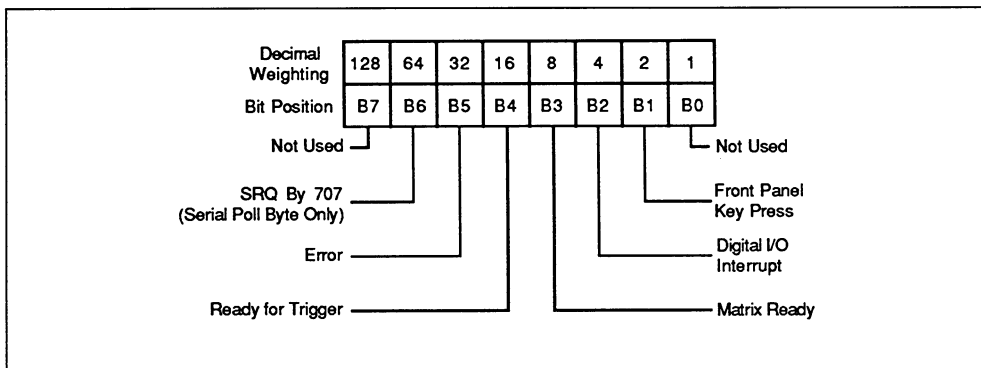


Figure 4-12. SRQ Mask and Serial Poll Byte Format

- A front panel key has been pressed (M2).
- An interrupt condition has been received at the Digital I/O port (M4).
- The Matrix Ready signal has been asserted (M8).
- The Ready (for trigger) signal has been asserted (M16).
- An error has occurred (M32). The nature of the error can be determined by reading the U1 error word as described in paragraph 4.9.21.

Serial Poll Byte

The general format of the serial poll byte is shown in Figure 4-12. Note that all bits except for bit 6 correspond to the bits in the SRQ mask. These bits flag the following conditions:

Front Panel Key Press (bit 1)—Set when the Model 707 detects a key press, including keys “pressed” with the H command. It is cleared by reading U0 machine status word (see paragraph 4.9.21).

Digital I/O Interrupt (bit 2)—Set when the INLATCH signal of the digital I/O port is brought low to latch in the present states of the digital inputs. (Further changes to the inputs of the digital I/O port are ignored until the latched data is read.) Cleared by reading the master unit’s digital input with the U7 command (see paragraph 4.9.21).

NOTE

It is not necessary to read the digital input value every time INLATCH causes an SRQ to be generated. The INLATCH signal can be used to indicate a user-defined interrupt condition, with multiple assertions causing multiple interrupts.

Matrix Ready (bit 3)—Set whenever the Matrix Ready signal is asserted. Cleared by the start of relay switching.

Ready for Trigger (bit 4)—Set whenever the Ready signal is asserted. This bit is cleared by:

- Receipt of X.
- Start of relay switching.
- Front panel key press on master unit.
- Changing Make/Break or Break/Make row.
- Performing self-test.
- Pressing RELAYS key.

NOTE

Using the H command to “hit” keys could cause the Ready bit to cycle twice: once when the H command is processed and again when the key press is processed.

Error (bit 5)—Set if an error condition occurs. Cleared by reading the U1 error status word (paragraph 4.9.21).

SRQ (bit 6)—Set if the Model 707 requests service via the SRQ line; cleared by a serial poll.

Programming Notes

1. The serial poll byte should be read once the instrument has generated an SRQ to clear the SRQ line.
2. All bits in the serial poll byte latch when the instrument generates an SRQ.
3. If an error occurs, bit 5 (error) in the serial poll byte latches and remains so until the U1 word is read (paragraph 4.9.21).
4. Multiple error conditions can be programmed by adding up the individual command values. For example, send M12X for SRQ under matrix ready and digital I/O interrupt conditions.

**Programming
Example**

```
10 CLEAR 718
20 REMOTE 718
30 OUTPUT 718;''M32X''
40 OUTPUT 718;''A2X''

50 STATUS 7,2;S
60 IF NOT BIT(S,5) THEN 50
70 S=SPOLL(718)
80 PRINT ''B7 B6 B5 B4 B3 B2 B1 B0''
90 FOR I=7 TO 0 STEP -1
100 PRINT BIT(S,I);
110 NEXT I
120 PRINT
130 OUTPUT 718;''U1X''
140 ENTER 718;ERROR#
150 PRINT ERROR#
160 END
```

! Program for SRQ on error
! Attempt to program invalid option
! Check interface status
! Wait for SRQ to occur
! Serial poll the instrument
! Label the bit positions
! Loop eight times
! Display the bit positions

! Program for error status
! Get U1 status to clear error
! Display error status

4.9.14 N - Open Crosspoint

Purpose To open crosspoints in a setup.

Format **Nrc(rc)...(rc)**

Parameters r = Row A to row H
c = Column 1 to column 360

Description The N command opens crosspoints in the setup indicated by the edit pointer. If the edit pointer indicates the current relay setup (zero), the specified crosspoint relays are opened immediately. If the edit pointer indicates a setup stored in memory (1-100), the specified crosspoints are cleared.

Programming Notes

1. Do not include parentheses in command strings. They indicate that the option and associated comma delimiter are optional.
2. Up to 25 crosspoints per mainframe can be specified in one open command (with a master and four slaves, the limit is 125 crosspoints). In the same command string, up to 25 crosspoints per mainframe can be closed. If either limit is exceeded, an IDDCO results.
3. The maximum value of the column parameter depends on the configuration (72 for stand-alone, 360 for master with four slave units). An IDDCO results if the maximum value is exceeded.
4. This command is equivalent to multiple front panel key presses or light pen operation(s).

Programming Example

```
10 OUTPUT 718; 'NA55X' ! Open one crosspoint
20 OUTPUT 718; 'NA55,A56,B49,B50X' ! Open multiple crosspoints
```

4.9.15 O - Digital Output

- Purpose** To set the states of the digital output lines.
- Format** **Ovvv**
- Parameters** vvv = 000 to 255 (Decimal value of digital output)
- Default** Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to O000 (all digital outputs set to logic low).
- Description** This command is a decimal representation of the states of individual output lines of the Digital I/O port, where "1" is logic high and "0" is logic low. Bit assignments and corresponding connector pins are shown below:

Bit Position	7	6	5	4	3	2	1	0
Bit Weight	128	64	32	16	8	4	2	1
Digital Output Pin	24	23	22	21	20	19	18	17

- Programming Notes**
1. In a master/slave configuration, only the output of the master unit is updated.
 2. Leading zeros are not necessary in the parameter value.
 3. The OUTPUTPULSE signal on the digital I/O port is brought low when this command is executed, even if the output states are not changed.
 4. This command is equivalent to a front panel menu selection.

Programming Example

```
10 OUTPUT 718;''015X'' ! Set bits <3-0> high
20 OUTPUT 718;''00X'' ! Restore default condition
```

4.9.16 P - Clear Crosspoints

Purpose To clear all crosspoints at the specified setup.

Format Pn

Parameters n = 0 Current relay setup
n = 1 to 100 Stored relay setup

Description The P command clears all crosspoints in the setup indicated by its parameter. If the current relay setup (zero) is specified, all crosspoint relays are opened immediately. If setup stored in memory (1-100) is specified, all crosspoints of that setup are cleared.

Programming Note This command is equivalent to multiple front panel key presses.

Programming Example 10 OUTPUT 718; 'P0X' ! Open all relays
20 OUTPUT 718; 'P20X' ! Clear setup #20

4.9.17 Q - Delete Setup

Purpose To delete a setup from memory.

Format Qn

Parameters n = 1 to 100 Stored relay setup

Description During execution of this command, setups "n+1" through 100 are shifted down to the next lower location in memory ("n+1" to "n" ... 100 to 99). Then, all crosspoints of setup #100 are cleared. The front panel display is blanked during a delete operation.

Programming Notes

1. The command Q100 clears all crosspoints of setup #100.
2. This command is equivalent to a single or multiple front panel key press(es).

Programming Example 10 OUTPUT 718;''Q50X'' ! Delete setup #50 from memory

4.9.18 R - Restore Defaults

Purpose To restore the Model 707 to factory default conditions.

Format Rn

Parameters R0 = Restore factory defaults

Description An R0 command performs the following actions:

- All setups stored in memory are cleared.
- Make/Break and Break/Make rows are cleared.
- A Device Clear operation (all crosspoint relays are opened, RELAY STEP pointer is set to 000, MEMORY STEP is set to 001).

DDC parameters are set to the values shown below:

A0	Initiate trigger on falling edge of External Trigger pulse.
B0	Set Matrix Ready output signal to negative true.
DX	Set normal operation for alphanumeric display.
E000	Set edit pointer to current relay setup.
F0	Disable triggers.
G0	Set full output format, all data sent in one talk.
K0	Send EOI with last byte, hold-off on X until ready.
M000	Disable all SRQ sources.
O000	Set all digital outputs to logic low.
S00000	Set user settling time to zero.
T7	Trigger Model 707 on external trigger pulse.
V00000000	De-select all rows for make/break.
W00000000	De-select all rows for break/make.
Y0	Set terminator characters of <CR> <LF>.

- Programming Notes**
1. The primary IEEE-488 address and master/slave operation are not affected by the Restore command.
 2. This command is equivalent to a front panel menu selection.

Programming Example 10 OUTPUT 718; '*R0X*' ! Restore default conditions, clear setups

4.9.19 S - Programmed Settling Time

Purpose To program the settling time.

Format Sn

Parameters n = 0 to 65000 Time in msec

Default Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to S0 (programmed settling time of zero).

Description With the S command, you can program the settling time (up to 65 seconds). The programmed settling time starts after the longest relay settling time has elapsed.

Programming Notes

1. This command is equivalent to multiple front panel key presses.
2. The total settling time equals the longest relay settling time of any card in the system plus any user-programmed settling time. Figure 4-13 shows a timing diagram of the settling times. Additional timing diagrams are given in paragraph 3.11.

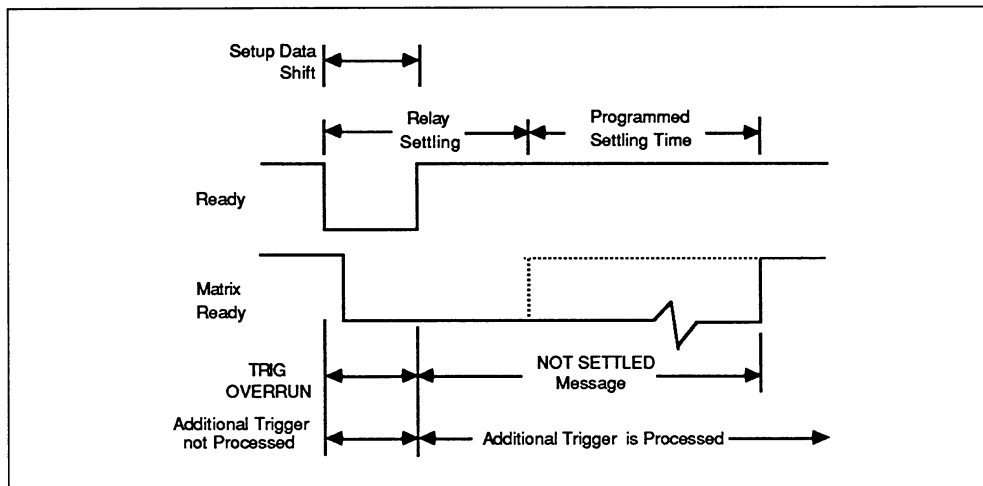


Figure 4-13. READY and MATRIX READY Signal Timing

Programming Examples

```
10 OUTPUT 718; 'S5000X' ! Program 5sec settling time
20 OUTPUT 718; 'S0X' ! Restore default conditon
```

4.9.20 T - Trigger

Purpose	To select the trigger source.
Format	Tn
Parameters	T0 or T1 = Trigger on talk T2 or T3 = Trigger on GET T4 or T5 = Trigger on X T6 or T7 = Trigger on External Trigger pulse T8 or T9 = Trigger on front panel MANUAL key
Default	Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to T7 (trigger on external trigger pulse).
Description	With the trigger command, you can determine the trigger source: over the bus, from an external trigger pulse, or from the front panel. A valid trigger increments the RELAY STEP pointer by one, stopping at 100, and copies the setup data indicated by the new value to the relays.

Programming Notes	<ol style="list-style-type: none"> 1. Duplication of trigger sources allows compatibility with other Keithley IEEE-488 instruction sets. 2. It is good programming practice to disable triggers before changing the trigger source. 3. If the unit is re-triggered while it is still processing a previous trigger, it displays either a TRIG OVERRUN or NOT SETTLED message, depending on when the additional trigger occurs. The timing diagrams are shown in paragraph 3.11. 4. To trigger the instrument when using the trigger on talk, you must send the talk command derived from the correct primary address. The factory default primary address is 18. Trigger on talk does not occur when the Model 707 becomes a talker, but rather as the controller requests the first byte of data from the unit. 5. Trigger on GET allows the fastest IEEE-488 triggering response. 6. The X character that is sent when programming a trigger on X source triggers the instrument. 7. Front panel triggering with the MANUAL key is always enabled regardless of the programmed trigger source (while the TRIGGER ENABLE LED is lit); however, all front panel keys are locked out if the unit is in remote (REMOTE on). To restore local operation in this case, press the LOCAL key. 8. This command is equivalent to multiple front panel key presses.
--------------------------	--

Programming Examples	<pre> 10 OUTPUT 718;''F0T0X'' ! Disable triggers, program trigger on talk. 20 OUTPUT 718;''F1X'' ! Enable triggers 30 ENTER 718;A\$! Trigger next setup 10 OUTPUT 718;''F0T2X'' ! Disable triggers, program trigger on GET 20 OUTPUT 718;''F1X'' ! Enable triggers 30 TRIGGER 718 ! Trigger next setup </pre>
-----------------------------	---

4.9.21 U - Status

Purpose To obtain instrument status and system configuration.

Format Un

Parameters

- U0 = Send machine status word.
- U1 = Send error status word.
- U2,n = Output setup "n" (0-100) with current G format.
- U3 = Send value of RELAY STEP pointer.
- U4 = Send number of slaves.
- U5,u = Send ID of each card in unit "u" (0-4).
- U6 = Send longest relay settling time.
- U7 = Send digital input of unit.
- U8 = Send RELAY TEST input.

Description By sending the appropriate U command and then addressing the instrument to talk as with normal data, you can obtain information on machine status, error conditions, and other data.

U0 Machine Status Word

The format of U0 is shown in Figure 4-14. The letters correspond to modes programmed by the respective device-dependent commands. Returned values correspond to the programmed numeric values. The values shown in Figure 4-14 are the default values. (Note that the value for "H" is the last key hit, even after an R0 command or power cycle.)

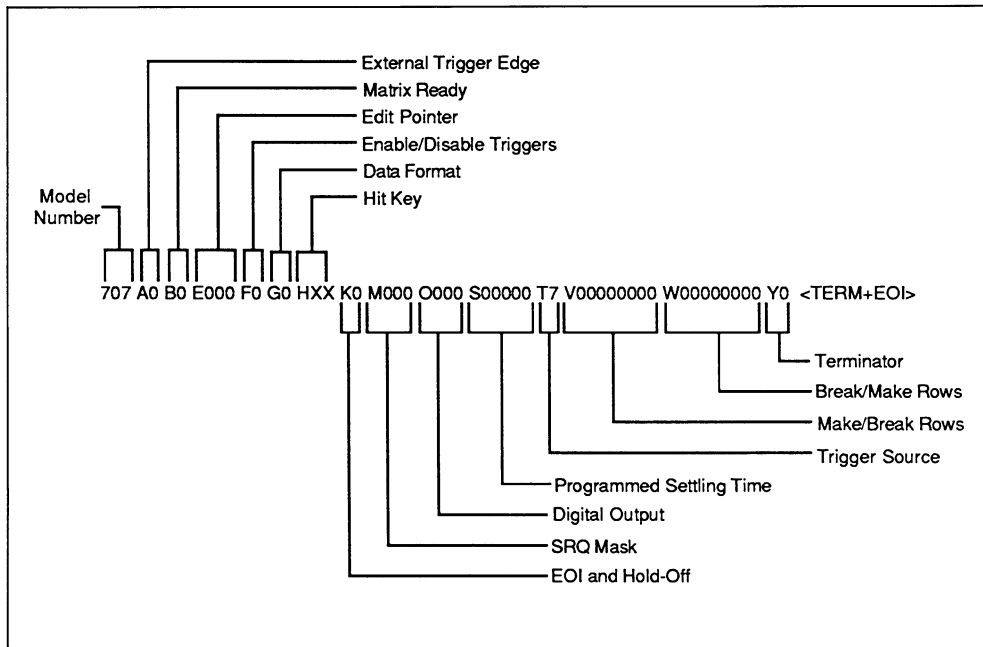


Figure 4-14. U0 Machine Status Word

U1 Error Status Word

The U1 command allows access to Model 707 error conditions. The error status word (Figure 4-15) is actually a string of ASCII characters representing binary bit positions. Reading the U1 status clears the error bits. An error condition is flagged in the serial poll byte while any bits in the error status word are set. The instrument can be programmed to generate an SRQ when an error condition occurs (see paragraph 4.9.13).

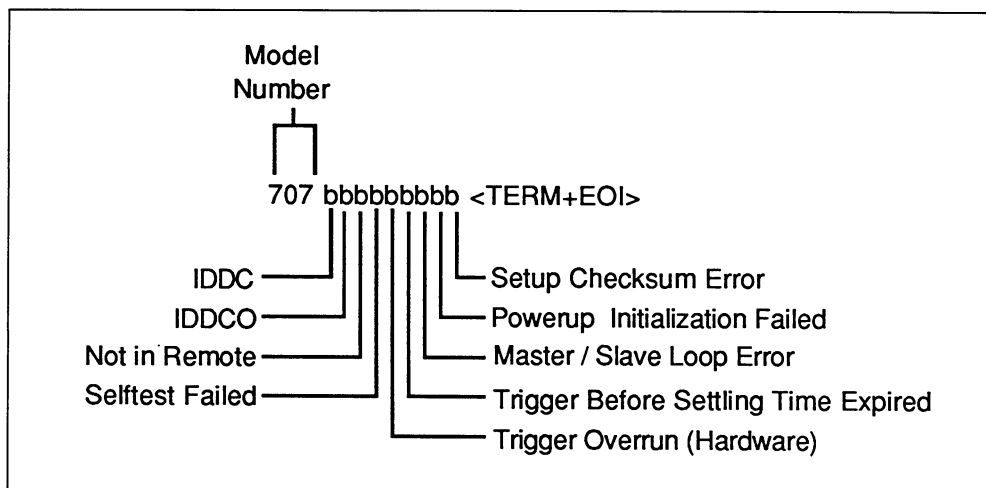


Figure 4-15. U1 Error Status Word

The various bits in the U1 error status word are set when the following conditions are present:

IDDC—An invalid device-dependent command (IDDC) is received.

IDDCO—An invalid device-dependent command option (IDDCO) is received.

Not in Remote—An X command is received over the bus but the Model 707 is not in remote.

Self-test Failed—The self-test detects a program ROM checksum error or a RAM error.

Trigger Overrun (Hardware)—A trigger is received before the Ready signal is asserted. The trigger is ignored.

Trigger Before Settling Time Expired—A trigger is received before the Matrix Ready signal is asserted. The trigger is processed.

Master/Slave Loop Error—There is a communication or timing error in the master/slave loop.

Power-up Initialization Failed—The power-up routine has detected a checksum error in the information from one or more matrix cards.

Setup Checksum Error—The power-up routine detects a checksum error in one or more setups stored in memory. (The affected setups are cleared.)

U2,n Formatted Setup

With the U2 command, you can request the Model 707 to output data of either the current relay setup ($n=0$) or a stored setup ($1 \leq n \leq 100$) according to the G format presently in effect. (See paragraph 4.9.7.)

U3 RELAY STEP pointer

The U3 command (Figure 4-16) requests the value of the RELAY STEP pointer, which indicates the last setup sent to the relays ($000 \leq nnn \leq 100$).

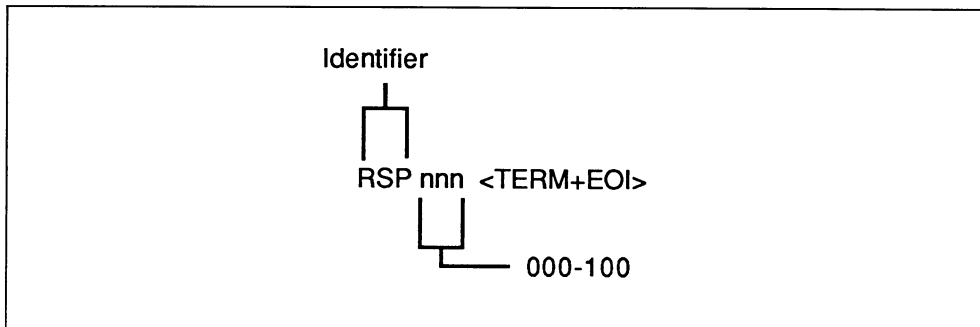


Figure 4-16. U3 Relay Step Pointer

U4 Number of slaves

With the U4 command (Figure 4-17), you can request the number of slaves present in a master/slave loop configuration (between 1 and 4).

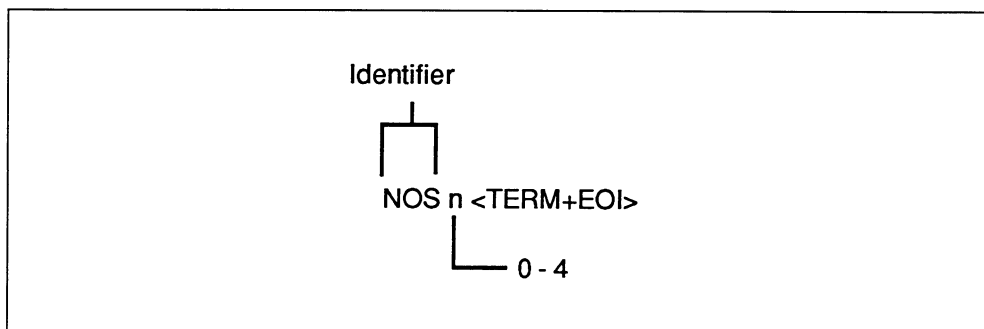


Figure 4-17. U4 Number of Slaves

U5,u Card IDs

By specifying a unit number in the U5 command (0 for master, 1-4 for slaves), you can request the model numbers of the cards present in each mainframe. The output format is shown in Figure 4-18. The character string for an empty slot is "NONE". This command is equivalent to a front panel menu selection.

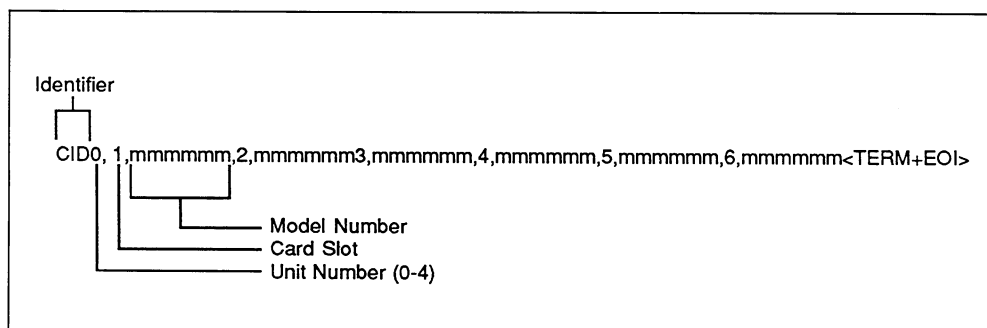


Figure 4-18. U5 Card Identifications

U6 Relay settling time

The U6 command (Figure 4-19) requests the Model 707 to output the longest relay settling time of all cards in the system (expressed in msec). This command is equivalent to a front panel menu selection.

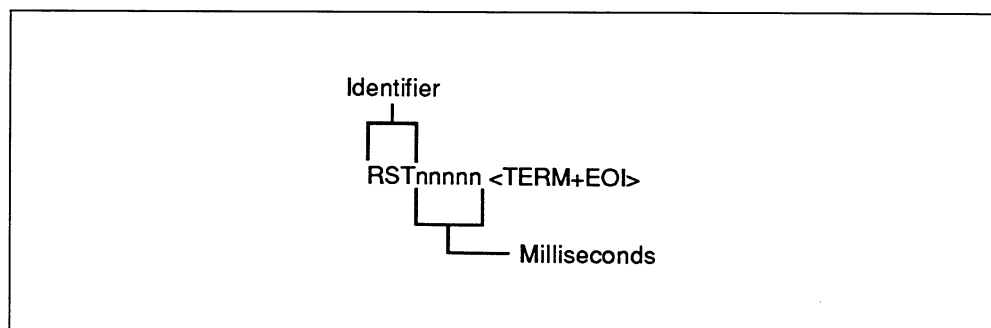


Figure 4-19. U6 Relay Settling Time

U7 Digital input

The U7 command (Figure 4-20) requests a decimal value of the inputs at the Digital I/O port. If bit 2 of the serial poll byte is set, the previously latched status is sent. If bit 2 of the serial poll byte is cleared, the present states of the digital inputs are read and sent. This command is equivalent to a front panel menu selection.

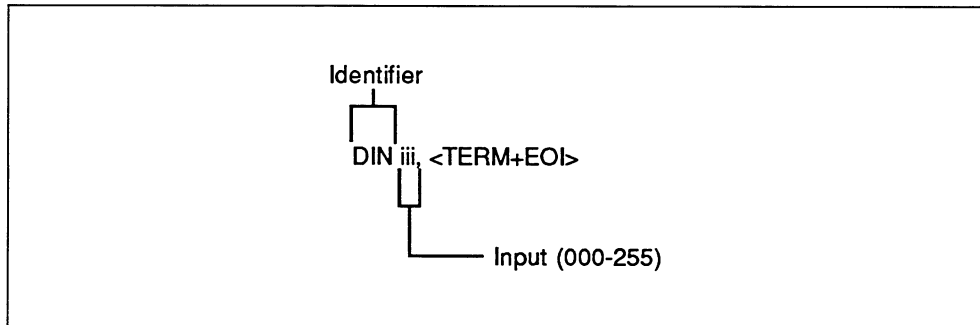


Figure 4-20. U7 Digital Input

In master/slave configurations, the digital input of the master unit is sent.

U8 Relay Test input

The U8 command (Figure 4-21) requests the status of the input pins at the Relay Test connector. Values between 0 and 15 represent the states of pins 1 (LSB) through 4 (MSB).

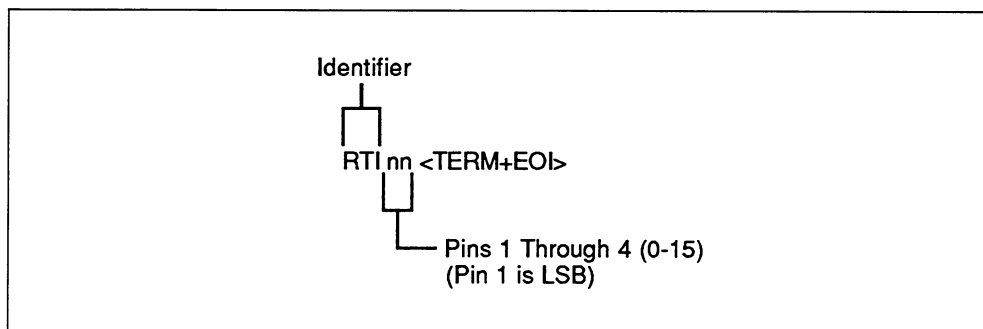


Figure 4-21. U8 Relay Test Input

Programming Notes

1. The instrument transmits the appropriate status word only once each time the corresponding U command is received.
2. To ensure that correct status is indicated, the status word should be requested immediately after the command is transmitted. The status sent by the Model 707 is that which is present at the time it is instructed to talk, not at the time the U command is received.
3. The bits in the U1 error status word latch and remain in that condition until the U1 word is read.
4. The programmed terminator (default CR LF) is transmitted at the end of each status word. Also, EOI is transmitted at the end (unless disabled with the K command).

5. If no U command has been received, an ENTER 718;A\$ command requests the letter (x) and number (nn) of the software revision for a stand-alone unit or the master unit of a master/slave configuration (707xnn). It is sent with two trailing spaces plus the terminator and EOI.

Programming Examples

```

10 DIM COMMAND#[50], STATUS#[650]      ! Dimension command and
                                         status.
20 REMOTE 718
30 INPUT 'ENTER U COMMAND' , COMMAND#  ! Get desired U command
40 IF LEN(COMMAND#)=0 THEN 100         ! Check for null
50 OUTPUT 718;COMMAND#&' 'X' '      ! Program 707 with U
                                         command
60 ENTER 718;STATUS#                  ! Get status
70 PRINT STATUS#                      ! Print status
80 PRINT CHR$(128)                    ! Print blank line
90 GOTO 30                             ! Return for next U
                                         command

100 END

```

4.9.22 V - Make/Break

Purpose To select rows for make/break operation.

Format **V**abcdefgh

Parameters abcdefgh = 00000000 All rows de-selected for make/break
to
11111111 All rows selected for make/break

Description The V command selects individual rows for make/break (make-before-break) operation. A "1" in the respective row field selects make/break; a "0" de-selects make/break operation.

- Programming Notes**
1. Specifying fewer than eight numbers in the parameter field (e.g. V1111) is invalid. The Model 707 takes no action on the rows and flags an IDDCO error.
 2. The rows can be programmed for one of three switching options: make/break, break/make, or don't care. A row cannot be selected for both make/break and break/make at the same time. Selecting it for one de-selects it for the other.
 3. This command is equivalent to multiple front panel key presses or light pen operation(s).
 4. Given the present states and actions performed, the next states of the rows are listed below:

Present State	Action	Next State
Don't Care	Select Make/Break	Make/Break
	De-select Make/Break	Don't Care
Make/Break	Select Make/Break	Make/Break
	De-select Make/Break	Don't Care
Break/Make	Select Make/Break	Make/Break
	De-select Make/Break	Break/Make

Programming Examples

```
10 OUTPUT 718; '*V11110000X*' ! Select rows ABCD for make/break
20 OUTPUT 718; '*V00000000X*' ! Restore default condition
```

4.9.23 W - Break/Make

Purpose To select rows for break/make operation.

Format **W**abcde fgh

Parameters abcde fgh = 00000000 All rows de-selected for break/make
to
11111111 All rows selected for break/make

Description The W command selects individual rows for break/make (break-before-make) operation. A "1" in the respective row field selects break/make; a "0" de-selects break/make operation.

- Programming Notes**
1. Specifying fewer than 8 numbers in the parameter field (e.g. W1111) is invalid. The Model 707 takes no action on the rows and flags an IDDCO error.
 2. The rows can be programmed for one of three switching options: make/break, break/make, or don't care. A row cannot be selected for both make/break and break/make at the same time. Selecting it for one de-selects it for the other.
 3. This command is equivalent to multiple front panel key presses or light pen operation(s).
 4. Given the present states and actions performed, the next states of the rows are listed below:

Present State	Action	Next State
Don't Care	Select Break/Make	Break/Make
	De-select Break/Make	Don't Care
Make/Break	Select Break/Make	Break/Make
	De-select Break/Make	Make/Break
Break/Make	Select Break/Make	Break/Make
	De-select Break/Make	Don't Care

Programming Examples

```
10 OUTPUT 718; '*W11110000X*' ! Select rows ABCD for break/make
20 OUTPUT 718; '*W00000000X*' ! Restore default condition
```

4.9.24 X - Execute

Purpose To direct the Model 707 to execute device-dependent commands received since last X.

Format <command>X

Parameters None

Description The execute command is implemented by sending an ASCII X over the bus. Its purpose is to direct the Model 707 to execute other device-dependent commands. Generally, the execute character is the last byte sent in the command string; however, there may be some cases when it is desirable to send a string of characters at one time and then send the execute character later on.

Programming Notes

1. Commands or command strings sent without the X character are not executed at that time, but they are stored in an internal command buffer for later execution once the X character is finally received.
2. The X character can also be used to trigger, as described in paragraph 4.9.20.
3. Commands are not necessarily executed in the order sent (see Table 4-7). To force a particular command sequence, include the X character after each command in the command string.

Programming Examples

```
10 OUTPUT 718; 'E1X'           ! Execute single command
20 OUTPUT 718; 'E1CA47X'      ! Execute multiple commands
30 OUTPUT 718; 'T6XA1XR1X'    ! Force command sequence
40 OUTPUT 718; 'G2Y1'         ! Send string without execute
50 OUTPUT 718; 'X'           ! Now execute command string at later
                             time
```

4.9.25 Y - Terminator

Purpose	To select the ASCII terminator sequence that marks the end of the instrument's data string or status word.
Format	Yn
Parameters	Y0 <CR> <LF> Y1 <LF> <CR> Y2 <CR> Y3 <LF>
Default	Upon power-up, or after receiving a DCL, SDC, or R0X command, the instrument defaults to Y0 (<CR> <LF>).
Description	By using the Y command, you can program the number and type of terminator characters the instrument sends at the end of its data string. Available terminator characters are the commonly used CR (carriage return, ASCII 13) and LF (line feed, ASCII 10) characters. These terminator characters are recognized by most controllers.
Programming Notes	<ol style="list-style-type: none"> 1. EOI is another method that can be used to terminate the controller input sequence, as discussed in paragraph 4.9.11. EOI is asserted with the last terminator byte when enabled. 2. The programmed terminator is sent at the end of the transmission each time the Model 707 is addressed to talk, regardless of the selected data format. 3. Status word programming is covered in paragraph 4.9.21.
Programming Examples	<pre> 10 OUTPUT 718; 'Y2X' ! Program CR only as terminator 20 OUTPUT 718; 'Y3X' ! Terminate on LF 30 OUTPUT 718; 'Y0X' ! Restore default terminator </pre>

4.9.26 Z - Copy Setup

- Purpose** To copy a setup from relays or memory to relays or memory.
- Format** **Zm,n**
- Parameters** 0,n = Current relay setup to stored setup "n" (1-100)
n,0 = Stored setup "n" (1-100) to current relay setup
m,n = Setup "m" (0-100) to setup "n" (0-100)
- Description** By specifying a source and destination in the Z command, you can copy data between stored setups and between the relays and setups stored in memory. Copying a setup to the relays sets the RELAY STEP pointer to that setup.
- Programming Notes**
1. The command "Z0,0X" sends the current relay setup to the relays. There is no effect on the relays. The command merely resets the RELAY STEP pointer to 000.
 2. This command is equivalent to multiple front panel key presses.
- Programming Examples**
- ```
10 OUTPUT 718;''Z0,10X'' ! Copy current relay setup to setup 10
20 OUTPUT 718;''Z20,0X'' ! Copy setup 20 from memory to relays
30 OUTPUT 718;''Z10,20X'' ! Copy setup 10 to setup 20
```



## 4.10 RELAY COMMAND COMBINATIONS

There are four device-dependent commands that have an immediate effect on relay states:

- E0N... - Point to relays, open specified crosspoints.
- E0C... - Point to relays, close specified crosspoints.
- P0 - Open all relays.
- Zn,0 - Copy setup "n" to relays.

Combinations of these commands in the same command string cause only one relay switching operation when the X character is received. The command hierarchy (E, P, Z, N, C) determines the final data that is sent to the relays. This is shown in the following examples.

### Example 1

The command string "E0P0CA1X" sets the edit pointer to the current relay setup, opens all relays, and closes crosspoint A1. A1 will be the only closed crosspoint.

### Example 2

The string "E0Z5,0CA1X" sets the edit pointer to the relays, copies stored setup #5 to the relays, and closes crosspoint A1. A1 will be closed regardless of the state of A1 in setup #5. The status of the relays will be a combination of setup #5 and a closed A1 crosspoint.

### Example 3

The command string "E0Z5,0NA1X" points to the relays, copies setup #5 to the relays, and opens crosspoint A1. If setup #5 had specified A1 to be closed, the command NA1 overrides that. (A1 will not close, then open, as there will be only one relay switching operation.) The relays will reflect setup #5 and an open A1 crosspoint.

## 4.11 TIMING CONSIDERATIONS

Timing considerations for IEEE-488 programming include:

- Data transfer rates between the controller and Model 707 (stand-alone or master) over the IEEE-488 bus.
- Command string parse time within the stand-alone or master unit.
- Data transfer rates among the units in a master/slave loop.
- Execution times of the tasks defined by device-dependent commands.

The times needed for these actions are determined by the length of the command string, the number of units in a master/slave configuration, the types of commands, and the speed of the controller.

Typically, a command string sent to the Model 707 will transmit at a rate of four characters per millisecond. (Assuming the transfer speed of the controller does not affect the listening rate of the Model 707.) For example, the command string "CA1,H72X" will take 2msec to transmit from the controller to the Model 707.

When the Model 707 is sending data to the controller (e.g., uploading setup data), the transmission rate will typically be 2.5 characters per millisecond.

The bus hold-off time for each command is the time from receipt of the "X" to "instrument configured". It includes the parsing time, data transfers within a master/slave loop, and command execution time.

Table 4-11 summarizes the total times (transmission plus hold-off) for device-dependent commands acting on a stand-alone unit. Table 4-12 summarizes these times for a master and one slave system. Bus hold-off times for individual command strings can be calculated by subtracting the transmission time (four characters per millisecond).

Table 4-11. Typical Transmission and Hold-off Times for a Stand-alone Unit

| Description              | Command    | Time     | Notes                 |
|--------------------------|------------|----------|-----------------------|
| External Trigger         | A0X        | 4.8msec  |                       |
| Matrix Ready             | B0X        | 4.8msec  |                       |
| Close Crosspoint(s)      | CA1X       | 18.6msec | To relays             |
|                          | CA1X       | 19.9msec | To setup 55           |
|                          | CA1,H72X   | 21.7msec | To relays             |
|                          | CA1,H72X   | 23.1msec | To setup 55           |
|                          | D*****X    | 10.6msec |                       |
| Display                  | DX         | 3.8msec  |                       |
| Edit Pointer             | E0X        | 4.4msec  |                       |
|                          | E100X      | 5.8msec  |                       |
| Enable/Disable Triggers  | F0X        | 4.8msec  |                       |
| Data Format              | G7X        | 4.4msec  |                       |
| Hit Key                  | H5X        | 5.3msec  |                       |
| Insert Blank Setup       | I1X        | 835msec  |                       |
|                          | I100X      | 21.1msec |                       |
|                          | J0X        | 3.3msec  |                       |
| Self-test                | K0X        | 4.4msec  |                       |
| EOI and Hold-off         | Lbbb...X   | 56.0msec | Binary format (G6,G7) |
| Download Setup           | M32X       | 4.9msec  |                       |
| SRQ                      |            |          | Similar to "C" times  |
| Open Crosspoint(s)       |            |          |                       |
| Digital Output           | O255X      | 5.8msec  |                       |
| Clear Crosspoints        | P0X        | 18.9msec |                       |
|                          | P55X       | 20.5msec |                       |
|                          | Q1X        | 828msec  |                       |
| Delete Setup             | Q100X      | 21.0msec |                       |
|                          | R0X        | 628msec  |                       |
| Restore Defaults         | S0X        | 5.0msec  |                       |
| Programmed Settling Time | S65000X    | 7.3msec  |                       |
|                          | T7X        | 4.8msec  |                       |
|                          | U0X        | 4.5msec  |                       |
| Trigger Source Status    | U2,100X    | 6.8msec  |                       |
|                          | V00000000X | 124msec  | With no B/M rows set  |
|                          | V11111111X | 134msec  | With no B/M rows set  |
| Make/Break Rows          | V00001111X | 153msec  | With A,B,C,D as B/M   |
|                          |            |          | Similar to "V" times  |
|                          |            |          |                       |
| Break/Make Rows          | X          | 3.2msec  |                       |
| Execute                  | Y0X        | 4.7msec  |                       |
| Terminator               | Z0,0X      | 21.1msec |                       |
| Copy Setup               | Z0,100X    | 23.3msec |                       |
|                          | Z100,99X   | 21.6msec |                       |

Table 4-12. Typical Transmission and Hold-off Times for a Master and One Slave

| Description              | Command    | Time     | Notes                 |
|--------------------------|------------|----------|-----------------------|
| External Trigger         | A0X        | 12.3msec |                       |
| Matrix Ready             | B0X        | 12.3msec |                       |
| Close Crosspoint(s)      | CA1X       | 31.5msec |                       |
|                          | CA73X      | 48.7msec |                       |
|                          | CA1,A73X   | 51.0msec |                       |
| Display                  | D*****X    | 17.3msec |                       |
| Edit Pointer             | E0X        | 17.4msec |                       |
|                          | E55X       | 18.8msec |                       |
| Enable/Disable Triggers  | F0X        | 18.5msec |                       |
| Data Format              | G0X        | 11.7msec |                       |
| Insert Blank Setup       | I1X        | 1.7sec   |                       |
|                          | I100X      | 49.9msec |                       |
| Self-test                | J0X        | 3.4msec  |                       |
| EOI and Hold-off         | K0X        | 11.6msec |                       |
| Download Setup           | Lbbb...X   | 68.5msec | Binary format (G6,G7) |
| SRQ                      | M32X       | 12.0msec |                       |
| Open Crosspoint(s)       |            |          | Similar to "C" times  |
| Digital Output           | O255X      | 12.9msec |                       |
| Clear Crosspoints        | P0X        | 47.8msec |                       |
|                          | P55X       | 48.8msec |                       |
|                          | Q1X        | 1.7sec   |                       |
| Delete Setup             | Q100X      | 49.8msec |                       |
|                          | R0X        | 1.4sec   |                       |
| Restore Defaults         | S0X        | 11.9msec |                       |
| Programmed Settling Time | S65000X    | 14.6msec |                       |
|                          | T7X        | 12.3msec |                       |
| Trigger Source           | U0X        | 11.8msec |                       |
| Status                   | U2,100X    | 14.2msec |                       |
| Make/Break Rows          | V00000000X | 265msec  | With no B/M rows set  |
|                          | V11111111X | 284msec  | With no B/M rows set  |
|                          | V00001111X | 324msec  | With A,B,C,D as B/M   |
| Break/Make Rows          |            |          | Similar to "V" times  |
| Execute                  | X          | 10.5msec |                       |
| Terminator               | Y0X        | 11.5msec |                       |
| Copy Setup               | Z0,0X      | 51.1msec |                       |
|                          | Z0,100X    | 47.2msec |                       |
|                          | Z100,0X    | 55.4msec |                       |
|                          | Z100,99X   | 49.9msec |                       |

## 4.12 PROGRAMMING EXAMPLES

The following paragraphs give two examples of how to program the Model 707. As listed, the programs are not necessarily in the most efficient form, but instead are written for clarity in understanding program flow.

The first example (software only) shows how to handle a Model 707 interrupt condition with an SRQ interrupt routine. The second example (software and hardware) shows fast triggering of the Model 707.

### 4.12.1 Interrupt Example

In this example, an SRQ interrupts the controllers main task when the Model 707 is ready after a triggered setup change. You can program source and measure routines within the interrupt service routine.

A flowchart of the main program is shown in Figure 4-22; the interrupt service routine is shown in Figure 4-23. Highlights of program listed in Figure 4-24 are as follows:

1. Pending SRQs are cleared by serial polling the unit.
2. The interrupt condition (SRQ) and location of the interrupt service routine are defined.
3. The Model 707 is programmed for settling time, break/make rows, and trigger on GET. Stored setups of the unit are modified and the Relay Step Pointer is set to 000.
4. The unit is programmed to generate an SRQ on Matrix Ready by the M8X command.
5. The first setup is triggered. Interrupts are enabled and the main program loops until it detects an interrupt.

When an SRQ occurs, the program will service the SRQ as follows:

1. The Model 707 is first serial polled to obtain the status byte.
2. Bit 6 of the status byte is tested to determine if the SRQ was generated by the Model 707. If not, the service routine returns to the main task. This step is essential when more than one instrument is on the bus.
3. Bit 3 of the status byte is checked to determine if Matrix Ready is asserted. If so, the routine branches to a source/measure subroutine (commented out in this example).
4. Before returning to the main task, a new setup is triggered and interrupts are enabled.

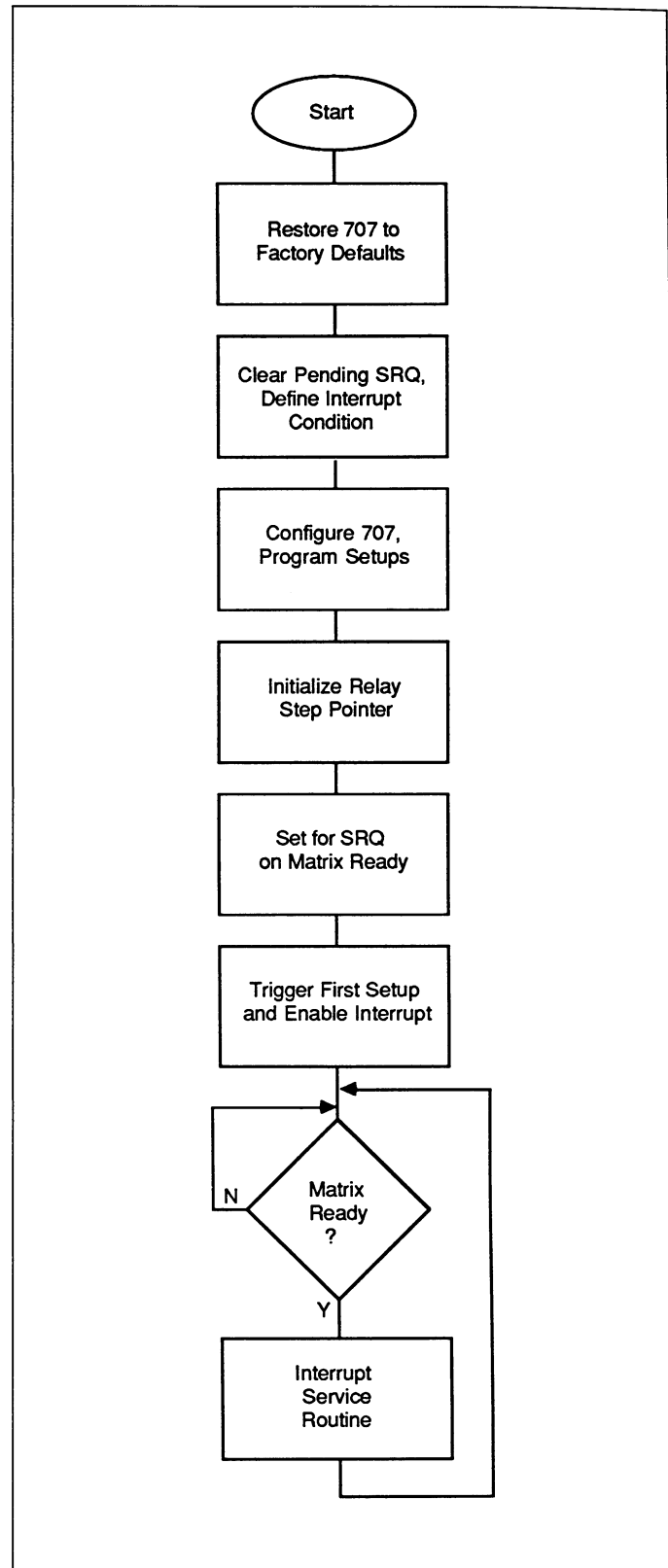


Figure 4-22. Interrupt Example Flowchart (Main Program)

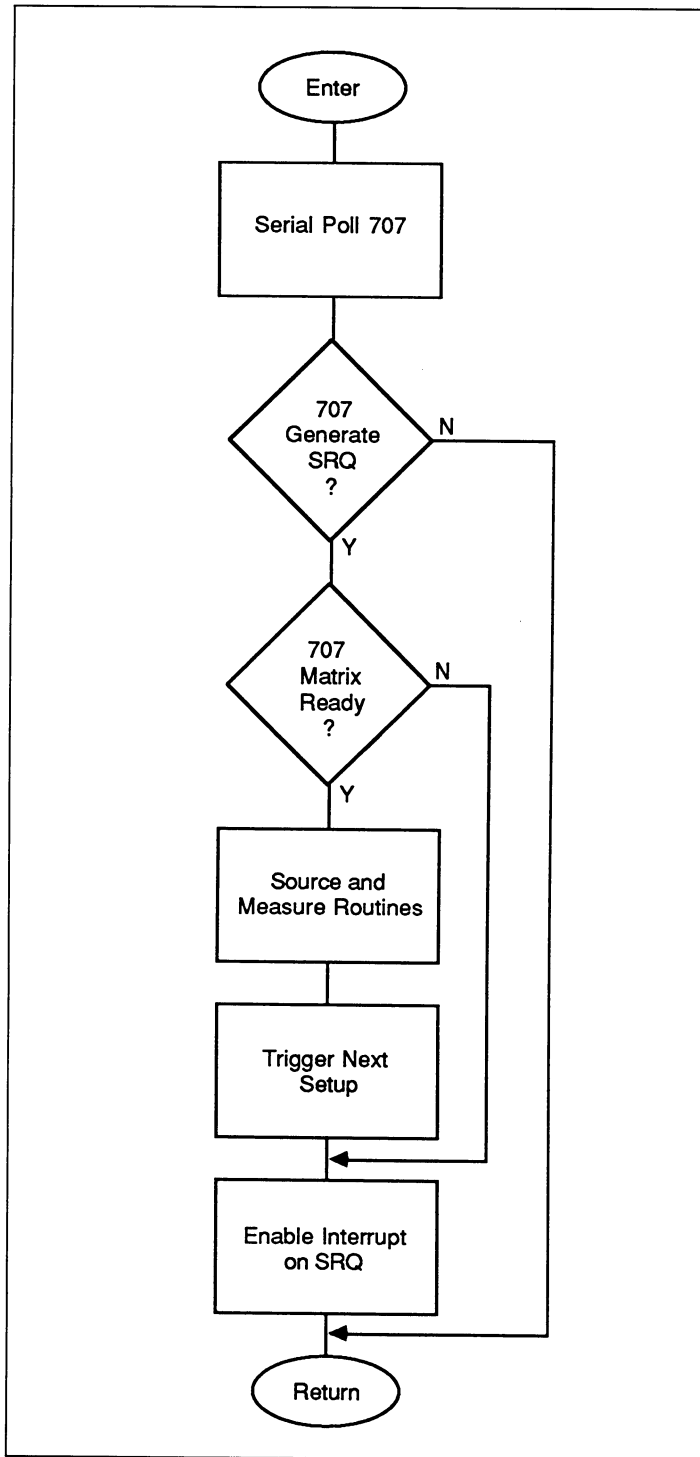


Figure 4-23. Interrupt Example Flowchart (Interrupt Service Routine)

```

10 ! ADDRESS 718 = MODEL 707 SWITCHING MATRIX
20 ! ADDRESS 706 = SOURCE INSTRUMENT
30 ! ADDRESS 714 = MEASURE INSTRUMENT
40 CLEAR 7 ! Send device clear to all
50 OUTPUT 718; 'R0X' ! Restore 707 factory defaults
60 S=SPOLL(718) ! Clear out pending SRQ
70 HPIB=7 ! Define interface select code
80 MASK=2 ! Define bit mask
90 ON INTR HPIB GOSUB SERVICE-ROUTINE ! Define interrupt initiated branch
100 ! CONFIGURE 707 MATRIX
110 REMOTE 718 ! Put 707 in remote
120 OUTPUT 718; 'S100X' ! Program settling time
130 OUTPUT 718; 'W11111111X' ! All rows break/make
140 OUTPUT 718; 'T2X' ! Trigger on GET
150 OUTPUT 718; 'F1X' ! Enable triggers
160 ! LOAD SETUPS
170 OUTPUT 718; 'E1XCA1,A2X' ! Setup #1 crosspoints
180 OUTPUT 718; 'E2XCB1,B2X' ! Setup #2 crosspoints
190 OUTPUT 718; 'E3XCC1,C2X' ! Setup #3 crosspoints
200 OUTPUT 718; 'E4XCD1,D2X' ! Setup #4 crosspoints
210 ! SELECT SETUP
220 OUTPUT 718; 'Z0,0X' ! Select setup #0
230 WAIT .2 ! Wait for setup to settle
240 OUTPUT 718; 'MX' ! Set mask for matrix ready
250 TRIGGER 718 ! Trigger first setup
260 ENABLE INTR HPIB; MASK ! Enable interrupts
270 WAIT-MATRIX-RDY: !
280 N=N+1 ! Wait for matrix to be ready
290 PRINT N ! Perform software housekeeping
300 GOTO WAIT-MATRIX-RDY ! at this time.
310 STOP
320 SERVICE-ROUTINE: !
330 S=SPOLL(718) ! Perform serial poll on 707
340 IF BIT(S,6)=0 THEN RETURN ! Determine if 707 generated SRQ
350 IF BIT(S,3)=1 THEN GOSUB SOURCE ! Determine if 707 matrix ready
360 ENABLE INTR HPIB ! Enable interrupt on SRQ
370 RETURN
380 SOURCE: !
390 ! OUTPUT 706; CURRENT ! Source test stimulus
400 MEASURE: !
410 ! ENTER 714; READING ! Make measurement
420 TRIGGER 718 ! Trigger next setup on 707
430 RETURN
440 END

```

Figure 4-24. Interrupt Example Program Listing

### 4.12.2 Triggering Example

This example uses the trigger connections of a Model 707, a voltage source, and a DMM to coordinate setup changes for measuring stepped voltages. It will run at a speed of approximately seven milliseconds per setup.

Figure 4-25 shows the trigger connections among the instruments. The switched source and measure paths are shown in Figure 4-26. In this example, no DUTs are used. The system only measures the output of the source.

Program operation is outlined below and flowcharted in Figure 4-27. The program listing is given in Figure 4-28.

1. Model 707 is initialized, setups 91-100 are modified, and the Relay Step pointer is set to 90.
2. A Model 230 voltage source is initialized and programmed to step through 1-10 volts.
3. A Model 196 DMM is initialized and programmed to store ten readings.
4. The voltage source outputs the value of the first step.

5. Model 707 triggers are enabled and a front panel keypress of the MANUAL trigger key is emulated with an H17X command. The following steps occur:
  - A. Setup 91 is triggered on the Model 707. Crosspoint A1 is closed.
  - B. The MATRIX READY output signal of the Model 707 triggers a reading on the DMM.
  - C. VOLTMETER COMPLETE of the DMM triggers the Model 707 to the next setup and the voltage source to the next step.
  - D. Steps b and c are repeated for the remaining setups and voltage steps.

A sample output of the program is shown below:

|    |         |
|----|---------|
| 1  | .99473  |
| 2  | 1.98907 |
| 3  | 2.99912 |
| 4  | 3.98823 |
| 5  | 4.99827 |
| 6  | 5.99784 |
| 7  | 7.00265 |
| 8  | 7.99699 |
| 9  | 9.0018  |
| 10 | 9.99614 |

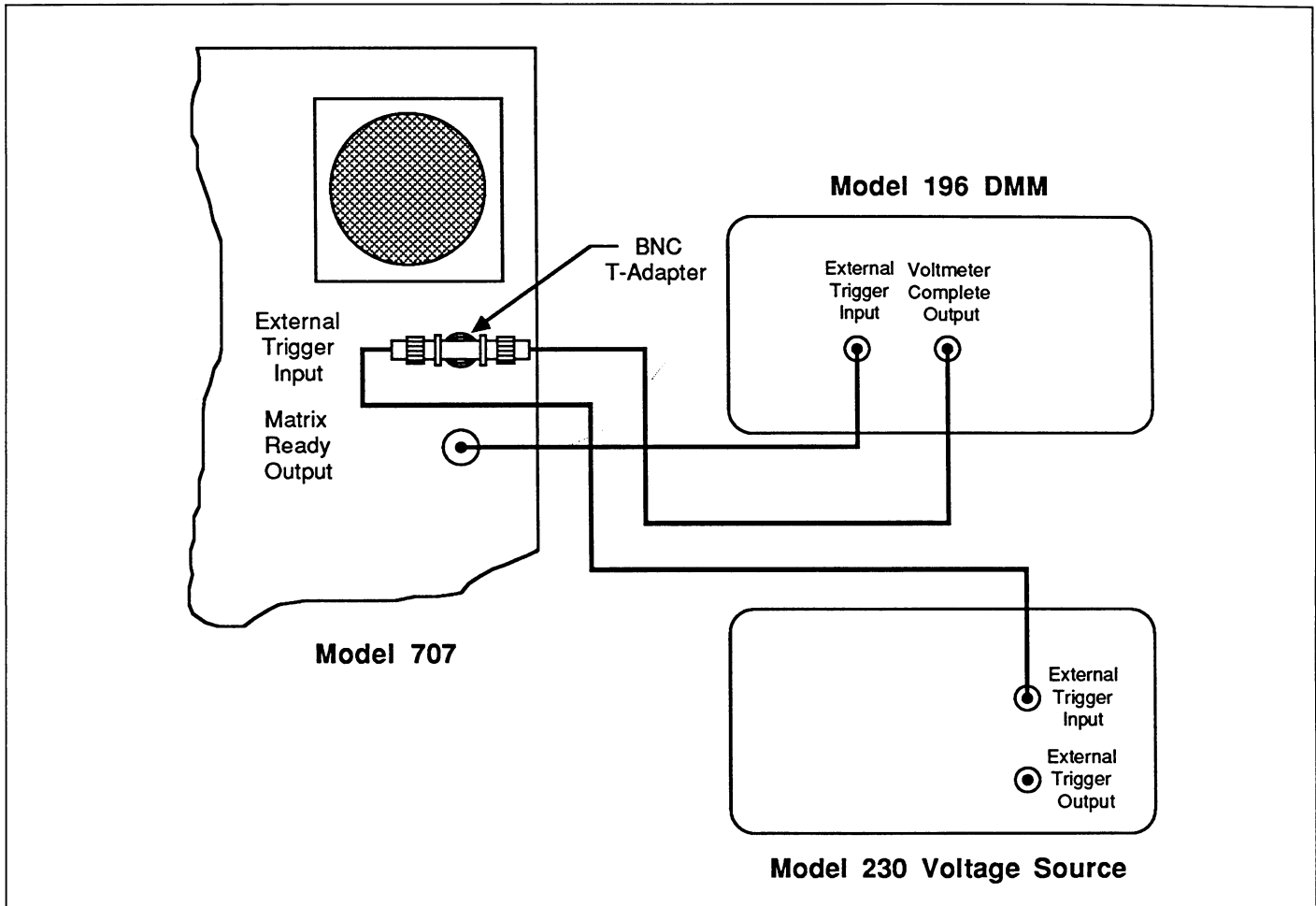


Figure 4-25. Trigger Connections for Triggering Example



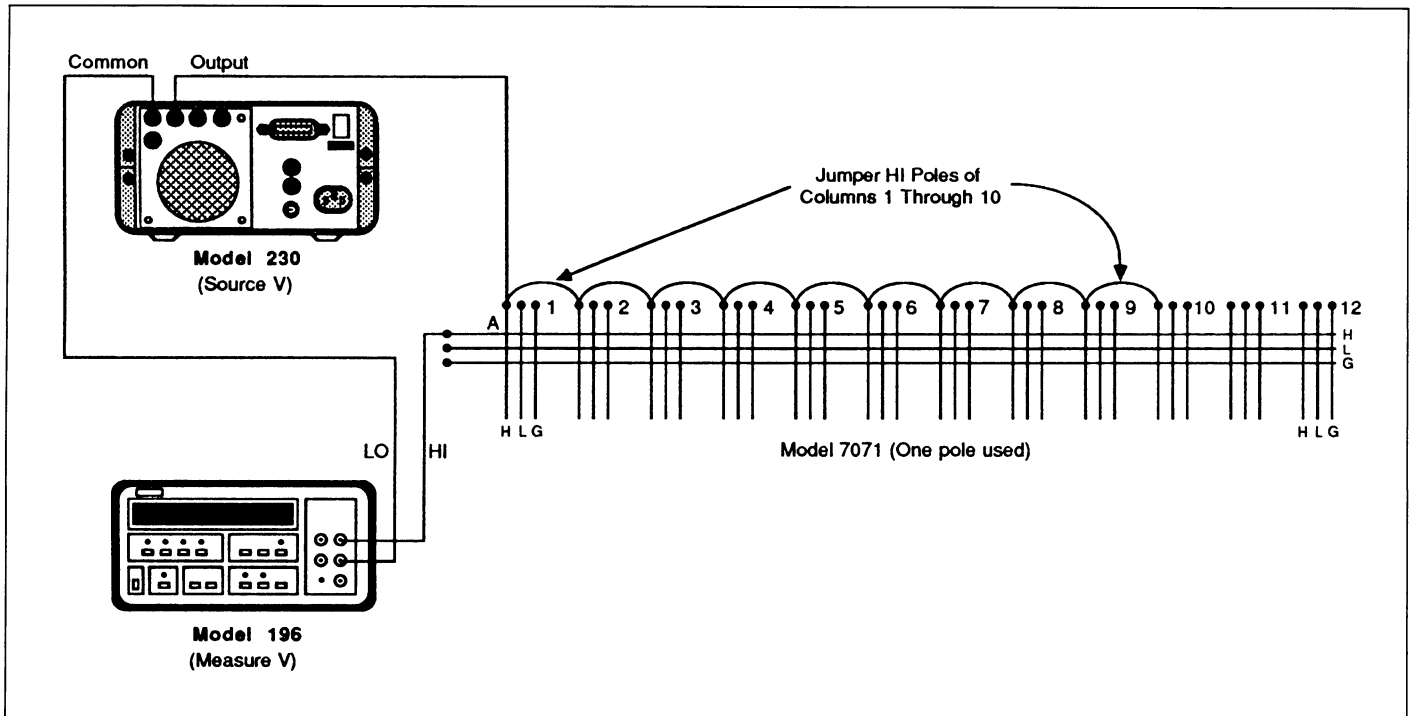


Figure 4-26. Analog Connections for Triggering Example

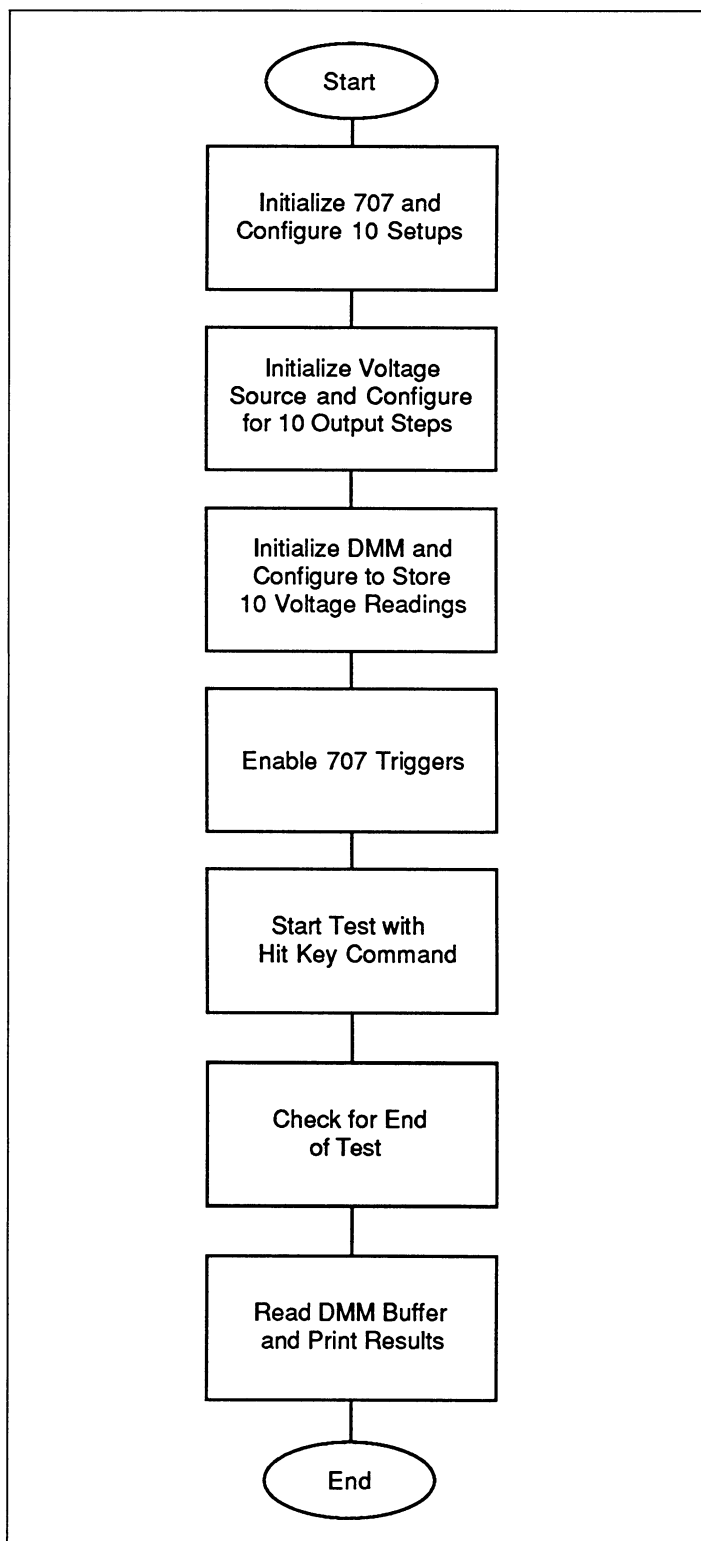


Figure 4-27. Triggering Example Flowchart

```

10 ! ADDRESS 718 = MODEL 707 SWITCHING MATRIX
20 ! ADDRESS 713 = MODEL 230 PROGRAMMABLE VOLTAGE SOURCE
30 ! ADDRESS 707 = MODEL 196 DMM
40 OUTPUT KBD;CHR$(255)&CHR$(75); PRINT ! Clear screen
50 PRINT "INITIALIZING THE INSTRUMENTS; PLEASE
 WAIT"
60 ! INITIALIZE THE 707 MATRIX
70 OUTPUT 718;"R0X" ! Restore defaults (clear 707)
80 FOR I=91 TO 100 ! Select stored setup
90 C=I-90 ! Select crosspoint column
100 OUTPUT 718;"E"&VAL$(I)&"CA"&VAL$(C)&"X" ! Configure setups
110 NEXT I
120 OUTPUT 718;"Z90,0X" ! Set Relay Step to 90
130 ! INITIALIZE THE 230 VOLTAGE SOURCE
140 CLEAR 713 ! Clear 230
150 OUTPUT 713;"R3I2F1X" ! 10V range, operate ON
160 FOR I=1 TO 10 ! Program 10 locations to be 1-10V
170 OUTPUT 713;"B"&VAL$(I)&"L"&VAL$(I)&"U"&
 VAL$(I)&"I2W5E-3X"
180 NEXT I
190 ! INITIALIZE THE 196 DMM
200 CLEAR 707 ! Clear 196
210 WAIT 2 ! Wait for DMM autocal
220 OUTPUT 707;"F0R3S0I10Q0A0N0T7X" ! 30Vdc range, 3½ digits, 10
230 ! buffer locations, one-shot
240 ! trigger, auto cal off, internal
250 ! filter off, ext. triggering
260 ! RUN THE TEST
270 OUTPUT KBD;CHR$(255)&CHR$(75); ! Clear screen
280 OUTPUT 713;"B1L1X" ! Set Vsource to step 1
290 OUTPUT 718;"F1X" ! Enable 707 triggers
300 OUTPUT 707;"M2X" ! Enable DMM SRQ on buffer full
310 OUTPUT 718;"H17X" ! Emulate 707 front panel trigger
320 CHECK: !
330 STATUS 7,7;S ! Check status register
340 IF BIT(S,10)=0 THEN GOTO CHECK ! Test for SRQ
350 S=SPOLL(707) ! Perform serial poll on DMM
360 IF BIT(S,6)=0 THEN GOTO CHECK ! Test for SRQ by DMM
370 OUTPUT 707;"B1G1X" ! Set DMM to dump buffer
380 FOR I=1 TO 10
390 ENTER 707;D
400 PRINT I,D ! Print readings
410 NEXT I
420 END

```

Figure 4-28. Triggering Example Program Listing

# SECTION 5

## Principles of Operation

---

### 5.1 INTRODUCTION

This section contains a functional description of the Model 707 in block diagram form as well as details of the various sections of the instrument. Information is arranged to provide a description of each of the functional blocks within the instrument. Many of these descriptions include simplified schematics and block diagrams. Detailed schematics and component layout drawings are located at the end of Section 7.

### 5.2 OVERALL FUNCTIONAL DESCRIPTION

The Model 707 mainframe contains three circuit boards, primarily digital, and two power supplies (5-volt and 6-volt). Relay matrix cards that plug into the mainframe have analog circuits for signal paths and digital circuits for control. Figure 5-1 shows the interconnection of the mainframe's digital board, front panel display board, and backplane in a block diagram.

The following paragraphs describe Model 707 circuitry by function, with some functions residing on more than one board (e.g., relay control circuits and display circuits).

### 5.3 MICROCOMPUTER

The Model 707 is controlled by an internal microcomputer. As shown in the block diagram of Figure 5-2 and schematic 707-106, the digital board contains the CPU, memory, and associated components:

- 68B09 microprocessor (U6)
- Oscillator (Y1)
- Power-up reset (U17)
- Address decoding PALs (U1, U2)
- 32Kx8-bit EPROM (U7)
- 32Kx8-bit RAM (U8) with battery back-up (BT1, U3)

The microcomputer centers around the 8-bit 68B09 microprocessor. The MPU has direct control over relay switching, front panel displays and switches, and rear panel interfaces (master/slave, digital I/O, IEEE-488 bus, and triggers).

Although the 68B09 microprocessor will operate at frequencies up to 8MHz, a clock frequency of 7.15909MHz is used to reduce interference with instruments that use measurement signals with harmonics of 1MHz. Crystal Y1 provides timing for the microprocessor. Internally, the clock frequency is divided down by four to obtain an operating frequency on the microprocessor bus of 1.78977MHz.

#### 5.3.1 Reset Circuit

The reset circuit, which is based on an 8211 (or 6728) voltage detector (U17), senses the output of the 5-volt power supply. When the output drops below approximately 4.6V, the 8211 asserts the RESET (low true) line. Two 1% resistors (R34 and R35) form a voltage divider, which is calibrated to match the comparator threshold voltage of the 8211 by either removing or leaving in R36, which is in parallel with R35.

During power-off or brownout conditions, the RESET line must be asserted before the 5-volt supply drops into the comparator threshold range (4.25 to 4.5 volts) of the DS-1210 non-volatile RAM controller (U3). During power-up, capacitor C91 is charged up to delay the RESET line going high for 110 to 260msec.

#### 5.3.2 Address Decoding

U1, a 16P8A programmable array logic (PAL) chip, decodes microprocessor address lines A15-A12 for the 32K EPROM (\$8000-\$FFFF) and the 32K bytes of bank-selected RAM (\$0000-\$2FFF). Bank selection is used so that the RAM appears as 12K bytes of address space to the microprocessor.

U1 also decodes the three bank-select lines (BS3-BS0) from the PB6-PB4 outputs of U9, a 6522A versatile interface adapter (VIA). The RAM is decoded as 8K bytes (\$0000-\$1FFF) and six 4K byte banks, which appear to the microprocessor at addresses \$2000-\$2FFF. This permits the microprocessor to select one of the six 4K byte banks.

Address decoding for peripheral devices on the microprocessor bus is performed by another 16P8A PAL

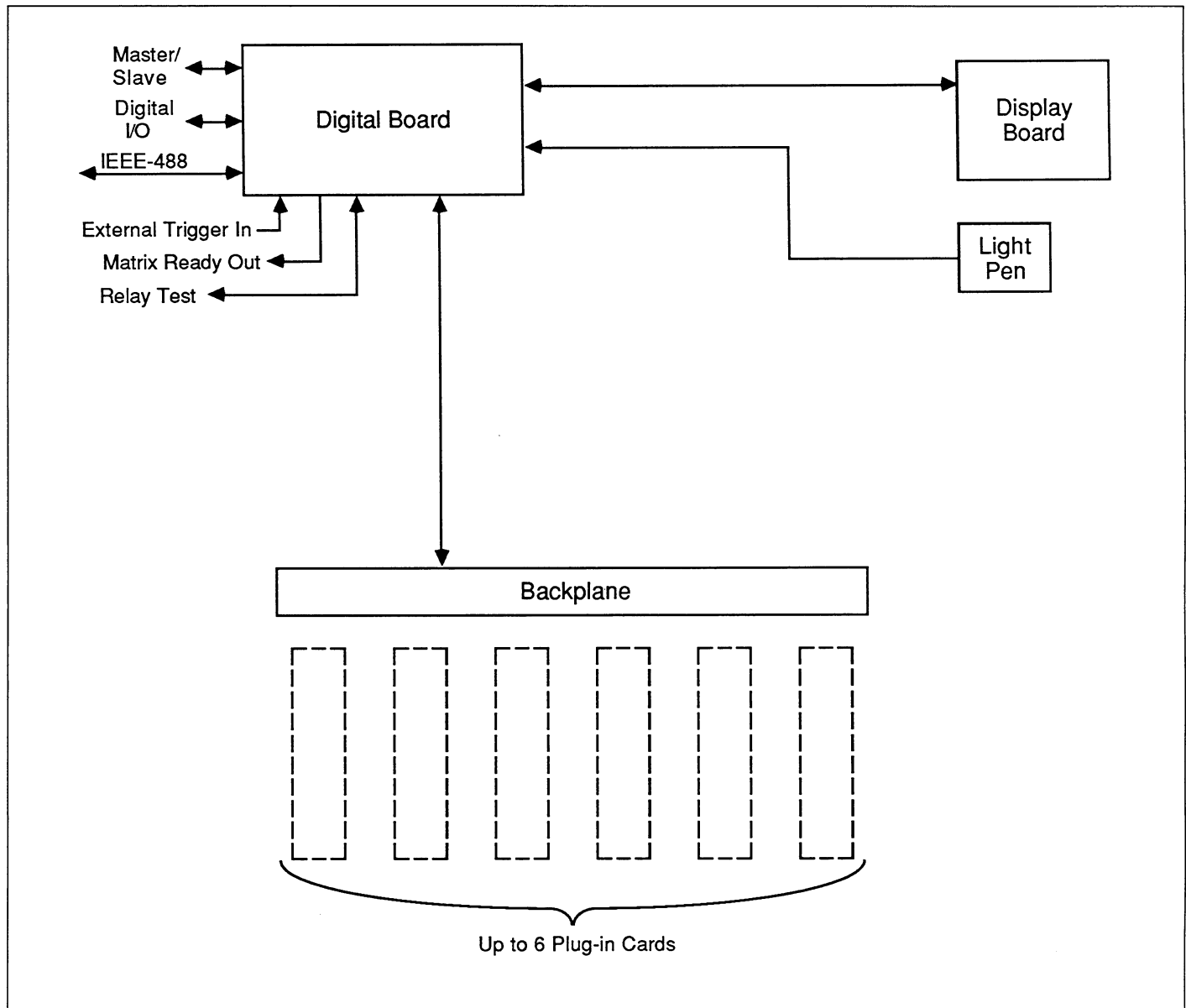


Figure 5-1. Model 707 Block Diagram

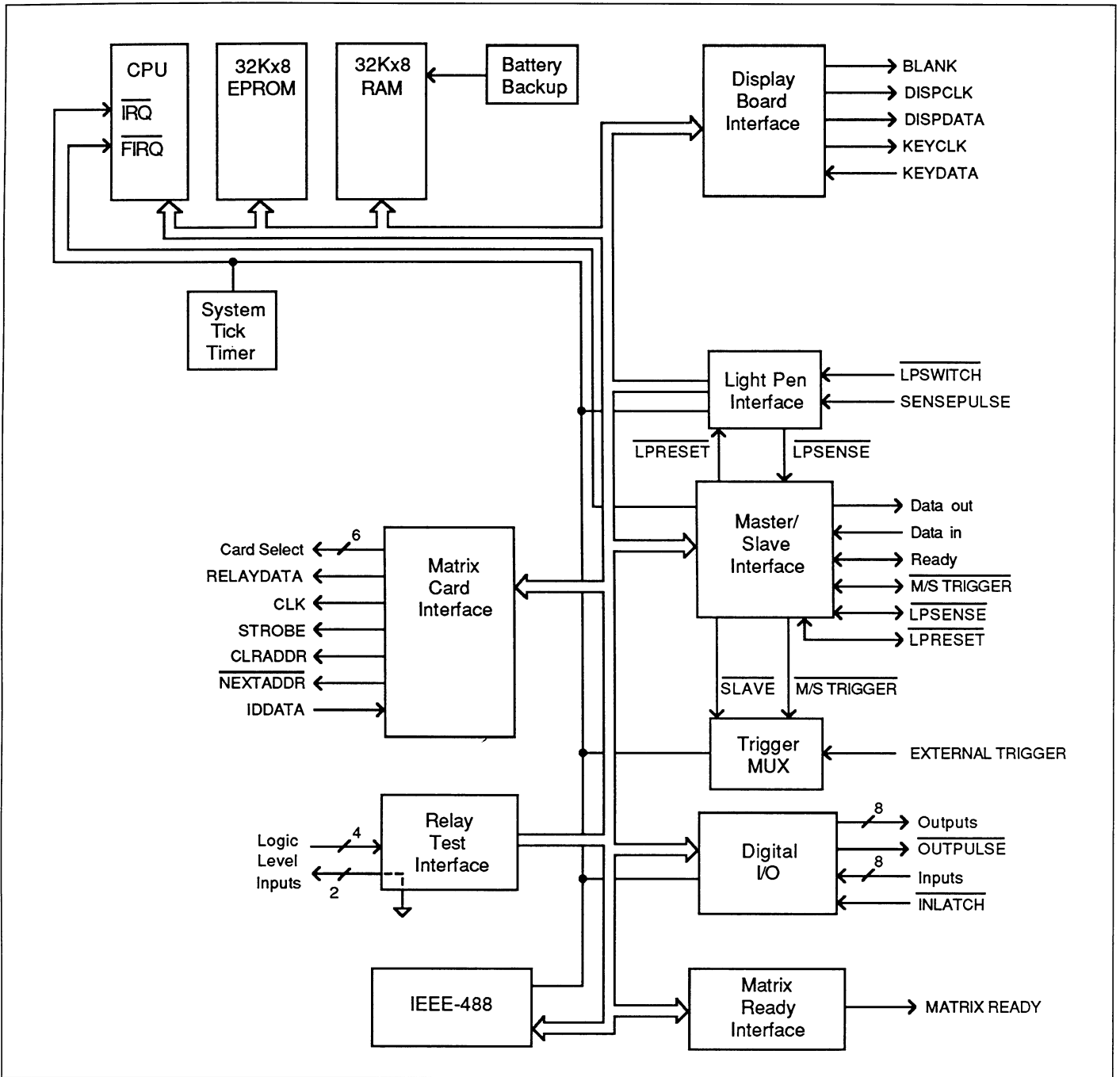


Figure 5-2. Digital Board Block Diagram

(U2). Peripheral devices are decoded at 16-byte intervals in the address range of \$3800-\$38FF. These include, for example, a 6522A VIA, a 65C21 peripheral interface adapter (PIA), and a 68B50 asynchronous communication interface adapter (ACIA).

### 5.3.3 Memory

The 32K bytes of instrument operation software are stored in U7, which is a 27256 EPROM. The revision level of the software is displayed on power-up.

U8 is a 32K byte static CMOS RAM chip that is used for storing relay setups and as a scratchpad during normal operation. Its power source and chip enable lines are routed through U3, a DS-1210 NVRAM controller. Figure 5-3 shows a simplified schematic of the RAM and battery back-up circuitry.

The NVRAM controller performs the functions of switching the RAM power source between Vcc and the lithium battery (BT1). It also disables chip enable (CE) to the RAM when Vcc is outside the specified limits. (See the paragraph describing the reset circuitry.)

In addition, if the battery power goes below a specified limit while Vcc is not present, a DS-1210 chip normally inhibits the second chip enable signal to the RAM after Vcc is restored. Since this feature is not used in the Model 707, the software always does a dummy read of memory locations \$0000-\$0001 on power-up to get past the second chip enable cycle. This permits the Model 707 to operate properly with no battery, or if the battery has been replaced.

## 5.4 RELAY CONTROL CIRCUITRY

The relay control circuits reside on the backplane board, digital board, and each matrix card.

The backplane board acts as a passive conduit for:

- Control signals from the digital board to the matrix cards and response from the matrix cards to the digital board.
- Power lines to the matrix cards.
- Expansion of analog signals among the matrix cards.

Operations of relay control circuitry on the digital board and a typical matrix card are described in the following paragraphs.

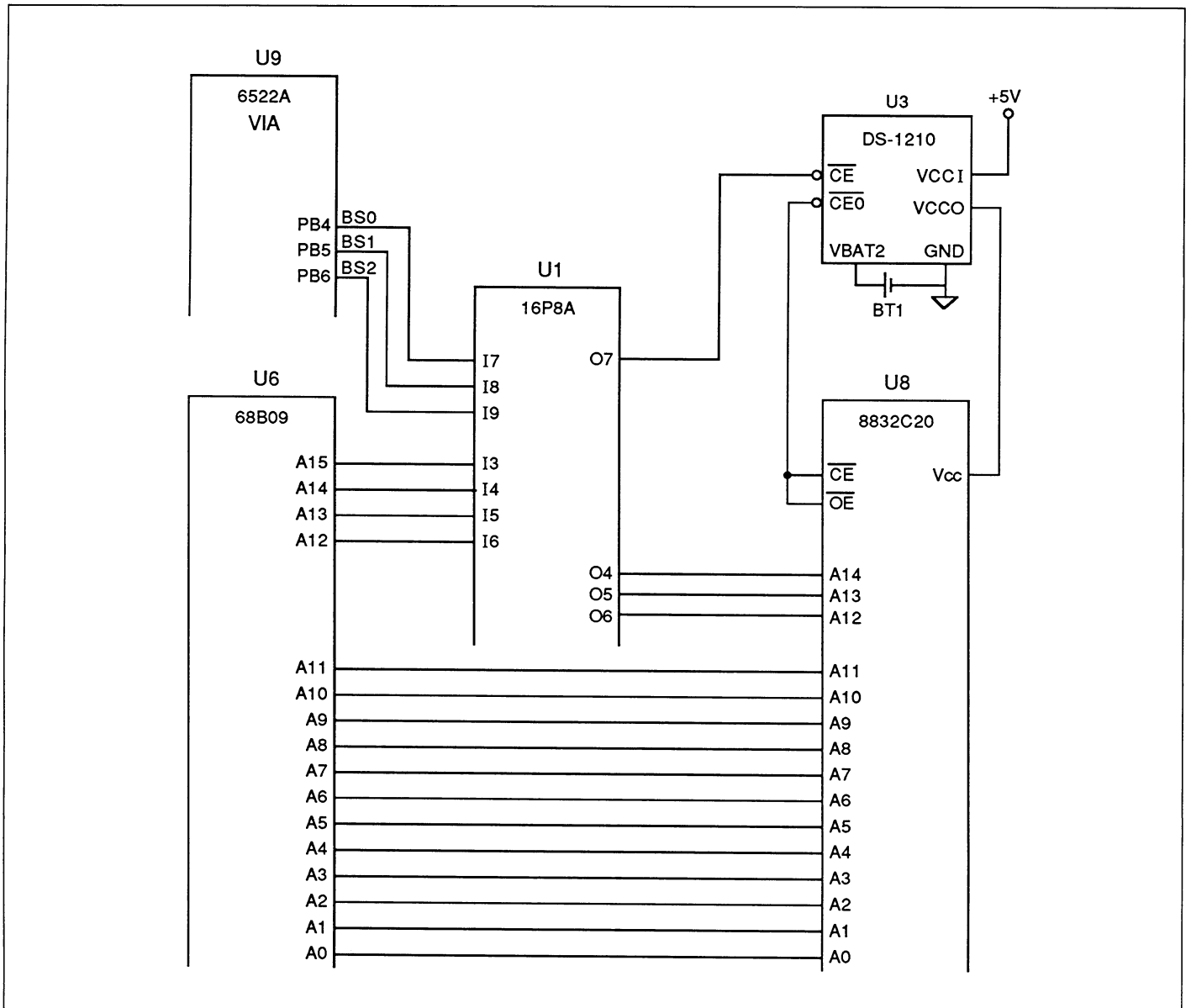


Figure 5-3. RAM and Battery Backup



### 5.4.1 Matrix Card Interface

A simplified schematic and timing diagram of the digital board's matrix card interface are shown in Figures 5-4 and 5-5. Also see schematic 707-106.

The microprocessor reads card identification data from the EPROM on each installed matrix card during power-up, and also sends relay control data to the matrix cards during the course of operation. This data is sent and received in serial form.

On the digital board, U25, U26, U5, U22, and U24 form an 8-bit parallel-in, serial out data converter. When the microprocessor executes a write to the address decoded for U25 (a 74HCT165 shift register), the data bus contents are loaded into U25, and all ones are loaded into U26 (another 74HCT165) causing its output OH to go high.

At the end of the write cycle, the SELECT RELAYDATA decode line goes high, causing the output of the U5 (74HCT08) AND gate to go high. This signal (ACTIVE)

gates the microprocessor E clock through a U22 (74HCT00) NAND, which is buffered by U28 and sent to the matrix cards as the CLK signal. Clock cycles are counted by the U26 shift register. After 8 cycles, OH of U26 returns low, disabling further CLK pulses.

The U24 (74HCT74) flip-flop and another U22 NAND gate are used to create an inverted version of CLK, which does not start until after one cycle of CLK. This signal is used to shift data out of the U25 RELAYDATA shift register.

### 5.4.2 Matrix Card Logic

See Figure 5-6 for a block diagram of the logic on a typical matrix card.

ON the matrix cards, the CLK and RELAYDATA signals are buffered and sent to a string of UCN-5841 serial input latched driver chips. The CLK signal is sent in parallel to all of the driver ICs and the serial data out of one driver is connected to the serial data in of the next.

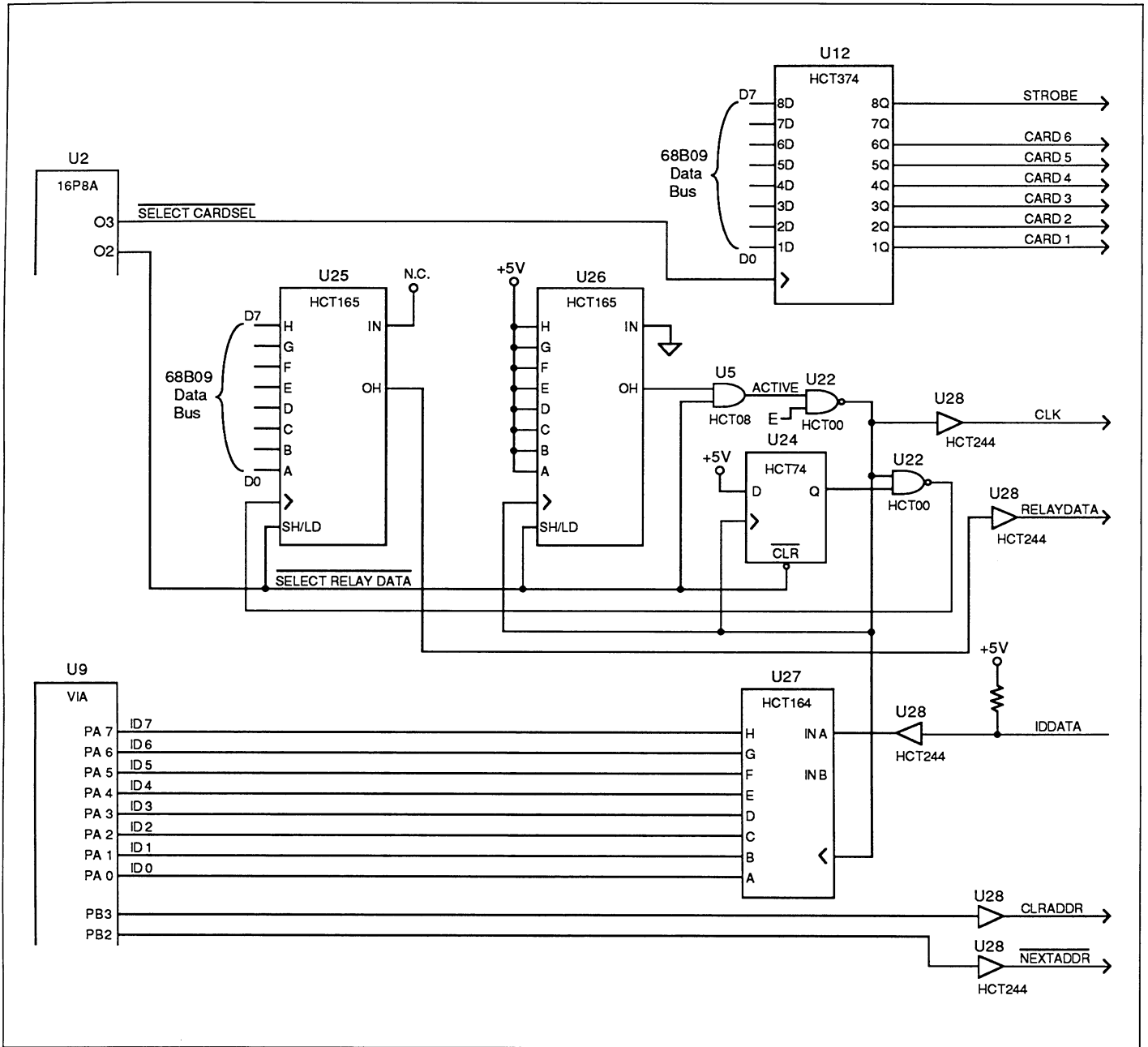


Figure 5-4. Matrix Card Interface Simplified Schematic

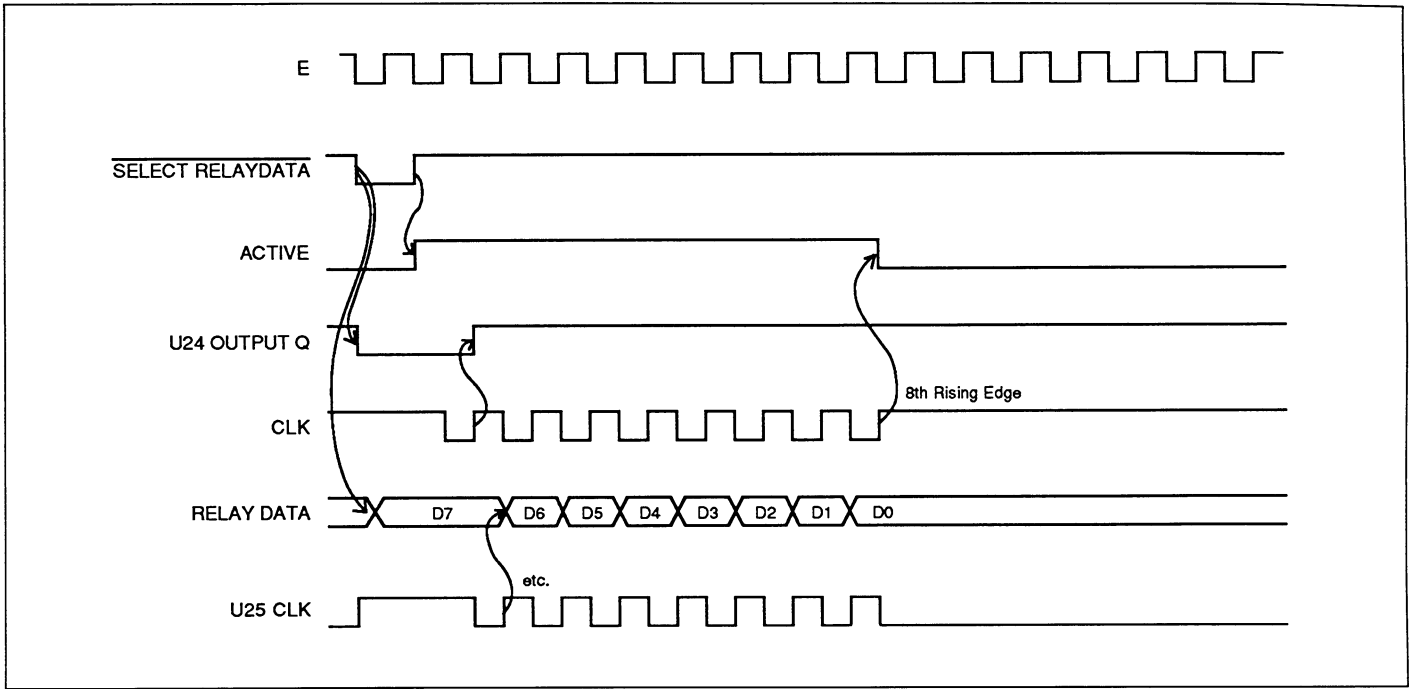


Figure 5-5. Matrix Card Interface Timing Diagram

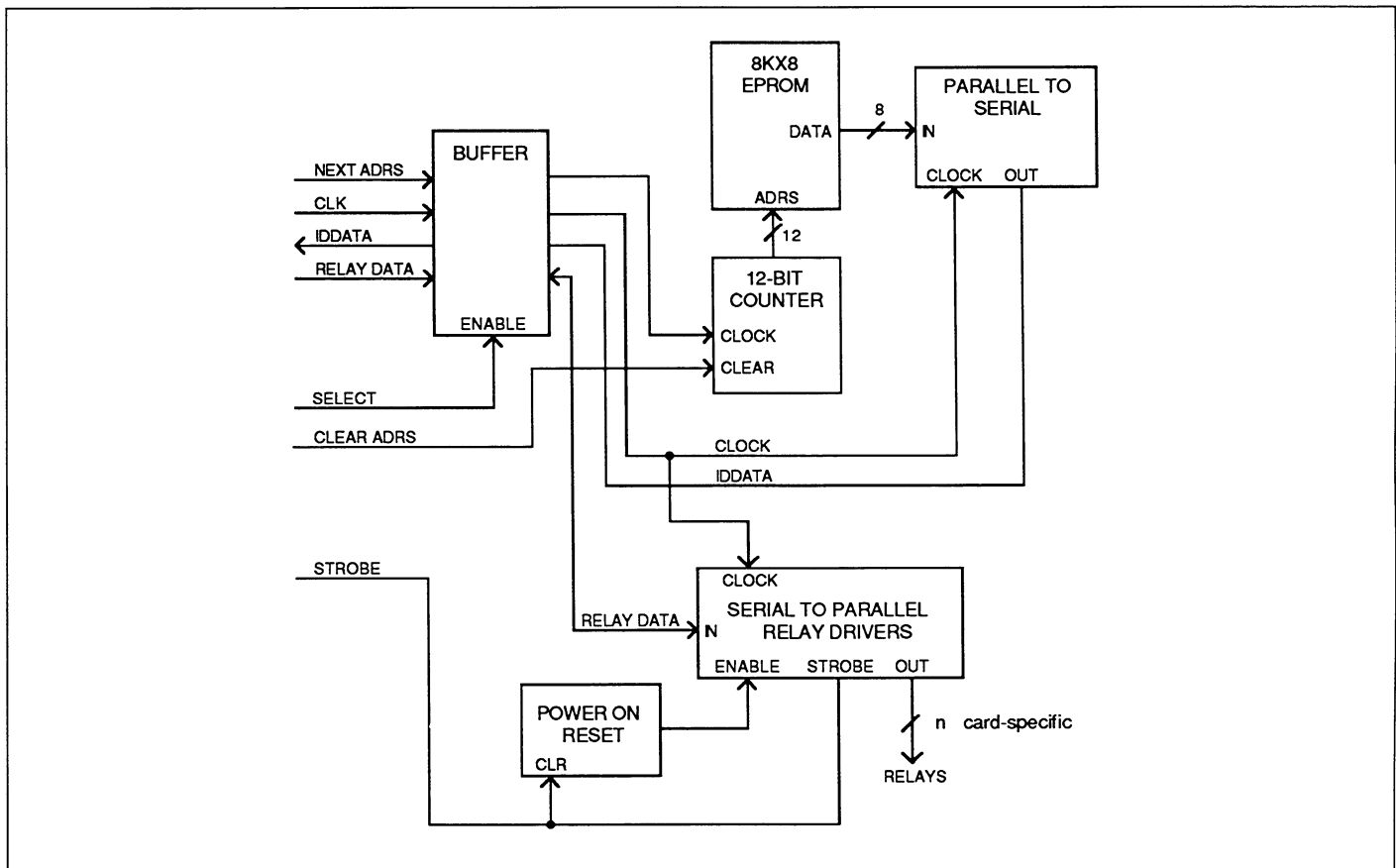


Figure 5-6. Typical Matrix Card Logic Block Diagram

### ID Data Circuits

Each matrix card has a 2764 EPROM that contains the following identification data:

- Card model number
- Relay (hardware) settling time
- Relay configuration table

The configuration table defines the location of each relay driver within the serial RELAYDATA bit stream. The table is necessary because the physical layout of matrix cards varies. In addition, the table accommodates row and/or column isolation relays, such as those on the Model 7072 card.

To read this ID data, the sequence below is performed upon power-up. Figure 5-7 shows the general timing of this sequence.

1. The CARSEL line is brought low, enabling the EPROM outputs. This line remains low throughout the ID data transmission sequence.
2. The CLRADDR line (generated by port signal PB3 of the digital boards VIA) is pulsed high to clear the 12-bit address counter (74HCT4040) to zero. At this point, an EPROM address of zero is selected. This pulse occurs only once.

3. The NEXTADDR line (PB2 of VIA) is set low. This increments the counter and enables parallel loading of the parallel-to-serial converter (74HCT165). NEXTADDR is kept low long enough for the counter to increment and the EPROM outputs to stabilize. This sequence functions because the LOAD input of the parallel-to-serial converter is level-sensitive rather than edge-sensitive. The first EPROM address used by the Model 707 is location one, not zero.

4. The same CLK signal that shifts RELAYDATA into the relay driver also clocks the parallel-to-serial converter to shift all eight data bits from the converter to the digital board via the IDDATA line. (This means that a byte of RELAYDATA must be sent to a matrix card to get the next byte of IDDATA.)

Steps 3 and 4 are repeated until all the necessary EPROM locations are read.

As seen in Figure 5-4 of the digital board, IDDATA is converted back to parallel by U27 (74HCT164) and is read by the microprocessor through the port A lines of U9 (6522A VIA).

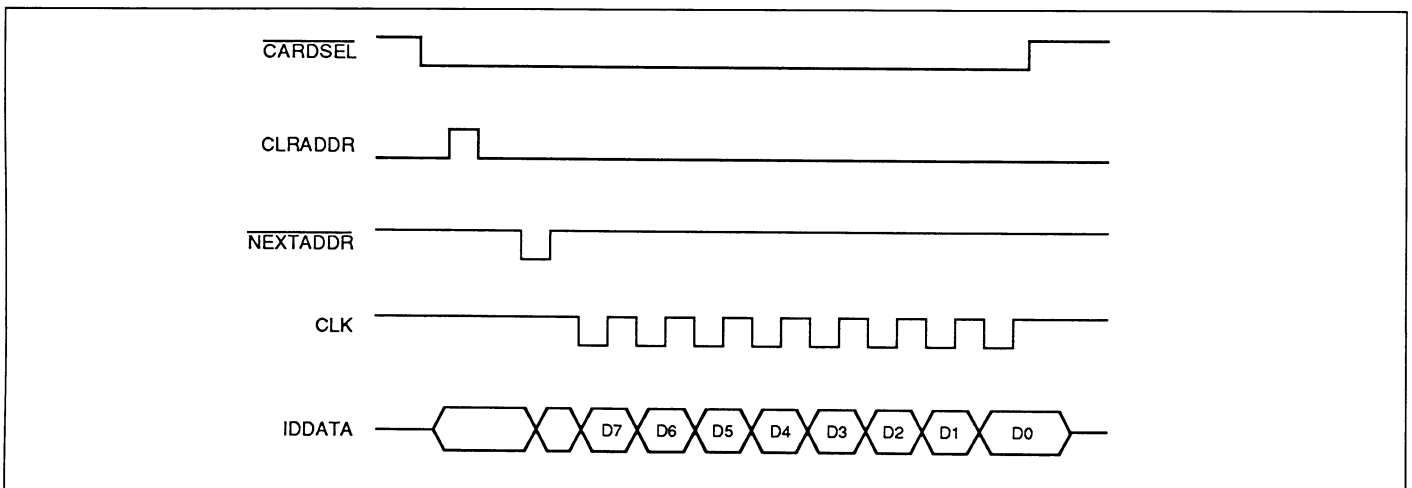


Figure 5-7. IDDATA Timing Diagram

### Relay Control

The CLK, RELAYDATA, and IDDATA lines are bused to all six card slots on the backplane board. A separate card select signal is sent to each matrix card to enable it for receiving RELAYDATA and sending IDDATA. The microprocessor controls the card select signals through U12 (74HCT374) on the digital board, which is decoded as an output port on the microprocessor bus.

The relays are controlled by the serial data transmitted via the RELAYDATA lines. Bytes for each card are shifted serially into latches located in the relay drivers. The serial data is fed in through the DATA lines under control of the CLK signal. As data overflows one register, it is fed out the Q'S line of that register to the next IC down the chain.

Once all the bytes have been shifted into each card in the mainframe, the STROBE line is set high to latch the relay information into the Q outputs of the relay drivers, and the appropriate relays are energized (assuming the driver outputs are enabled, as discussed below). Logic convention is such that the corresponding relay driver output must be low to energize the associated relay, while the output is high when the relay is de-energized. The STROBE signal is received by all matrix cards regardless of the state of their respective card select lines.

### Power-on Safeguard

Each matrix card has a power-on safeguard circuit to ensure that relays do not randomly energize upon power-up. Two NAND gates of a 74HCT00 are configured as an R-S flip-flop. On power-up, the Q output of the flip-flop is set high, holding the low true OEN (output enable) pins of the relay drivers high. Hence, the driver outputs are disabled and all relays remain de-energized regardless of the relay data information present at that time.

The falling edge of the first STROBE pulse that comes along (to load relay data) clears the R-S flip-flop, setting the OEN pins low and enabling the driver outputs. (At this time, valid relay control data has been sent to the matrix cards and is present in the latches of the driver chips.) This action allows the relays to be controlled by the transmitted relay data information.

### NOTE

The STROBE signal can be high or low on power-up since the outputs of U12 (74HCT374) on the digital board are undefined at power-on. Since a falling edge on STROBE, after the Output Enable circuit times out, enables the relay driver outputs, the power-up software must set STROBE low before the time-out. It then pulses STROBE high only after the relay driver shift registers have been cleared.

A hold-off period (typically 470msec) is included in the safeguard circuit to guard against premature enabling of the relays. The time constant of the hold-off period is determined by an R-C network.

## 5.5 DISPLAY CIRCUITRY

Model 707 display circuitry includes components needed to control the alphanumeric display, front panel annunciator LEDs, crosspoint LEDs, make/break and break/make LEDs, and needed to read front panel switches.

The display circuitry resides on the display board and digital board. See Figure 5-8 for a block diagram of the display board and Figure 5-9 for a simplified schematic of the display board interface on the digital board. Also refer to schematics 707-116 and 707-106 at the end of Section 7.

The front panel display is multiplexed as 15 columns of up to 63 segments each:

- There are 14 columns of alphanumeric display digits, plus a 15th column containing the discrete annunciator LEDs (TALK, LISTEN, REMOTE, MEMORY, RELAYS, etc.).
- There are 12 columns of crosspoint display LEDs. They line up under the right-most 12 alphanumeric digits.
- There is one column of Make/Break and Break/Make LEDs. It lines up under the column containing the discrete annunciators.

The first 15 display segments are the 14-segment plus decimal point alphanumeric digits (in columns 1-14), or the discrete annunciators (in column 15). The remaining 48 display segments are divided into six groups of eight, one group for each block of crosspoint displays (for columns 1-12), or for the Make/Break and Break/Make LEDs (for column 15). Segment assignments for the multiplexed columns are shown in Table 5-1.

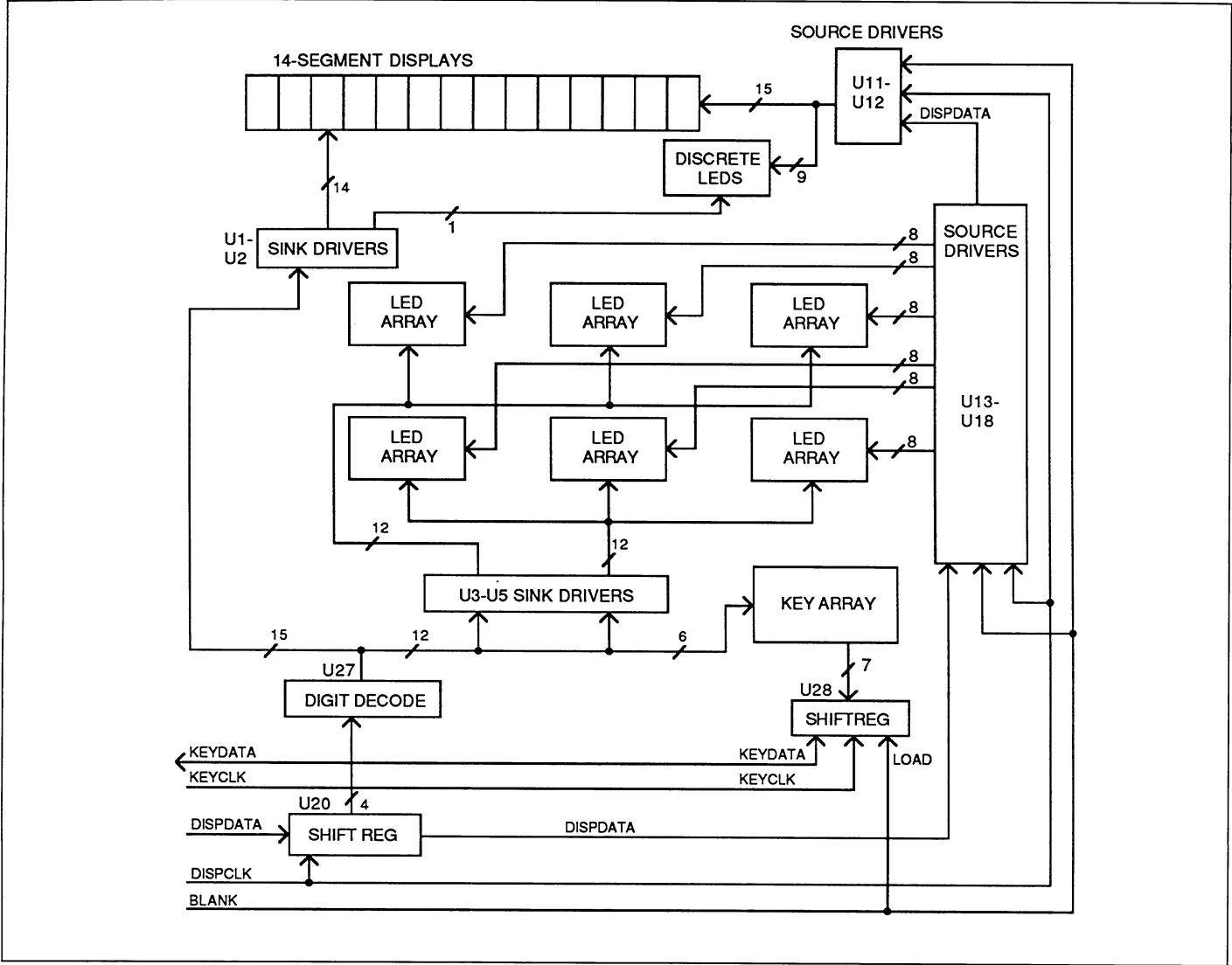


Figure 5-8. Display Board Block Diagram

**Table 5-1. Display Segment Assignments**

| Display Segments | Display MUX Columns             |                                |                           |                           |                            |                            |                                 |                           |
|------------------|---------------------------------|--------------------------------|---------------------------|---------------------------|----------------------------|----------------------------|---------------------------------|---------------------------|
|                  | 15                              | 14                             | 13                        | 12                        | 11                         | 10                         | 9                               | 8                         |
| 1-15             | Annunciator LEDs DS32-DS40      | Alphanumeric Digit 1 DS1 (MSD) | Alphanumeric Digit 2 DS1  | Alphanumeric Digit 3 DS2  | Alphanumeric Digit 4 DS2   | Alphanumeric Digit 5 DS3   | Alphanumeric Digit 6 DS3        | Alphanumeric Digit 7 DS4  |
| 16-23            | Upper Make/Break LEDs DS8-DS9   |                                |                           | Card 1 Column 1 DS8-DS9   | Card 1 Column 2 DS8-DS9    | Card 1 Column 3 DS8-DS9    | Card 1 Column 4 DS8-DS9         | Card 1 Column 5 DS8-DS9   |
| 24-31            |                                 |                                |                           | Card 2 Column 1 DS12-DS13 | Card 2 Column 2 DS12-DS13  | Card 2 Column 3 DS12-DS13  | Card 2 Column 4 DS12-DS13       | Card 2 Column 5 DS12-DS13 |
| 32-39            | Upper Break/Make LEDs DS18-DS19 |                                |                           | Card 3 Column 1 DS16-DS17 | Card 3 Column 2 DS16-DS17  | Card 3 Column 3 DS16-DS17  | Card 3 Column 4 DS16-DS17       | Card 3 Column 5 DS16-DS17 |
| 40-47            | Lower Make/Break LEDs DS20-DS21 |                                |                           | Card 4 Column 1 DS20-DS21 | Card 4 Column 2 DS20-DS21  | Card 4 Column 3 DS20-DS21  | Card 4 Column 4 DS20-DS21       | Card 4 Column 5 DS20-DS21 |
| 48-55            |                                 |                                |                           | Card 5 Column 1 DS24-DS25 | Card 5 Column 2 DS24-DS25  | Card 5 Column 3 DS24-DS25  | Card 5 Column 4 DS24-DS25       | Card 5 Column 5 DS24-DS25 |
| 56-63            | Lower Break/Make LEDs DS30-DS31 |                                |                           | Card 6 Column 1 DS28-DS29 | Card 6 Column 2 DS28-DS29  | Card 6 Column 3 DS28-DS29  | Card 6 Column 4 DS28-DS29       | Card 6 Column 5 DS28-DS29 |
| Display Segments | Display MUX Columns             |                                |                           |                           |                            |                            |                                 |                           |
|                  | 7                               | 6                              | 5                         | 4                         | 3                          | 2                          | 1                               |                           |
| 1-15             | Alphanumeric Digit 8 DS4        | Alphanumeric Digit 9 DS5       | Alphanumeric Digit 10 DS5 | Alphanumeric Digit 11 DS6 | Alphanumeric Digit 12 DS6  | Alphanumeric Digit 13 DS7  | Alphanumeric Digit 14 DS7 (LSD) |                           |
| 16-23            | Card 1 Column 6 DS10-DS11       | Card 1 Column 7 DS10-DS11      | Card 1 Column 8 DS10-DS11 | Card 1 Column 9 DS10-DS11 | Card 1 Column 10 DS10-DS11 | Card 1 Column 11 DS10-DS11 | Card 1 Column 12 DS10-DS11      |                           |
| 24-31            | Card 2 Column 6 DS12-DS13       | Card 2 Column 7 DS14-DS15      | Card 2 Column 8 DS14-DS15 | Card 2 Column 9 DS14-DS15 | Card 2 Column 10 DS14-DS15 | Card 2 Column 11 DS14-DS15 | Card 2 Column 12 DS14-DS15      |                           |
| 32-39            | Card 3 Column 6 DS16-DS17       | Card 3 Column 7 DS16-DS17      | Card 3 Column 8 DS18-DS19 | Card 3 Column 9 DS18-DS19 | Card 3 Column 10 DS18-DS19 | Card 3 Column 11 DS18-DS19 | Card 3 Column 12 DS18-DS19      |                           |
| 40-47            | Card 4 Column 6 DS22-DS23       | Card 4 Column 7 DS22-DS23      | Card 4 Column 8 DS22-DS23 | Card 4 Column 9 DS22-DS23 | Card 4 Column 10 DS22-DS23 | Card 4 Column 11 DS22-DS23 | Card 4 Column 12 DS22-DS23      |                           |
| 48-55            | Card 5 Column 6 DS24-DS25       | Card 5 Column 7 DS26-DS27      | Card 5 Column 8 DS26-DS27 | Card 5 Column 9 DS26-DS27 | Card 5 Column 10 DS26-DS27 | Card 5 Column 11 DS26-DS27 | Card 5 Column 12 DS26-DS27      |                           |
| 56-63            | Card 6 Column 6 DS28-DS29       | Card 6 Column 7 DS28-DS29      | Card 6 Column 8 DS30-DS31 | Card 6 Column 9 DS30-DS31 | Card 6 Column 10 DS30-DS31 | Card 6 Column 11 DS30-DS31 | Card 6 Column 12 DS30-DS31      |                           |

Note:  
 MSD - most significant digit  
 LSD - least significant digit

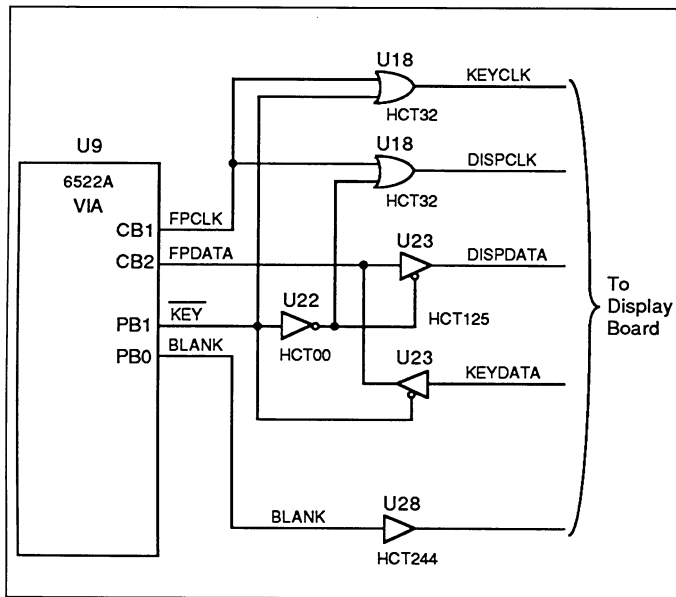


Figure 5-9. Display Interface Simplified Schematic

### 5.5.1 Display Data

The digital board sends display data (DISPDATA) to the display board serially. Nine bytes are required: eight bytes of segment data and one byte to select the column.

All displays are arranged as common cathode. The segment anodes are driven by source drivers U11-U18 (UCN5895A) connected in a serial data chain. Each source driver has eight outputs and an output disable. (While new data is being sent to the display drivers, the BLANK line is set high to disable the outputs.) Display data is first shifted into serial-to-parallel converter U20 (74HCT164) and then to the chain of segment source drivers.

Four parallel outputs of U20 are decoded by the 4-to-16 decoder U27 (74HCT154). The outputs of this decoder drive the display cathodes through sink drivers U1-U5 (UDN2597A).

### 5.5.2 Front Panel Keys

The front panel keys are SPST normally-open pushbutton switches. They are connected in a 6-column by 7-row array.

Each column is connected to one of the column drive outputs of decoder U27 (74HCT154) through a diode. The diodes isolate the columns from one another in case more than one key on the same row is pressed. As the display is multiplexed, each key column is pulled low in its turn. The other columns float.

Each row is connected to Vcc through a pull-up resistor and to one of the inputs of parallel-to-serial converter U28 (74HCT165). The load input of U28 is connected to the BLANK signal, so that each time new data is sent to the display, another column of the key array gets latched into U28.

### 5.5.3 Display Interface

The display interface circuitry of the digital board generates clock signals and communicates serial data for the front panel display and keyboard. The shift register of U9 (6522A VIA) controls the display and reads the key array. Pin CB1 is the clock signal (FPCLK) and pin CB2 is the data signal (Fpdata).

Signal FPCLK is generated for both serial output (display) and serial input (keyboard) operations. This single bi-directional port is converted to two uni-directional ports by two OR gates of U18 (74HCT32), two tri-state drivers of U23 (74HCT125), and a NAND gate of U22 (74HCT00) used as an inverter.

When the low true KEY signal (generated on VIA pin PB1) is asserted, DISPCLK is held high, KEYCLK follows FPCLK, the KEYDATA driver (U23) is enabled, and the DISPDATA driver (U23) is disabled.

When the low true KEY signal is negated, the KEYCLK signal is held high and the DISPCLK signal follows FPCLK. Also, the KEYDATA driver is disabled while the DISPDATA driver is enabled. The remaining signal that goes to the display board, BLANK, is generated by VIA output pin PB0.

### 5.5.4 Refresh Display/Read Keyboard

The refresh display/ read keyboard sequence is as follows:



**NOTE**

During power-on hardware initialization, the U9 shift register (6522A VIA) is configured for output and the low true KEY signal is negated.

1. Set the BLANK line high.
2. Send nine bytes of DISpdata out the VIA shift register to drive the next column in the multiplex sequence.
3. Set the BLANK line low.
4. Configure the VIA shift register for input.
5. Assert the low true KEY line.
6. Read the VIA shift register to cause the KEYDATA to be shifted into the VIA.
7. Read the VIA shift register again to get the byte of KEYDATA.
8. Negate the low true KEY line.

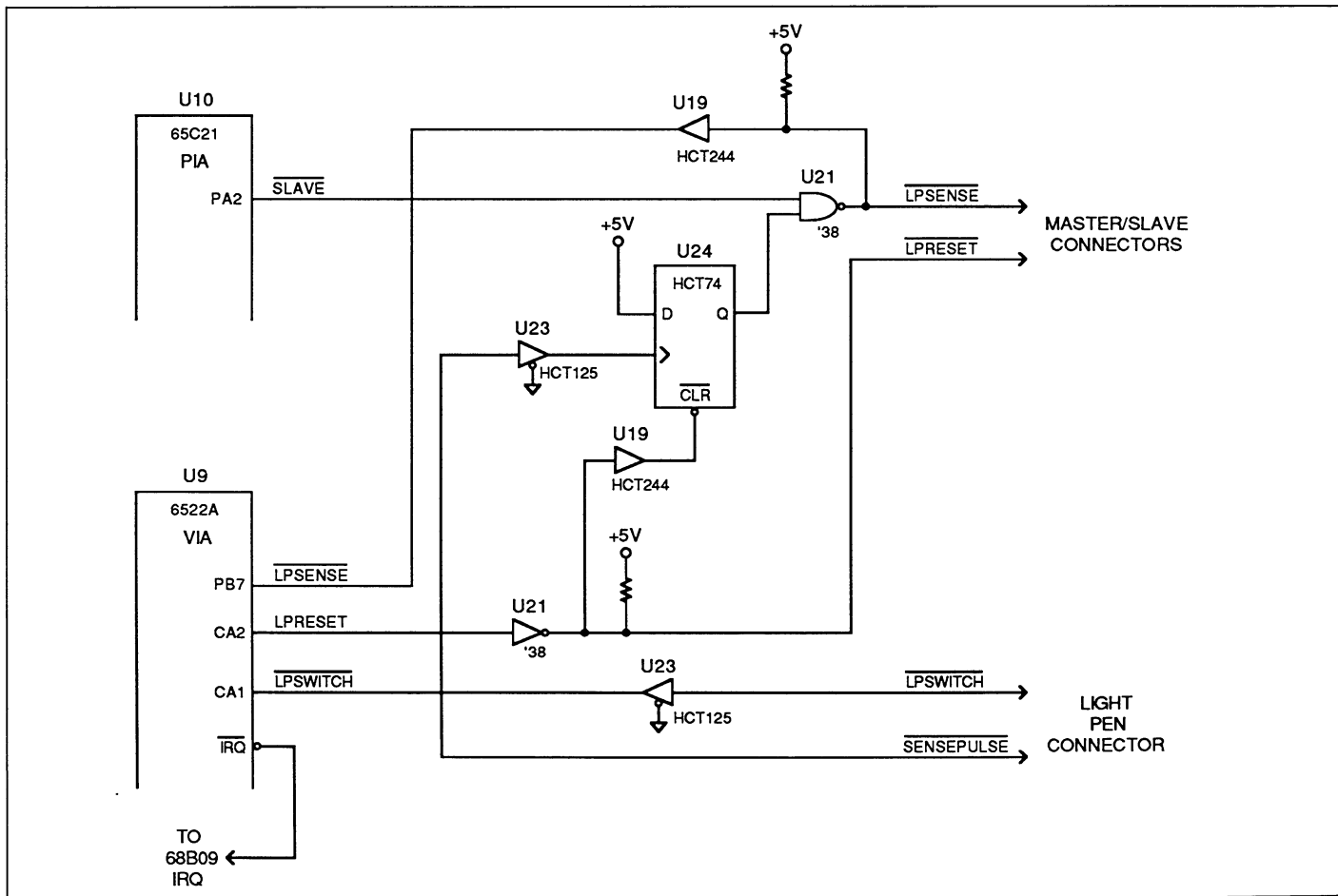
9. Configure the VIA shift register for output.

**5.6 LIGHT PEN INTERFACE**

The light pen interface circuitry resides on the digital board. A simplified schematic is shown in Figure 5-10.

The light pen is a self-contained unit requiring only a 5-volt supply. It returns two TTL-compatible signals:

- **SENSEPULSE** — A low-going pulse of about 15 $\mu$ sec width that occurs as the light intensity at the end of the pen rises above a preset threshold.
- **LPSWITCH** — A debounced switch signal that is low while the light pen pushbutton is depressed.



**Figure 5-10. Light Pen Interface Simplified Schematic**

The rising edge of SENSEPULSE clocks a high into flip-flop U24 (74HCT74). This converts the signal into a level that can be read by the microprocessor through the PB7 input of U9 (6522A VIA) as signal LPSENSE (low true).

If the low true SLAVE signal from U10 (65C21 PIA) is high (i.e., the unit is either a stand-alone or master), the microprocessor reads its own U24 flip-flop. The output of U24 also gets driven onto the LPSENSE line of the master/slave connectors. If SLAVE is asserted, the microprocessor reads the LPSENSE signal from the master/slave connectors.

The microprocessor can clear flip-flop U24 by setting the LPPRESET output of U9 high. This signal also gets driven onto the master/slave connectors by open-collector driver U21. This method permits the master and all slaves in a master/slave system to read and clear the U24 flip-flop in the master unit. Thus, one light pen can serve for all units, while each unit controls its own display for the scan routine.

The switch signal (low true LPSWITCH) goes to the CA1 interrupt input of the VIA, which is programmed to generate an IRQ interrupt on the falling edge of LPSWITCH. The interrupt service routine stops the normal display refresh multiplexing and takes over control of the display.

The routine then scans the display one column at a time, clearing flip-flop U24 before scanning each column. After the display is scanned, the processor examines the LPSENSE signal to determine if the light pen "sees" one of the LEDs that is currently being scanned.

If a master scans its display and gets no response from the light pen, it instructs the slaves in turn to scan their displays. Each slave monitors the U24 flip-flop of the master to check whether or not the light pen "sees" any of the LEDs that are lit on its own display.

## 5.7 MASTER/SLAVE CIRCUITRY

The master/slave interface is a closed loop of serial communication and bused control signals. Its control circuitry resides on the digital board. See Figure 5-11 for a simplified schematic.

Each mainframe has a Master/Slave In connector and a Master/Slave Out connector. Serial data is sent from the TXDATA pin of the output connector to the RXDATA pin of the input connector on the next mainframe in the loop. All other interface signals (M/S TRIGGER, ALLREADY, LPPRESET, and LPSENSE) are common to input and output connectors. The light pen signals LPPRESET and LPSENSE are described in paragraph 5.6.

### 5.7.1 Serial Communication

Serial data communication is managed by U11 (68B50 ACIA) and automatic retransmit logic: a U22 NAND gate used as an inverter, U5 AND gates, and U18 OR gate.

The RTS (low true) output of U11 controls the automatic retransmission of serial data. Stand-alone and master units assert RTS to gate the TXDATA output of U11 through a U5 AND gate and via U18 onto the TXDATA pin of the master/slave interface. Relay K1 is energized (as shown) whenever power is applied to the Model 707.

Slave units negate RTS except when responding to a request by the master for setup or status information. A negated RTS signal blocks the TXDATA signal at its corresponding U5 AND gate. Incoming serial data to the RXDATA input of U11 is also routed through a U5 gate, the U18 OR gate, and onto the master/slave TXDATA pin to effect the automatic retransmission.

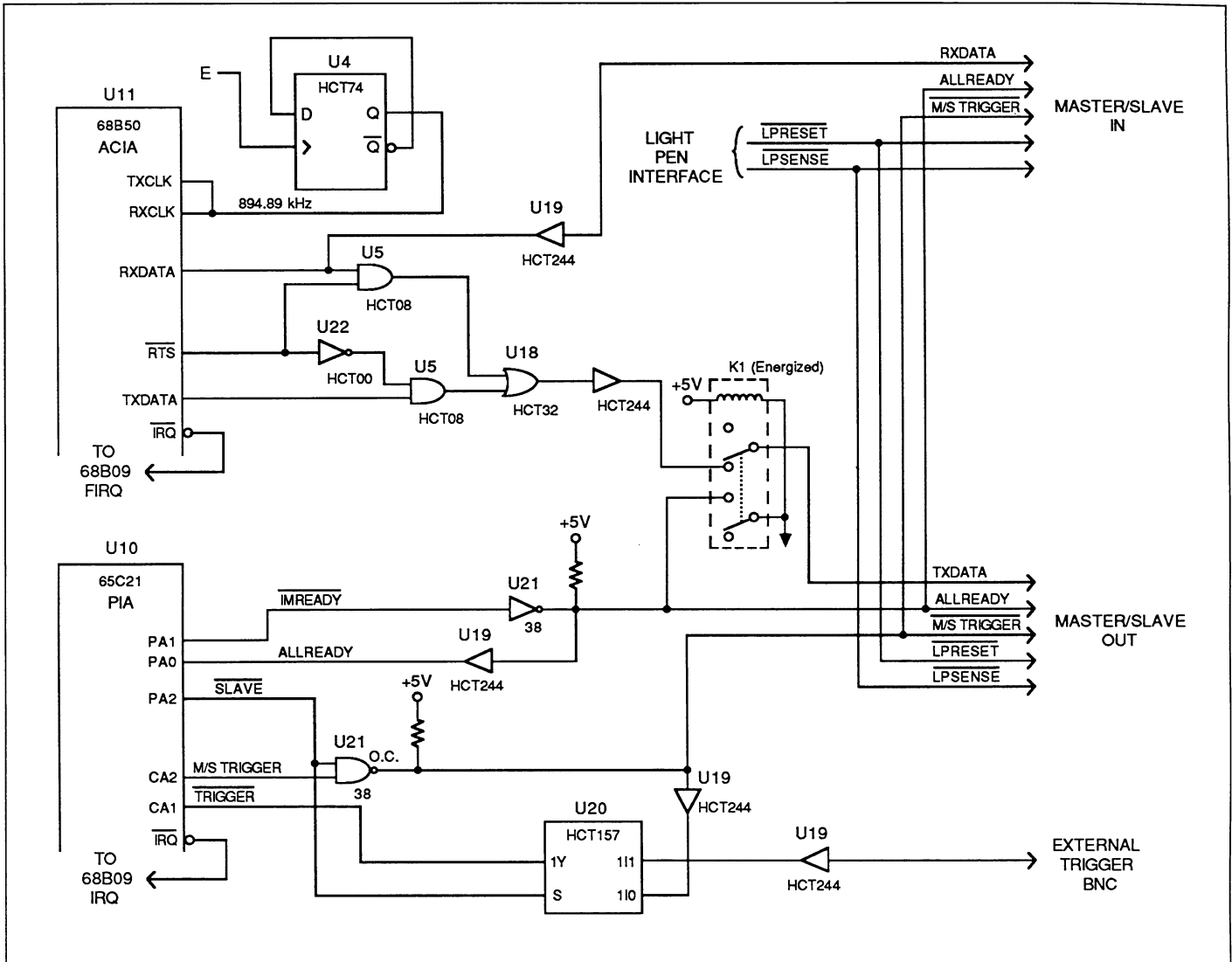


Figure 5-11. Master/Slave Interface Simplified Schematic

## 5.7.2 Control Signals

Bused control signals are managed by U10 (65C21 PIA). Bused outputs are driven by U21 (7438 open-collector NAND) and are buffered for input with U19 receivers.

The SLAVE (low true) signal selects the external trigger source:

- In slave units, the SLAVE signal is asserted. This causes multiplexer U20 to select the M/S TRIGGER (low true) signal for the trigger interrupt. Slaves are disabled from driving the M/S TRIGGER signal by the U21 NAND.
- In master or stand-alone units, SLAVE is negated and multiplexer U20 selects the External Trigger Input BNC for the trigger interrupt. This also enables the unit to drive the M/S TRIGGER signal via the U21 NAND.

The ALLREADY signal is wire-ORed so that it is negated whenever any unit in the master/slave loop has negated its IMREADY (low true) signal when receiving and processing data. When a slave unit is powered down, relay K1 provides a path to digital ground, simulating a negated IMREADY signal. This provides positive indication to other units on the master/slave loop that one of the units is not running. (Power-up software waits until all units have asserted their IMREADY signal.)

Assertion of M/S TRIGGER when the ALLREADY signal is false causes the slave units to reset to a known state. (Asserting M/S TRIGGER with ALLREADY true triggers the slaves.)

## 5.8 DIGITAL I/O

The digital input and digital output ports are two separate interfaces, even though they are on the same DB25 connector. A simplified schematic of the circuitry is shown in Figure 5-12.

Digital inputs are managed by U13 (74HCT373 transparent latch), which is decoded as a port on the microprocessor bus by PAL U2. U13 latches in the states of lines IN(0:7) whenever the INLATCH (low true) signal is asserted or the microprocessor reads the digital input port.

To cause the microprocessor to read the input lines, the INLATCH signal is also routed to the CB1 input of U10

(65C21 PIA), which is programmed to generate an interrupt on a falling edge.

Digital outputs are managed by port B of U10. The OUTPULSE (low true) signal on pin CB2 is asserted for about one E clock cycle (600nsec) after the microprocessor has written to the port B output lines OUT(0:7).

## 5.9 IEEE-488 BUS INTERFACE

The Model 707 has an IEEE-488 standard interface that allows the instrument to be programmed from a system controller. Commands can be sent over the bus to the instrument and data can be requested from the instrument.

The IEEE-488 interface is made up of U14, U15, and U16. U14 is a 9914A GPIA (general purpose interface adapter), while U15 and U16 are interface bus drivers.

The GPIA simplifies microprocessor interfacing to the IEEE-488 bus because many control sequences take place automatically. For example, when the microprocessor writes to the GPIA data output register, the handshake sequence is performed automatically. Without the GPIA chip, complex microprocessor routines would be required.

On the microprocessor side of the GPIA, data transmission is handled much like any other data bus transaction. Microprocessor data access is performed through the D0-D7 lines, while A0-A2 (the three least significant address lines) select among the 14 internal registers (seven read, seven write) of the GPIA. Chip selection is performed by the SELECT 9914 line.

The output of the GPIA is in standard IEEE-488 format. All of these lines are active low with approximately zero volts representing a logic one:

- Eight data lines (D1-D8).
- Three handshake lines (DAV, NRFD, NDAC).
- Five management lines (ATN, REN, IFC, SRQ, EOI).

The two IEEE-488 bus drivers, U15 and U16, bring the drive capability of the interface up to the requirements of the IEEE-488 standard, which includes provisions for up to 15 devices on the bus at once. The outputs of the bus drivers are connected to J25, a standard IEEE-488 connector.

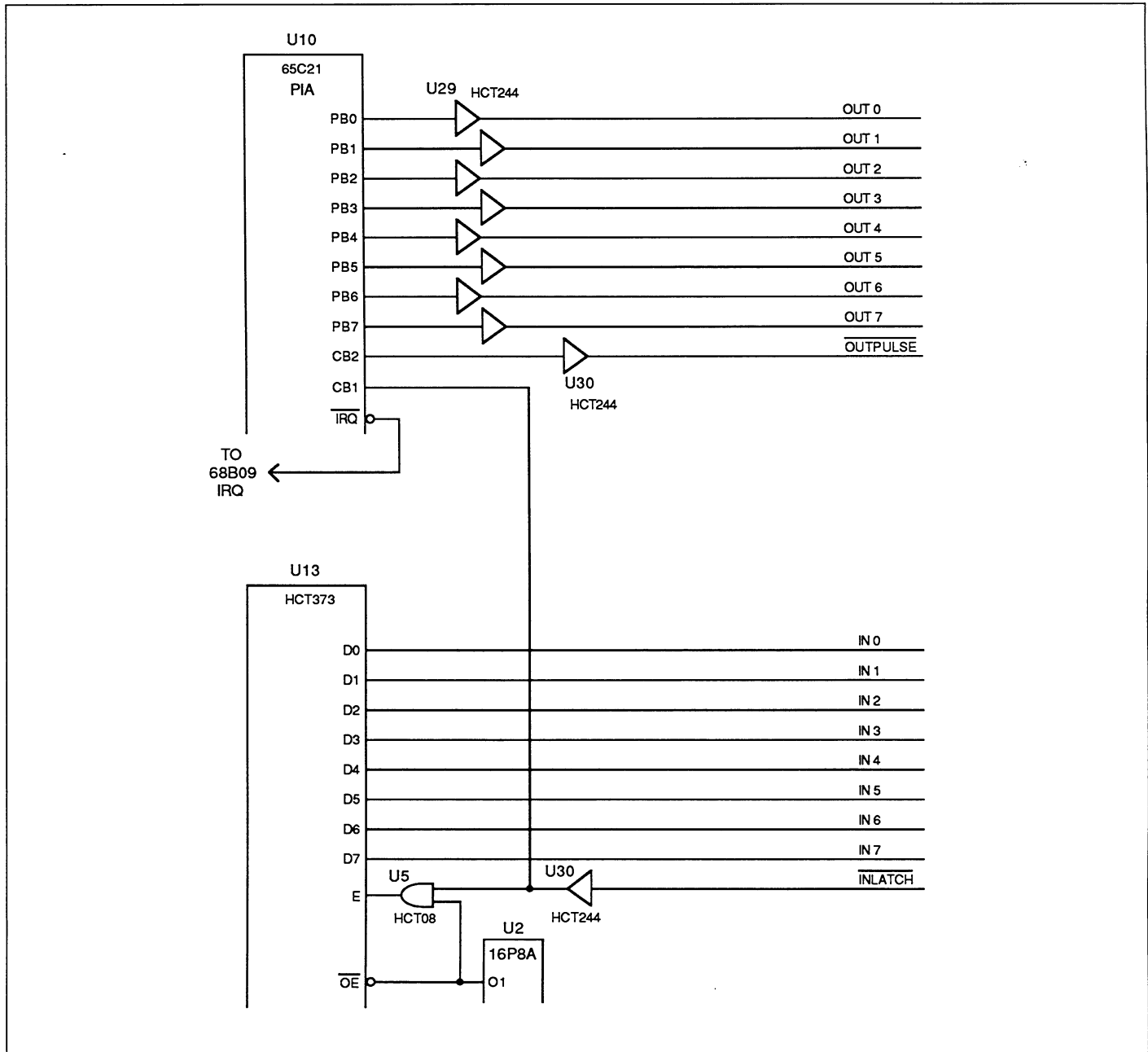


Figure 5-12. Digital I/O Interface Simplified Schematic

## 5.10 POWER SUPPLIES

The major components of the power section are two open-frame linear power supplies. A 5-volt supply powers all logic circuitry, including that on the matrix cards. A 6-volt supply drives the front panel display and relay coils on the matrix cards.

Sources for the display and relay coils are separated to reduce noise on the coils induced by multiplexing the display. This is done with an additional voltage regulator (mounted above the 6-volt supply) that gets its input from the unregulated voltage of the power supply.

The 6-volt outputs are distributed to the backplane board

for the matrix cards and to the display board. The 5-volt supply output is routed through the digital board and then via ribbon cables to the display and backplane boards.

The primary sides of the two power supplies are connected to the line voltage selector switch on the rear panel through a barrier strip. This switch selects between two primary voltage ranges by connecting the two primary windings of each power supply either in parallel (115V) or in series (230V).

The rear panel fan is also connected to the line voltage switch. The fan is always in parallel with one and only one primary winding of the power supplies, so that it receives the correct voltage regardless of the switch setting.

# SECTION 6

## Maintenance

### 6.1 INTRODUCTION

This section contains information necessary to maintain and troubleshoot the Model 707 Switching Matrix. Handling and cleaning procedures are also included.

#### WARNING

**The servicing procedures in this section are intended only for qualified electronics service personnel. Do not attempt to perform these procedures unless you are qualified to do so. Some of the procedures may expose you to potentially lethal voltages (>30V RMS) that could result in personal injury or death if normal safety precautions are not observed.**

The section is outlined as follows:

- 6.2 **Line Voltage Selection:** Provides the procedure for setting the instrument operating voltage.
- 6.3 **Fuse Replacement:** Gives the procedure for replacing the line fuse located on the rear panel.
- 6.4 **Fixed Rack Installation:** Lists the installation instructions for a fixed rack mount kit for a Model 707.
- 6.5 **Disassembly:** Covers the procedure for disassembling the instrument, including circuit board removal.
- 6.6 **Battery Replacement:** Outlines the procedure for replacing the lithium battery.
- 6.7 **Static-sensitive Devices:** Covers precautions necessary when handling static-sensitive parts within the instrument.
- 6.8 **Mainframe Troubleshooting:** Outlines troubleshooting procedures for the Model 707.
- 6.9 **Relay Test Software:** Describes operation of software used to check the functionality of relay cards installed in Model 707 units.
- 6.10 **Cleaning:** Gives the procedure for cleaning the backplane board and the fan filter.

### 6.2 LINE VOLTAGE SELECTION

The Model 707 can be operated from either 105-125V or 210-250V, 50 or 60Hz sources. It can be rewired at the factory for the 90-110V and 180-220V ranges. The unit was shipped from the factory set for the operating voltage that corresponds to the one normally available in your area. The appropriate box on the rear panel (see Figure 6-1) is marked to indicate this range. The position of the switch indicates the selected line voltage. To change the line voltage proceed as follows.

#### WARNING

**Disconnect the Model 707 from the power line and all other sources.**

1. Locate the line voltage switch on the rear panel. Referring to Figure 6-1, place the switch in the correct position: to the right for low line voltage (90 to 125V) or to the left for high line voltage (180 to 250V).
2. Install a fuse consistent with the operating voltage, as described in paragraph 6.3.

#### CAUTION

**The correct fuse type must be used to maintain proper instrument protection.**

3. Mark the appropriate box on the rear panel for the selected line voltage. (Use Freon<sup>®</sup> or a similar fluid to remove any old marks.)

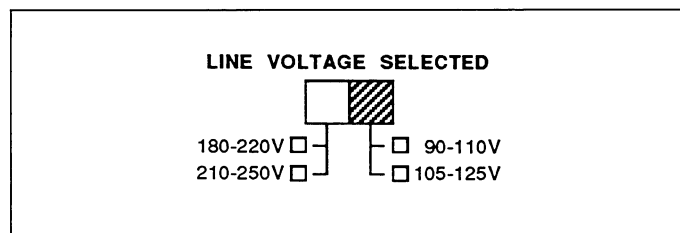


Figure 6-1. Line Voltage Switch

### 6.3 FUSE REPLACEMENT

The line fuse, located on the rear panel, protects the power line input of the instrument. Use the following procedure to replace the fuse, if necessary.

**WARNING**

**Disconnect the instrument from the power line and other equipment before replacing the fuse.**

1. With the power off, place the end of a flat-bladed screwdriver into the slot in the rear panel fuse holder. Press in gently and rotate the fuse holder approximately one-quarter turn counterclockwise. Release pressure on the holder and allow the internal spring to push the carrier and fuse out of the holder.
2. Separate the fuse from the carrier by carefully pulling the two apart.
3. Using an ohmmeter, check the fuse for continuity. A good fuse will show low resistance, while a blown fuse will read high (essentially infinite) resistance.
4. If the old fuse is defective, replace it with the type recommended in Table 6-1.

**CAUTION**

**Do not use a fuse with a higher rating than specified, or instrument damage may occur. If the instrument repeatedly blows fuses, locate and correct the cause of the problem before resuming operation of the unit.**

5. Install the new fuse, located in the fuse carrier, by reversing the above procedure.

**Table 6-1. Line Fuse Values**

| Line Voltage Range   | Fuse Rating              | Keithley Part No. |
|----------------------|--------------------------|-------------------|
| 90-110V or 105-125V  | 2A, slow blow, 250V, 3AG | FU-25             |
| 180-220V or 210-250V | 1A, slow blow, 250V, 3AG | FU-10             |

### 6.4 FIXED RACK INSTALLATION

Table 6-2 lists the necessary hardware for a fixed mounting of a Model 707 in a 19" wide rack (24 to 30" deep). Verify that all parts are available before beginning the installation procedure.

**Table 6-2. Fixed Rack Parts**

| Item | Description                         | Keithley Part No. | Qty. |
|------|-------------------------------------|-------------------|------|
| A    | Chassis Support (left)              | 707-321           | 1    |
| B    | Chassis Support (right)             | 707-322           | 1    |
| C    | Nut, #10-32 Captive                 | FA-148            | 4    |
|      | Bracket Kit consisting of:          | BR-31             | 1    |
| D    | Rear Support Bracket                | —                 | 2    |
| E    | Spacer Bar                          | —                 | 2    |
| F    | Nut Bar                             | —                 | 4    |
| G    | Screw, #10-32 × ½" Phil. Binder Hd. | —                 | 12   |

#### Rack Preparation

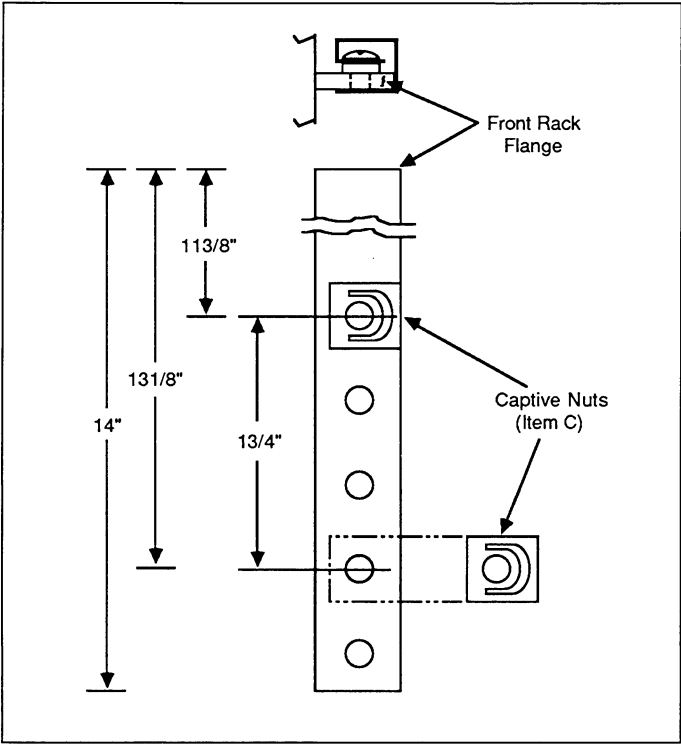
1. Select a position in the rack. In most cases, the weight of the Model 707 dictates a position in the lower half of the rack. The Model 707 will take up 14" of vertical space.

**NOTE**

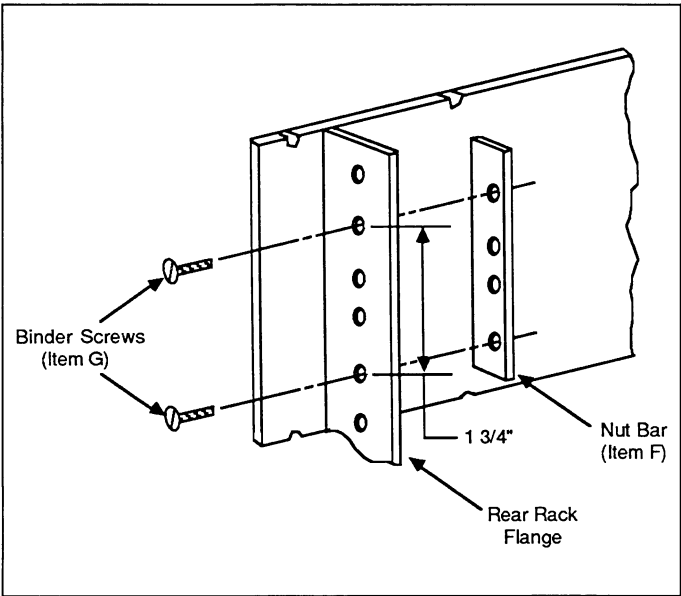
The mainframe must be mounted at a height that is an increment of 1-3/4" from the top or bottom of the rack. Attempting to mount the mainframe at a non-incremental height will lead to difficulties with hole alignment.

2. Referring to Figure 6-2, install two captive nuts (item C) on each front rack flange at holes 11-3/8" and 13-1/8" from the top of the selected space.
3. Loosely attach a nut bar (item F) to each rear rack flange with two binder head screws (item G). See Figure 6-3. Mount the nut bars with the outer holes at the same level as the captive nuts. Note that the hole pattern on the nut bar is not symmetrical.





**Figure 6-2. Captive Nut Installation**



**Figure 6-3. Nut Bar on Flange**

**Chassis Support Preparation**

4. Place a rear support bracket (item D) on the left chassis support (item A) and temporarily install the two pieces in the rack by sliding them apart until their flanged ends fit as shown in Figure 6-4. Note which holes will be used to attach the two pieces together.
5. Use a spacer bar, nut bar, and two binder head screws (items E, F, and G) to loosely attach each rear support bracket to a chassis support. Figure 6-5 shows the left side support.

**Chassis Support Mounting**

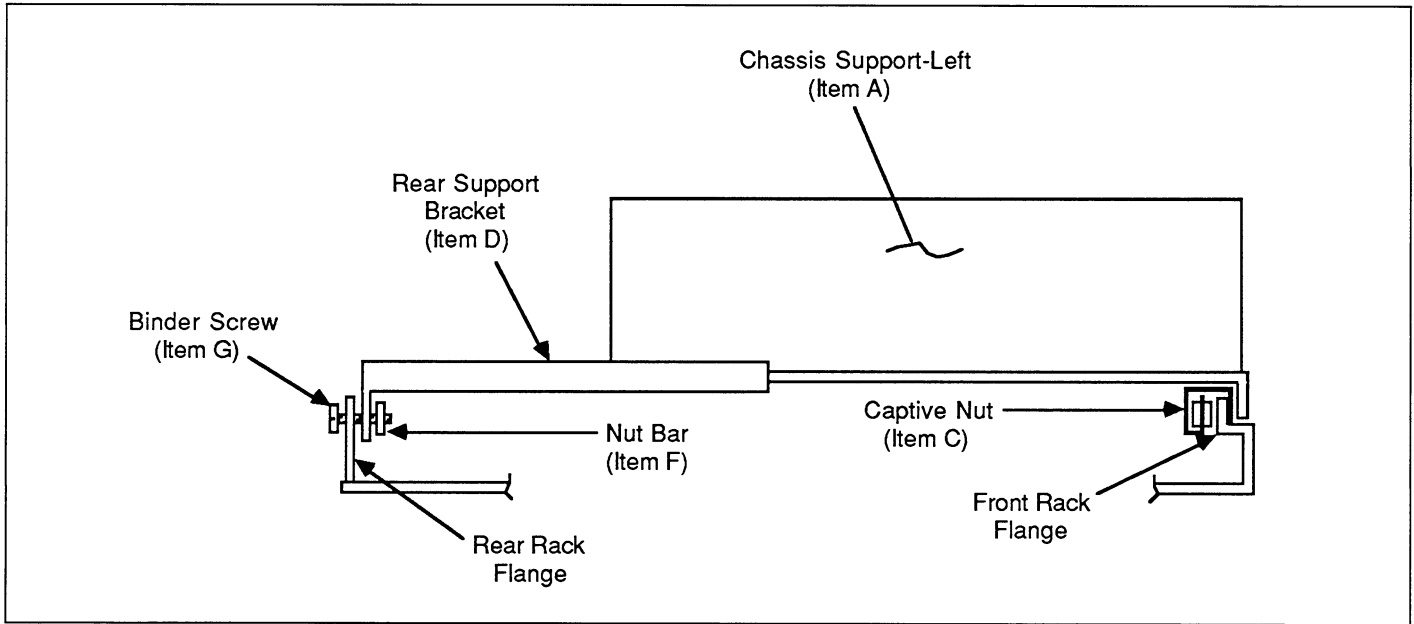
6. Reinstall the chassis support assemblies in the rack and secure them to the captive nuts with binder head screws. Also tighten the binder screws at the rear rack flange.
7. Tighten the screws attaching the two pieces of the chassis support assemblies.

**Mainframe Installation**

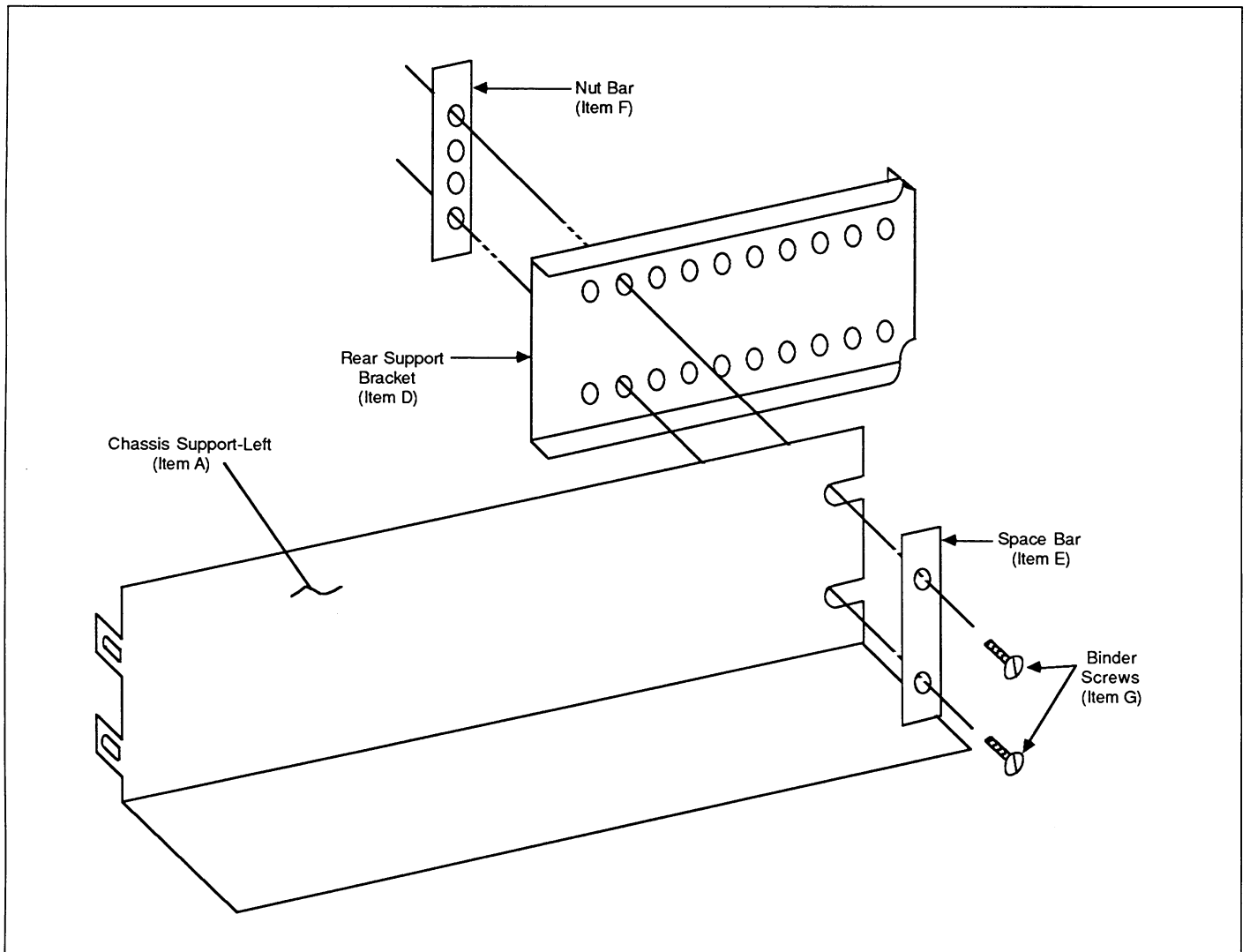
8. Lift the Model 707 mainframe onto the chassis supports and slide it into the rack.
9. The mainframe can be secured to the front rack flanges with user-supplied captive nuts and binder head screws (four each).

**NOTE**

The chassis supports hold the Model 707 in place while you are mounting the mainframe to the front rack flanges. Once the Model 707 is secured, there may be a gap between the mainframe and the chassis supports.



**Figure 6-4. Chassis Support Sizing**



**Figure 6-5. Chassis Support Assembly**

## 6.5 DISASSEMBLY

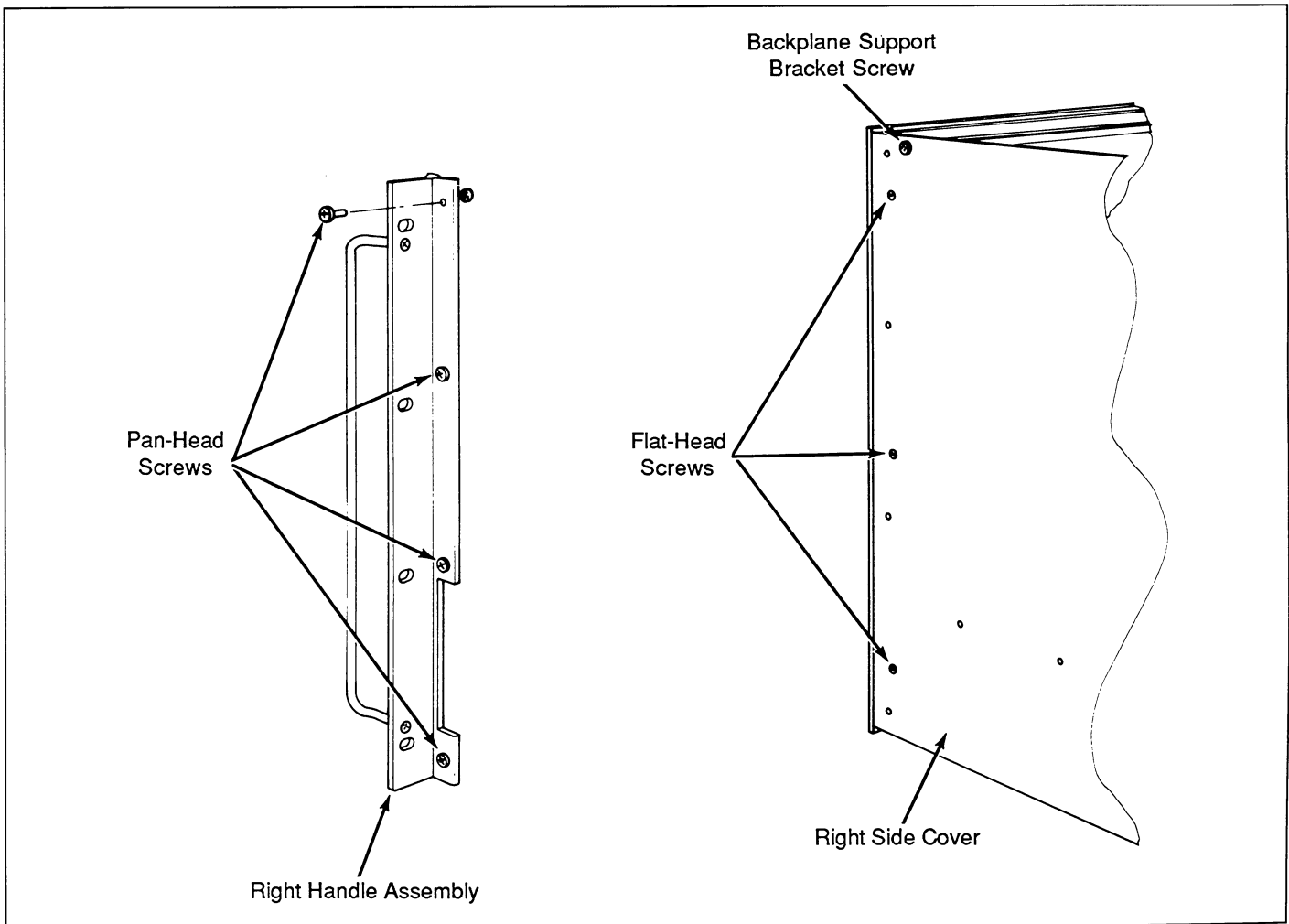
If it is necessary to troubleshoot the instrument or replace a component, use the following disassembly procedure.

### WARNING

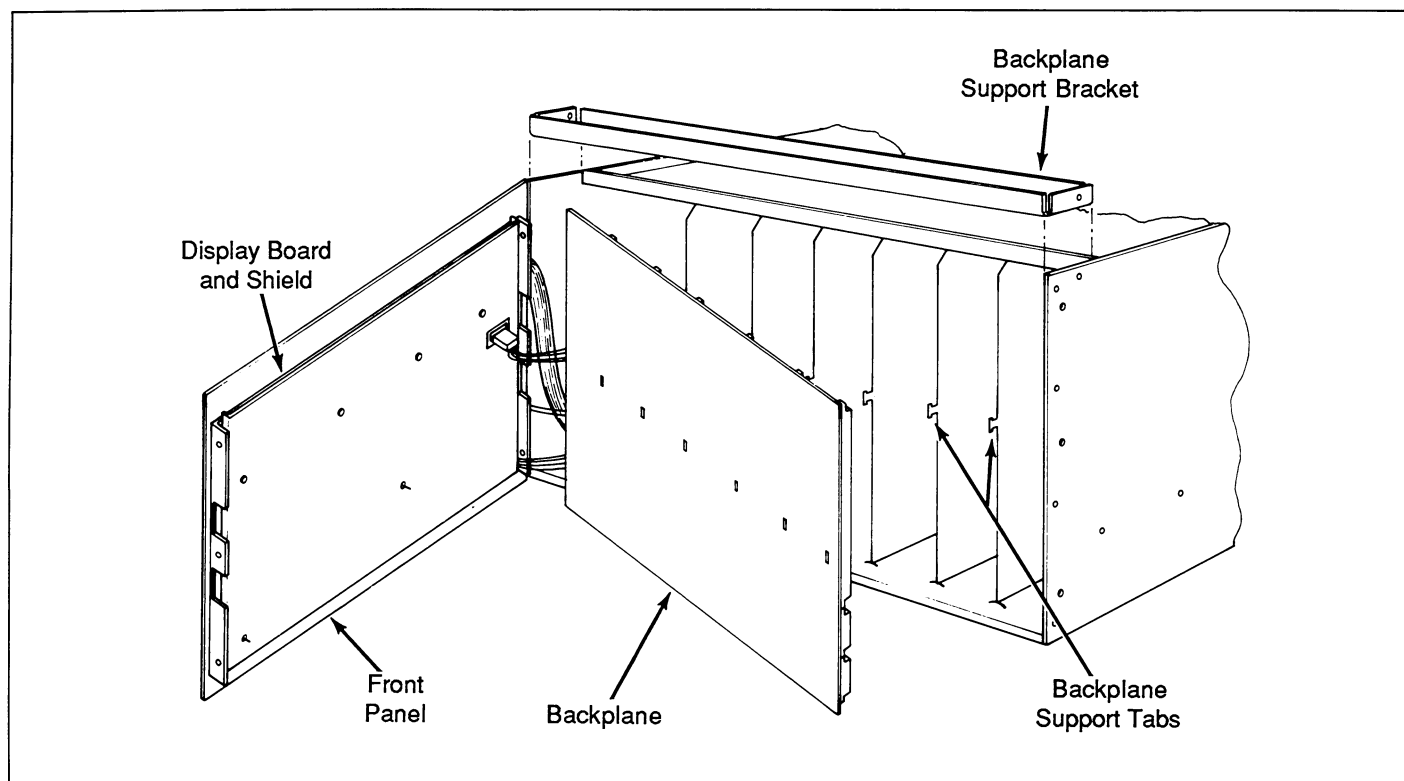
**Before disassembly, disconnect the line cord, ensure no voltage is applied from user circuits, and remove all plug-in cards from the instrument.**

1. With the Model 707 on a bench, remove the front panel as follows:

- A. Place a thin book or other support under the unit to slightly raise the bottom edge of the front panel off the bench surface.
- B. Remove the four pan-head screws that secure the right and left handle mounting brackets to the side covers. Figure 6-6 shows a view of the right side.
- C. Remove the three flat-head screws securing each side of the front panel to the right and left side covers.
- D. Gently pull the front panel away from the main-frame. It may be necessary to loosen the two pan-head screws holding the backplane support bracket.
- E. Swing the front panel to the left and rest it on its edge as shown in Figure 6-7.



**Figure 6-6. Right Side View of Disassembly**



**Figure 6-7. Front View of Disassembly**

2. As shown in Figure 6-8, remove the left side cover:
  - A. Remove the ten pan-head screws securing the left side cover, including the one previously loosened in step 1D.
  - B. Pull the side cover off its support tabs.
3. Remove the display board as follows:
  - A. Unplug the following connections:
    - P32—Digital board connection to light pen connector on front panel.
    - P34—Digital board ribbon cable connection to display board.
    - P36—Display board connection to voltage regulator board
  - B. Disconnect the plug between the power switch on the front panel and the terminal strip in front of the digital board.
  - C. Remove the four screws securing the display board shield and display board to the front panel.
4. Remove the backplane board as follows:

**CAUTION**

**Avoid touching the board surface. Contamination can result in degraded matrix card performance. If contamination occurs, clean the board as described in paragraph 6.10.**

- A. Remove the backplane support bracket shown in Figure 6-7.
  - B. Unplug the following connections:
    - P33—Digital board ribbon cable connection to backplane board.
    - P33—Backplane board connection to 6-volt power supply.
  - C. Remove the grounding wires from the grounding stud on the rear panel.
  - D. Lift the backplane board by its edges off the backplane support tabs.
5. Referring to Figure 6-8, remove the digital board as follows:

## MAINTENANCE

---

---

A. On the Model 707 rear panel, pull off the Relay Test connector (P24). Also unscrew the two black stand-offs from the IEEE-488 connector (J25).

B. On the digital board, disconnect the following plugs (if not previously removed):

P29—Connector to External Trigger and Matrix Ready jacks.

P30—Ribbon cable connector to Digital I/O port.

P31—Connector to 5-volt power supply.

P32—Connector to light pen connector on front panel.

P33—Ribbon cable connector to backplane board.

P34—Ribbon cable connector to display board.

C. Remove the five screws securing the digital board to the mainframe and lift out the board.

6. Reverse the above procedure to re-assemble the instrument.

## 6.6 BATTERY REPLACEMENT

When line power to the Model 707 is turned off, the lithium battery on the digital board provides backup power to the memory in which setups and parameters are stored. The battery has enough capacity to maintain data for one year of continuous power off, or two years if the Model 707 is powered on for 12 hours every day. The battery by itself has a shelf life of eight years. Although the Model 707 will operate without a battery, it will not retain any setups or parameters when turned off and will power up in random conditions.

The battery may be replaced with any CR2450 lithium coin cell. It can also be ordered from Keithley Instruments (part number BA-44), as discussed in Section 7 of this manual.

Replacement of the lithium battery is normally a safe procedure as long as these safety precautions are followed.

### WARNING

**The precautions below must be followed to avoid possible personal injury.**

1. Wear safety glasses or goggles when working with lithium batteries.
2. Do not short the battery terminals together.
3. Do not incinerate or otherwise expose to excessive heat ( $>60^{\circ}\text{C}$ ).
4. Keep lithium batteries away from all liquids.
5. Do not recharge lithium batteries.
6. Observe proper polarity when inserting battery into holder.

Replace the battery as follows:

1. Turn off the power and remove the line cord from the Model 707.
2. Remove the left handle and side panel. (See paragraph 6.5.)
3. The lithium battery is on the digital board, as previously shown in Figure 6-8. Pry the battery out of its holder using a non-metallic tool.

### WARNING

**Do not use a metal tool to pry out the battery as you could short its terminals.**

4. Install the new battery, taking care to observe proper polarity as stamped on the battery holder.
5. Reinstall the left side panel and handle.
6. Reinstall the power line cord and turn on the Model 707. It will power up in random conditions, such as make/break and break/make on the same rows.
7. Select the FACTORY INIT menu item to restore factory default conditions.

## 6.7 STATIC-SENSITIVE DEVICES

CMOS devices are designed to operate at high impedance levels for lower power consumption. As a result, any static charge that builds up on your person or clothing may be sufficient to destroy these devices if they are not handled properly. Use the precautions below when handling static-sensitive devices:

### NOTE

Since the many CMOS devices installed in the Model 707 are not denoted in this manual, all ICs and transistors should be handled as static-sensitive devices.

1. Transport such devices only in containers designed to prevent static build-up. Typically, these parts will be received in anti-static containers of plastic or foam. Always leave the devices in question in their original containers until ready for installation.
2. Remove the devices from their protective containers only at a properly grounded work station. Also ground yourself with a suitable wrist strap.
3. Handle the devices only by the body; do not touch the pins or terminals.
4. Any printed circuit board into which the device is to be inserted must also be properly grounded to the bench or table.
5. Use only anti-static type de-soldering tools.
6. Use only soldering irons with properly grounded tips.
7. Once the device is installed on the PC board, it is usually adequately protected, and normal handling can resume.

## 6.8 MAINFRAME TROUBLESHOOTING

This troubleshooting information is intended for qualified personnel having a basic understanding of digital and analog circuitry. The individual should also be experienced at using common test equipment, as well as ordinary

troubleshooting procedures. The information has been written to assist in isolating a defective circuit or circuit section. Isolation of the specific component is left to the technician.

Note that schematic diagrams and component layout drawings are located at the end of Section 7. Also, refer to Section 5 for an overview of operating principles.

### 6.8.1 Recommended Test Equipment

Success in troubleshooting equipment like the Model 707 depends not only on the skill of the technician, but also on the use of accurate, reliable test equipment. Table 6-3 lists the minimum recommended equipment for troubleshooting. Other equipment, such as logic analyzers, could also be helpful.

**Table 6-3. Recommended Troubleshooting Equipment**

| Description                                           | Application                   |
|-------------------------------------------------------|-------------------------------|
| DMM (Keithley 196)                                    | Measure dc voltage.           |
| Dual-trace, triggered sweep oscilloscope, dc to 50MHz | Check clock and logic pulses. |

### 6.8.2 Power-up Self-test

As described in Section 3, the Model 707 performs a series of tests on power-up. Individual tests in this series that can be used for troubleshooting a Model 707 include:

- ROM test—A checksum test of ROM. Test failure is indicated by a ROM FAIL error message.
- RAM test—A read/write test of battery backed-up CMOS RAM. Test failure is indicated by a RAM FAIL error message.
- Display test—A visual test of the front panel displays.

Check for all LEDs and indicators being lit.

These three tests can also be performed from the front panel through a menu selection (SELF TEST) or over the IEEE-488 bus with the command J0 (Perform Self-test).

### 6.8.3 Power Supply Checks

All power supply voltages should be checked first to make sure they are within the required limits. If the various operating voltages are not within the limits, troubleshooting the remaining circuitry can be quite difficult.

Table 6-4 outlines the voltages that should be checked. In addition to the usual checks with a voltmeter, it is a good idea to check the supplies with an oscilloscope to make sure that no noise or ringing is present.

### 6.8.4 Digital Board Checks

Tables 6-5 through 6-8 list the procedures to check circuitry on the digital board, including the microcomputer, relay control circuitry, display interface, and digital I/O port.

Procedures for testing the light pen and master/slave interface options are listed in Tables 6-9 and 6-10.

The waveforms described in the troubleshooting tables are present with power-up default conditions (idle state), unless otherwise indicated. To view some of the waveforms, it will be helpful to run a program loop. Refer to the programs of Figure 6-9 when directed by the procedure.

**NOTE**

If the ROM (U7) must be replaced for any reason, restore the Model 707 to factory default conditions with a menu selection (FACTORY INIT) or the device-dependent command R0.

**Table 6-4. Power Supply Checks**

| Step | Item/Component        | Signal                   | Comments                                                                        |
|------|-----------------------|--------------------------|---------------------------------------------------------------------------------|
| 1    | PS2                   | 5.15Vdc $\pm$ 1% (0.05V) | Measure and adjust logic voltage at power supply.                               |
| 2    | PS1                   | 6Vdc $\pm$ 2% (0.12V)    | Measure and adjust relay coil voltage at power supply.                          |
| 3    | Voltage Regulator PCB | 6Vdc $\pm$ 7% (0.42V)    | Measure front panel display voltage at voltage regulator board, not adjustable. |



**Table 6-5. Microcomputer Checks**

| Step | Item/Component | Signal                   | Comments                                                                                                                    |
|------|----------------|--------------------------|-----------------------------------------------------------------------------------------------------------------------------|
| 1    | PS2            | 5.15Vdc $\pm$ 1% (0.05V) | Measure and adjust at power supply.                                                                                         |
| 2    | U6 pin 37      | MPU Reset                | The following digital board signals are referenced to digital common.<br>Stays low 110-260msec on power-up, then goes high. |
| 3    | U6 pin 34      | E clock                  | 1.79MHz square wave for peripheral chips.                                                                                   |
| 4    | U9 pin 23      | SELECT VIA               | Low going pulses (1kHz).                                                                                                    |
| 5    | U6 pins 8-23   | Address bus (A0-A15)     | Check for stuck bit.                                                                                                        |
| 6    | U6 pins 24-31  | Data bus (D0-D7)         | Check for stuck bit.                                                                                                        |

**Table 6-6. Relay Control Checks**

| Step | Item/Component         | Signal           | Comments                                                                             |
|------|------------------------|------------------|--------------------------------------------------------------------------------------|
|      |                        |                  | The following digital board signals are referenced to digital common.                |
| 1    | U9 pin 23              | SELECT VIA       | See Figure 6-10 for waveforms of steps 8 through 10.<br>Low going pulses (1kHz).     |
| 2    | U28 pin 5              | CLRADDR          | High logic pulse at start of each matrix card ID byte transfer sequence on power-up. |
| 3    | U28 pin 3              | NEXTADDR         | Low logic pulse before each byte transfer from matrix card on power-up.              |
| 4    | U28 pin 6              | IDDATA           | Matrix card ID logic pulse train on power-up.                                        |
| 5    | U12 pin 11             | SELECT CARSEL    | Pair of low going pulse trains when program #1 is running.                           |
| 6    | U12 pin 16             | STROBE           | High logic pulse to strobe relay drivers on matrix card.                             |
| 7    | U12 pins 2,5,6,9,12,15 | CARD(1-6)        | Low logic pulse selects matrix card for data transfer sequence.                      |
| 8    | U24 pin 1              | SELECT RELAYDATA | Low going pulse when program #1 is running.                                          |
| 9    | U28 pin 7              | CLK              | Eight low-going pulses (1.79MHz square wave) after each SELECT RELAYDATA pulse.      |
| 10   | U28 pin 18             | RELAYDATA        | Logic pulses to load relay drivers on matrix card.                                   |

**Table 6-7. Display Interface Checks**

| Step | Item/Component | Signal     | Comments                                                                        |
|------|----------------|------------|---------------------------------------------------------------------------------|
|      |                |            | The following digital board signals are referenced to digital common.           |
| 1    | U9 pin 23      | SELECT VIA | See Figure 6-11 for waveforms of steps 2 through 6.<br>Low going pulses (1kHz). |
| 2    | U9 pin 18      | FPCLK      | Base frequency of 895kHz with ten sets of pulses every 1msec.                   |
| 3    | U18 pin 6      | DISPCLK    | Base frequency of 895kHz with nine sets of pulses every 1msec.                  |
| 4    | U18 pin 3      | KEYCLK     | Base frequency of 895kHz with one set of pulses every 1msec.                    |
| 5    | U23 pin 6      | DISPDATA   | See Figure 6-11.                                                                |
| 6    | U23 pin 2      | KEYDATA    | See Figure 6-11.                                                                |

**Table 6-8. Digital I/O Checks**

| Step | Item/Component               | Signal           | Comments                                                              |
|------|------------------------------|------------------|-----------------------------------------------------------------------|
|      |                              |                  | The following digital board signals are referenced to digital common. |
| 1    | U10 pin 23                   | SELECT PIA       | Low going pulses (1kHz).                                              |
| 2    | U10 pins 10-17               | OUT(0-7)         | Logic low when programmed low.                                        |
| 3    | U10 pin 19                   | OUTPULSE         | Low going pulses (600nsec) when program #2 is running.                |
| 4    | U13 pin 1                    | SELECT DIG INPUT | Low going pulses (5kHz) when digital input is displayed.              |
| 5    | U13 pins 3,4,7,8,13,14,17,18 | IN(0-7)          | Logic high when port disconnected.                                    |

**Table 6-9. Light Pen Checks**

| Step | Item/Component | Signal     | Comments                                                                                          |
|------|----------------|------------|---------------------------------------------------------------------------------------------------|
| 1    | U9 pin 23      | SELECT VIA | The following digital board signals are referenced to digital common.<br>Low going pulses (1kHz). |
| 2    | U23 pin 12     | SENSEPULSE | Low going pulse (15 $\mu$ sec) when light pen is pointed at lit LED.                              |
| 3    | U23 pin 9      | LPSWITCH   | Low logic level when light pen button is pressed.                                                 |
| 4    | U9 pin 17      | LPSENSE    | High going pulse when light pen button is pressed and pen is pointed at an LED.                   |
| 5    | U9 pin 39      | LPRESET    | Multiple high going pulses when light pen button is pressed.                                      |

**Table 6-10. Master/Slave Checks**

| Step | Item/Component | Signal         | Comments                                                                                                                                                                                                                                     |
|------|----------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1    | U11 pins 3,4   | RXCLK, TXCLK   | The following digital board signals are referenced to digital common.<br><br>When troubleshooting a single unit, loop a cable from Master/Slave Out to Master/Slave In of the same unit, then program unit as master.<br>895kHz square wave. |
| 2    | U11 pin 9      | SELECT ACIA    | Low going pulses with manual triggers.                                                                                                                                                                                                       |
| 3    | U11 pins 2,6   | RXDATA, TXDATA | Low going pulse pair with manual trigger.                                                                                                                                                                                                    |
| 4    | U11 pin 7      | FIRQ           | Low going pulse with manual trigger.                                                                                                                                                                                                         |
| 5    | U10 pin 23     | SELECT PIA     | Low going pulses (1kHz).                                                                                                                                                                                                                     |
| 6    | U10 pin 39     | M/S TRIGGER    | High going pulses when program #3 is running. (Triggering stops when Relay Step equals 100.)                                                                                                                                                 |

| Program #1                                                                                                                                                                      | Program #3                                                                                                                                                                            |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <pre> 10  REMOTE 718 20  CLEAR 718 30  OUTPUT 718;''CA1X'' ! Close crosspoint A1 40  OUTPUT 718;''NA1X'' ! Open crosspoint A1 50  GOTO 30 60  END </pre>                        | <pre> 10  REMOTE 718 20  CLEAR 718 30  OUTPUT 718;          ! Enable triggers on GET     ''T2XF1X'' 40  TRIGGER 718 50  GOTO 40              ! Triggering stops at 100 60  END </pre> |
| Program #2                                                                                                                                                                      |                                                                                                                                                                                       |
| <pre> 10  REMOTE 718 20  CLEAR 718 30  OUTPUT 718;''00X'' ! Set digital output to                         zero 40  WAIT .1            ! Delay 0.1sec 50  GOTO 30 60  END </pre> |                                                                                                                                                                                       |

**Figure 6-9. Troubleshooting Programs**

### 6.8.5 Display Board Checks

To troubleshoot the display board, it is helpful to disassemble it from the front panel, then reconnect the power cable and ribbon cable with the display board on a bench.

A troubleshooting procedure is outlined in Table 6-11.

### 6.9 RELAY TEST SOFTWARE

The Relay Test software verifies the operation of relay cards designed for use in the Model 707. It is a functional test of the cards, not a performance test. The Relay Test program should be run to:

- Check relay operation of a new system configuration.

- Verify relay operation as part of daily, weekly, or monthly preventive maintenance.
- Isolate a suspected bad relay.

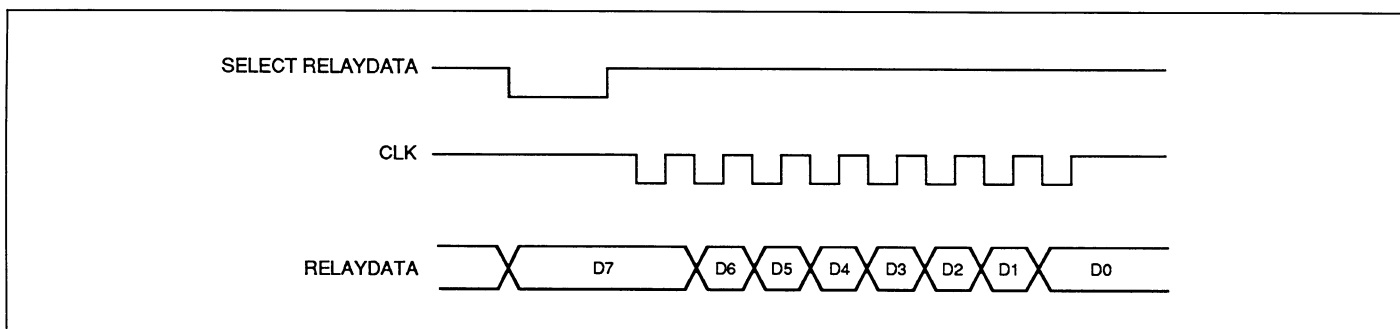
The program checks for open and closed crosspoint relays through connections made between card rows and the rear panel Relay Test connector of a stand-alone or master unit. The cards are tested one-by-one, mainframe-by-mainframe.

The software is supplied in two media formats:

- 5¼" diskette (Keithley P/N 707-851)—For an IBM PC or compatible with a Capital Equipment Corporation IEEE-488 interface. (PC-488-C&E is available from Keithley Instruments.)
- 3½" diskette (Keithley P/N 707-852)—For an HP Series 200 or 300 computer running HP BASIC 4.0.

**Table 6-11. Display Board Checks**

| Step | Item/Component                            | Signal         | Comments                                                                                                                                                                   |
|------|-------------------------------------------|----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|      |                                           |                | The following display board signals are referenced to digital common.                                                                                                      |
| 1    | U13 pin 12                                | Source driver  | When troubleshooting the LED array, check for these signals when the LED is lit (e.g., crosspoint A1).<br>Card row A - Logic high for 9msec.<br>Card column 1 - Logic low. |
| 2    | U3 pin 17                                 | Sink driver    |                                                                                                                                                                            |
| 3    | U11 pins 6-12;<br>U12 pins 5-12           | Source drivers | Segment displays:<br>Low going pulses (1kHz) with all segments lit.                                                                                                        |
| 4    | U1 pins 4-7, 14-16;<br>U2 pins 4-7, 14-17 | Sink drivers   | Low going pulses (1kHz).                                                                                                                                                   |
| 5    | U27 pins 1-6                              | Button columns | Front panel buttons:<br>Logic high with a low going 1msec pulse.                                                                                                           |
| 6    | U28 pins 3-5, 11-14                       | Button rows    | Logic high with a low going 1msec pulse when button pressed.                                                                                                               |



**Figure 6-10. Relay Control Waveforms**

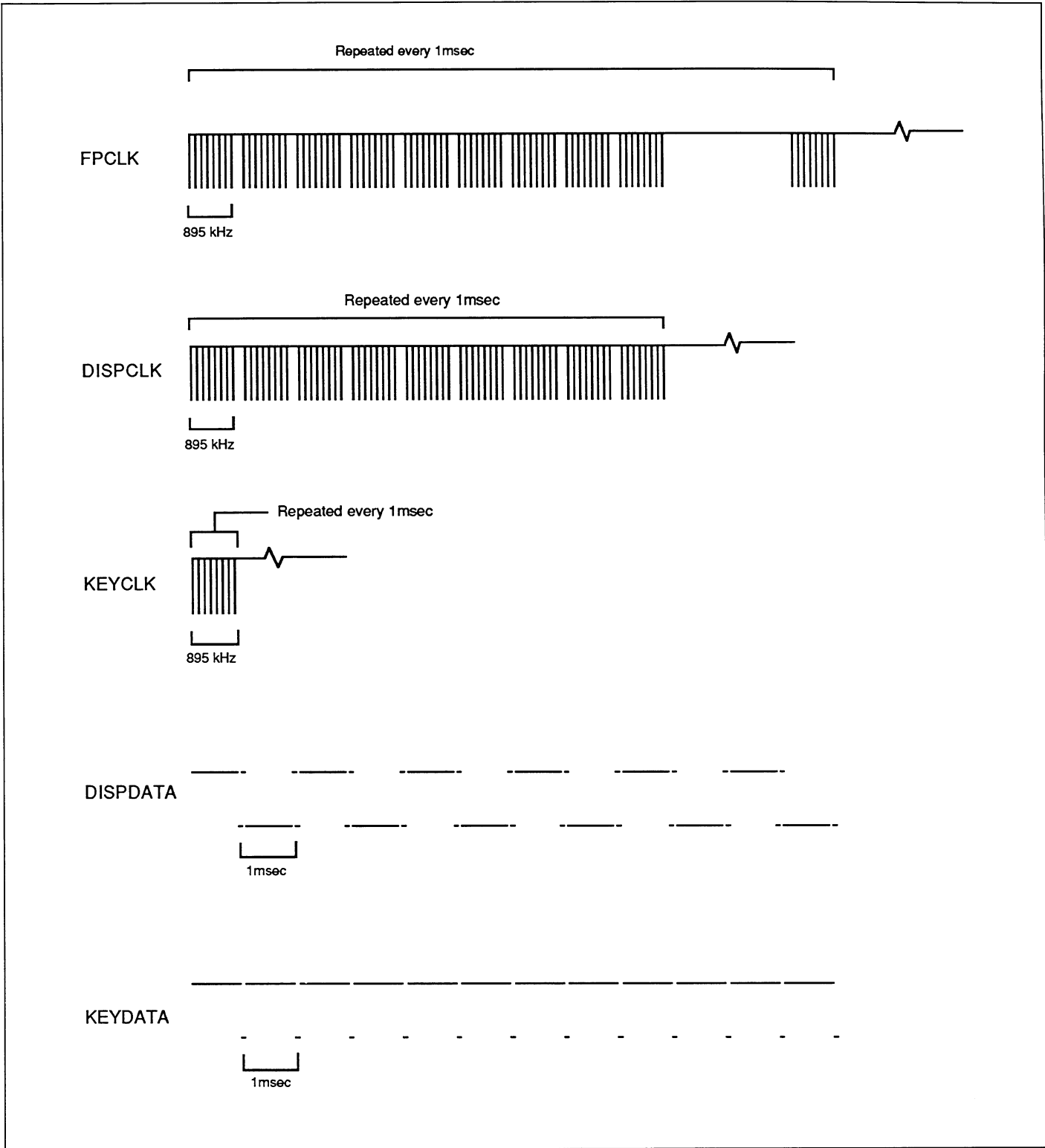


Figure 6-11. Display Interface Waveforms

### 6.9.1 Running the Program

Program operation is shown in the flowchart of Figure 6-12 and outlined below.

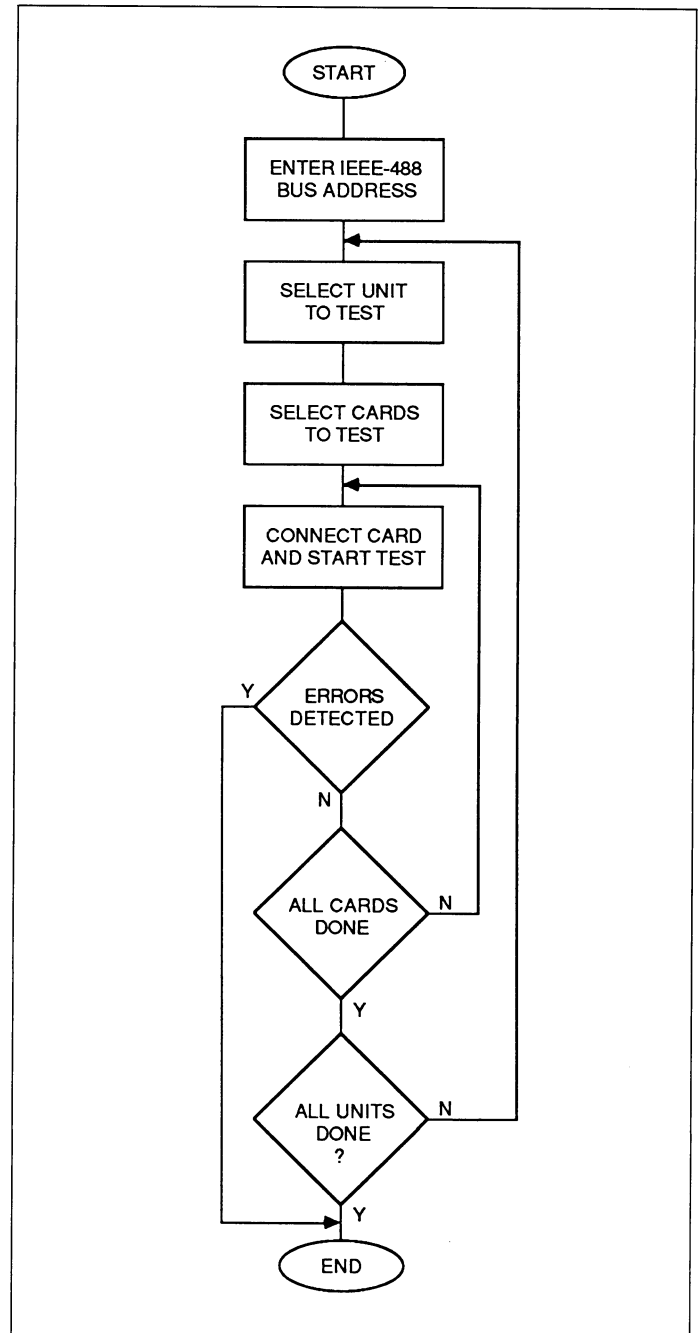
1. Start the Relay Test program on HP controllers by loading and running RELAYTEST from the 3½" disk. For an IBM PC or compatible with a CEC card, the 5¼" disk contains two files, one an executable file and the other a source file (written in Microsoft QuickBASIC). Type in RELAYTST to the appropriate disk drive prompt to start the program.
2. The program begins by displaying a header screen listing its revision and supported relay cards. Press the Continue (HP) or Enter (PC) key on the controller to continue.
3. You will be prompted to type in the IEEE-488 bus address of the Model 707. Be sure all units are powered up at this time.
4. The program checks for the number of units (1-5) and asks which mainframes you want to test.
5. The card configuration of the mainframe you selected is displayed. Choose which cards you want tested with a "Y/N" response. Although the cards are tested one-by-one, it might be advantageous to test like card models one after another so the wiring at the Relay Test connector does not change.
6. The next screen describes wiring between rows A and B of the card to be tested and the Relay Test connector of the stand-alone or master unit. These connections are shown in Section 4 of the appropriate card manual.

It is necessary to remove all user connections between the card and test system, not just the row A and B connections. Do not remove master/slave loop cables.

7. The "707 Relay Test in Progress" message is displayed and the program checks for improper relay operation on the card. The front panel crosspoint display shows which relays are closed as the test steps through the crosspoints.
8. If the card passes, the program returns to a screen describing wiring for the next card to be tested (step 6). If a relay does not open or close on command, the program exits and displays an error message, such as:

Found relay closed at row H, column 36

9. When all cards pass the test in a stand-alone system, the message "Test done. No errors detected" is displayed and the program exits. For a master/slave system, the program pauses and asks if you want to test the next mainframe. Steps 5 through 9 are repeated for the remaining units.



**Figure 6-12. Operation of Relay Test Program**

## 6.9.2 Analyzing Errors

When the Relay Test program detects a defective relay, it displays an error message and immediately exits. The message will be as specific as possible given the card configuration; it could identify one, two, or four suspected relays.

The error message "Found relay open at row A or row B, column 1" is an indication that the wiring between the card in slot 1 and the Relay Test connector might be incorrect. (The message would read column 13 when testing the card in slot 2, column 25 for slot 3, etc.)

If there is a short between the poles of a 2-pole relay (where one pole is tied to common), the Relay Test is able to detect the defective row only. This contact configuration is present on the Model 7073 and rows G and H of the Model 7072.

Row and column isolator relays, such as on the Model 7072, are more difficult to test. Isolators that remain closed are completely undetectable. If detected at all, isolators that remain open generate a crosspoint error message.

## 6.9.3 Using an Extender Card

To access circuitry on the plug-in relay cards of the Model 707, use a Model 7070 Universal Adapter Card. The Model 7070 must be configured as an extender card by placing the configuration jumper in the EXTEND position. See the Model 7070 Instruction Manual for complete details on using the card.

## 6.10 CLEANING

### 6.10.1 Backplane

Since matrix card rows are extended on the three analog backplanes of the Model 707 (as explained in Section 2), a contaminated backplane will degrade card isolation specifications. If an isolation problem exists, the backplane should be cleaned only after the isolation of each card has been tested according to the respective card manuals.

The following procedure is primarily intended to clean high impedance PC boards, such as the Model 707 backplane, but it can be used to clean all PC boards. To remove the backplane, refer to paragraph 6.5.

1. Handle the board by the edges only. Do not touch any board surfaces. When servicing, wear clean, white cotton gloves.
2. If making solder repairs on the board, use a flux that is rosin RMA based. Remove the flux from these areas when the repair is complete. Use Freon® TMS or TE, or the equivalent along with plenty of clean cotton swabs to remove the flux. Take care not to spread the flux to other areas of the board.
3. Once the flux has been removed, swab only the repaired area with methanol, then blow dry the board with dry nitrogen gas.
4. After cleaning, the board should be placed in a 50°C low humidity environment for several hours.

### 6.10.2 Fan Filter

The fan filter, which is located on the rear panel, keeps dirt from being drawn into the instrument by the internal cooling fan. The filter opening should be kept free of obstructions to ensure proper instrument cooling.

The filter should be checked periodically for dirt build-up, and cleaned or replaced, as necessary. Use the following procedure to clean or replace the filter.

1. Disconnect the line cord from the power line receptacle.
2. Grasp the filter holder, and pull it free of the rear panel.
3. Remove the filter element from the holder.
4. Soak the filter in a solution of warm water and mild detergent until clean. Rinse thoroughly in clean water, and allow the filter to dry completely before installation. If a new filter assembly is required, one may be obtained by ordering Keithley part number FL-6.

#### NOTE

Do not operate the instrument with the filter removed to avoid dirt build-up within the instrument.

5. If necessary, clean the fan guard with a damp cloth.
6. Install the filter element in the holder and snap the holder back onto the fan guard. The two tabs on the holder should be oriented at the right and left sides.



# SECTION 7

## Replaceable Parts

---

---

### 7.1 INTRODUCTION

This section contains replacement parts information, component location drawings, and schematic diagrams for the Model 707 Switching Matrix.

### 7.2 PARTS LIST

Electrical parts for the Model 707 circuit boards are integrated with the component layout drawings and schematic diagrams for the respective circuit boards. Parts are listed alphabetically in order of circuit designation.

A miscellaneous parts list is located at the end of this section.

### 7.3 ORDERING INFORMATION

Keithley Instruments maintains a complete inventory of all normal replacement parts. To place an order, or to obtain information concerning replacement parts, contact your Keithley representative or the factory. See the inside front cover of this manual for addresses.

When ordering parts, include the following:

1. Instrument model number
2. Instrument serial number
3. Part description

4. Circuit designation, if applicable
5. Keithley part number

### 7.4 FACTORY SERVICE

If the mainframe is to be returned to the factory for service, carefully pack it and include the following information:

1. Complete the service form located at the back of this manual and include it with the mainframe.
2. Advise as to the warranty status of the instrument (see the inside front cover of this manual for warranty information).
3. Write ATTENTION REPAIR DEPARTMENT on the shipping label. Note that it is not necessary to return matrix cards with the mainframe.

### 7.5 COMPONENT LOCATION DRAWINGS AND SCHEMATIC DIAGRAMS

Component location drawings and schematic diagrams for the various circuit boards can be found on the following pages arranged as follows:

| <b>Board</b>      | <b>Component Layout</b> | <b>Schematic Diagram</b> |
|-------------------|-------------------------|--------------------------|
| Digital           | 707-100                 | 707-106                  |
| Display           | 707-110                 | 707-116                  |
| Backplane         | 707-120                 | 707-126                  |
| Voltage Regulator | 707-160                 | 707-166                  |

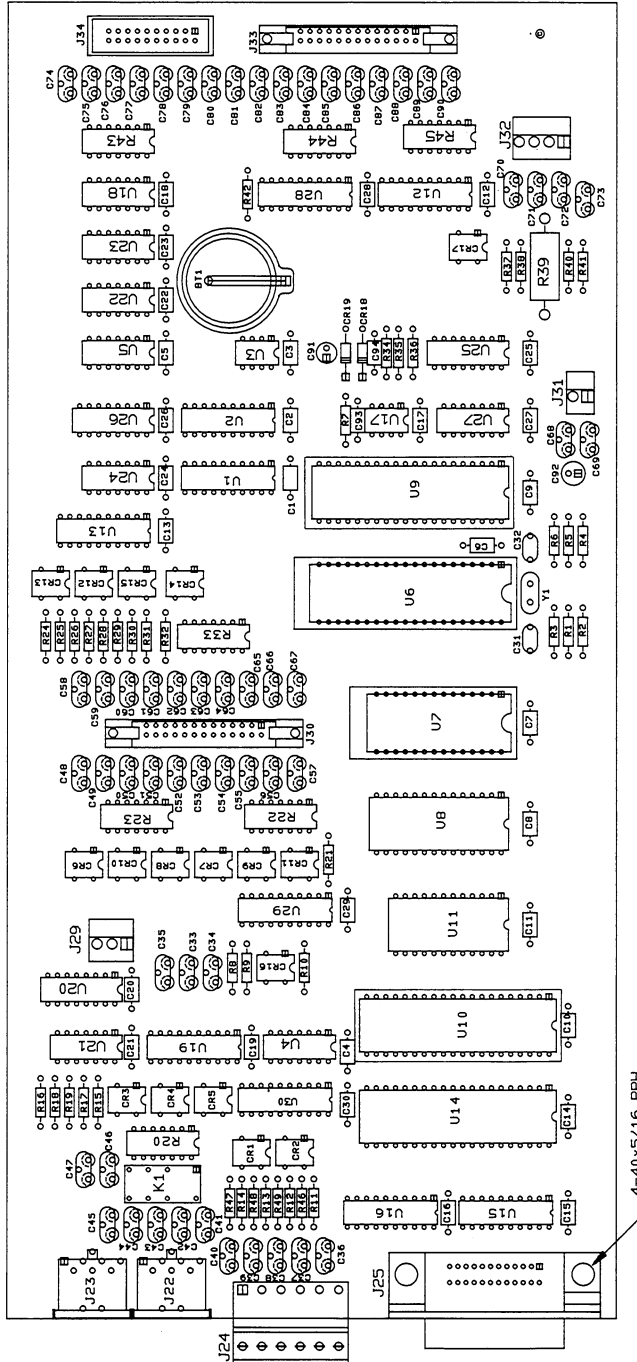
## MODEL 707 DIGITAL BOARD PARTS LIST

| CIRCUIT<br>DESIG. | DESCRIPTION                           | KEITHLEY<br>PART No. |
|-------------------|---------------------------------------|----------------------|
| BT1               | BATTERY                               | BA-44                |
| C1..C30,C93, C94  | CAP,.01uF,20%,50V,CERAMIC             | C-365-.01            |
| C31,C32           | CAP,15pF,10%,1000V,CERAMIC            | C-64-15P             |
| C33..C90          | CAP,270pF,20%,100V,CERAMIC/FERRITE    | C-386-270P           |
| C91               | CAP,3.3UF,10%,16V,ALUM ELEC           | C-321-3.3            |
| C92               | CAP,10uF,-20+100%,25V,ALUM ELEC       | C-314-10             |
| CR18,CR19         | DIODE,SILICON,1N4148 (DO-35)          | RF-28                |
| CR1..CR17         | DIODE,BRIDGE,VM18                     | RF-52                |
| J22,J23           | CONN,8 PIN CIRCULAR DIN               | CS-589               |
| J24               | CONN,6 PIN OPEN END HEADER            | CS-587-6             |
| J25               | CONN,RIGHT ANGLE,24 PIN               | CS-507               |
| J29               | CONN,MALE,3 PIN                       | CS-288-3             |
| J30,J33           | CONN,MALE HEADER 26-PIN               | CS-322-26            |
| J31               | CONN,MALE,2 PIN                       | CS-288-2             |
| J32               | CONN,MALE,4 PIN                       | CS-288-4             |
| J34               | CONN,HEADER STRAIGHT SOLDER PIN       | CS-368-20            |
| K1                | RELAY,MINIATURE (DPDT)                | RL-98                |
| P24               | CONN,6 PIN                            | CS-588-6             |
| R10,R15..R19,R21  | RES,4.7K,5%,1/4W,COMPOSITION OR FILM  | R-76-4.7K            |
| R1..R7,R42        | RES,3.3K,5%,1/4W,COMPOSITION OR FILM  | R-76-3.3K            |
| R20,R22,R23,R33   | RES NET,100,2%,.25W                   | TF-102-3             |
| R24..R31,R38,R40  |                                       |                      |
| R32,R37,R41       |                                       |                      |
| R34               | RES,20K,1%,1/8W,METAL FILM            | R-88-20K             |
| R35               | RES,63.4K,1%,1/8W,METAL FILM          | R-88-63.4K           |
| R36               | RES,,1M,5%,1/4W,COMPOSITION OR FILM   | R-76-1M              |
| R39               | RES,10.10%,2W,COMPOSITION             | R-3-10               |
| R43..R45          | RES NET,47,5%,.14W                    | TF-64                |
| R46..R49          |                                       |                      |
| R8,R9,R11..R14    | RES,100,5%,1/4W,COMPOSITION OR FILM   | R-76-100             |
| U1                | IC,OCTAL 10 INPUT GATE ARRAY,PAL16P8A | IC-573               |
| U10               | IC,PERIPHERAL INTERFACE ADAPTER 65C21 | LSI-61               |
| U11               | IC,ACIA,68B50                         | LSI-72               |
| U12               | IC,OCTAL FLIP FLOP,74HCT374           | IC-397               |
| U13               | IC,OCTAL LATCH,74HCT373               | IC-549               |
| U14               | IC,GPIB ADAPTER,9914A                 | LSI-49               |
| U15               | OCTAL INTERFACE BUS TRANSCEIVER       | IC-298               |
| U16               | OCTAL INTERFACE BUS TRANSCEIVER       | IC-299               |

|              |                                       |           |
|--------------|---------------------------------------|-----------|
| U17          | IC,MICROPOWER BIPOLAR MONOLITHIC,6728 | IC-177    |
| U18          | IC,QUAD 2 INPUT OR,74HCT32            | IC-443    |
| U19,U28..U30 | IC,BUF/LINE DRIVER 74HCT244           | IC-483    |
| U2           | IC,OCTAL 10 INPUT GATE ARRAY,PAL16P8A | IC-574    |
| U20          | IC,QUAD 2-INPUT MULTIPLEXER 74HCT157  | IC-553    |
| U21          | IC,QUAD 2 INPUT POSITIVE NAND 7438    | IC-154    |
| U22          | IC,QUAD 2 INPUT NAND,74HCT00          | IC-399    |
| U23          | IC,BUF/LINE DRIVER 74HCT125           | IC-552    |
| U25,U26      | IC,8 BIT PARALLEL TO SERIAL,74HCT165  | IC-548    |
| U27          | IC,8 BIT PAR OUT SHIFT REG,74HCT164   | IC-456    |
| U3           | IC,NONVOLATILE CONTROLLER DS1210      | IC-541    |
| U4,U24       | IC,DUAL D FLIP FLOP, 74HCT74          | IC-515    |
| U5           | IC,QUAD 2 INPUT AND GATE 74HCT08      | IC-550    |
| U6           | 8-BIT MICROPROCESSING UNIT,68B09      | LSI-65    |
| U7           | PROGRAM                               | 707-801-* |
| U8           | IC,32Kx8 STATIC CMOS RAM D43256C-15L  | LSI-76    |
| U9           | IC,VIA,6522A                          | LSI-45    |
| Y1           | CRYSTAL,7.15909MHZ                    | CR-24-4   |

\*Order same software as presently installed. For example, if the software revision level is A01, then order 707-801-A01.

| LTR. | ECO NO. | REVISION                     | ENG. | DATE     |
|------|---------|------------------------------|------|----------|
| A    | 12185   | RELEASED                     |      | 6-12-87  |
| B    | 12434   | REVISED                      | SZ   | 10/15/87 |
| C    | 12598   | REVISED                      | SZ   | 1-28-88  |
| D    | 12680   | REVISED BT1 & ADDED R50      | SZ   | 7-7-88   |
| E    | 13022   | REVISED                      | SZ   | 11-4-88  |
| F    | 14567   | ARTWORK WAS REV E. ADDED C94 | SZ   | 6-3-91   |



Digital Board, Component Location Drawing, Dwg. No. 707-100